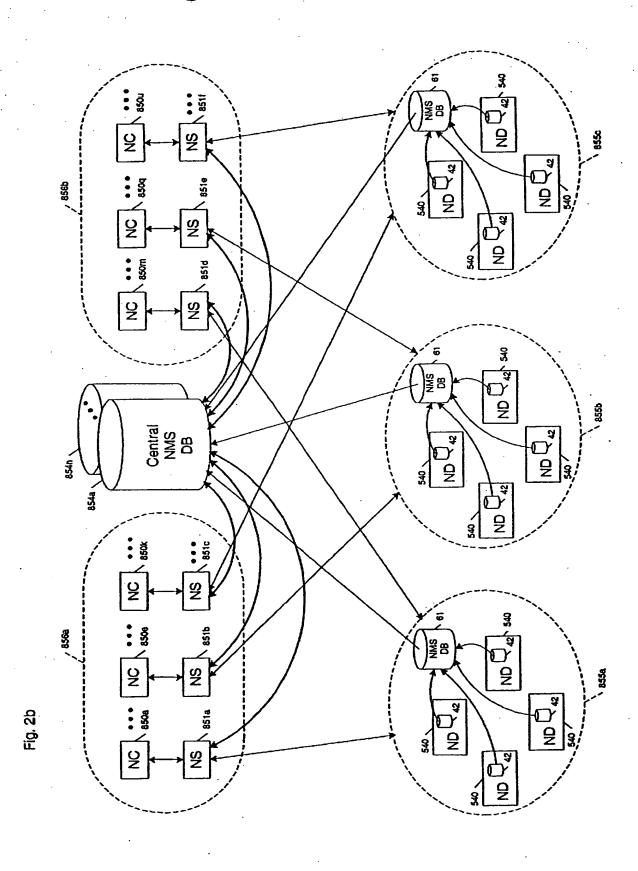


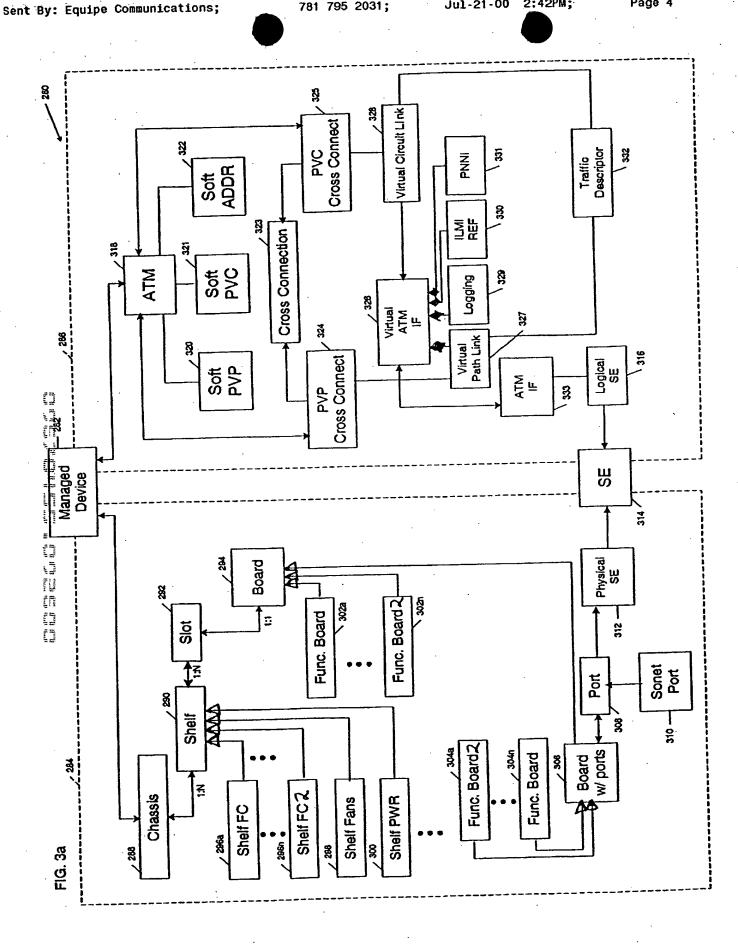
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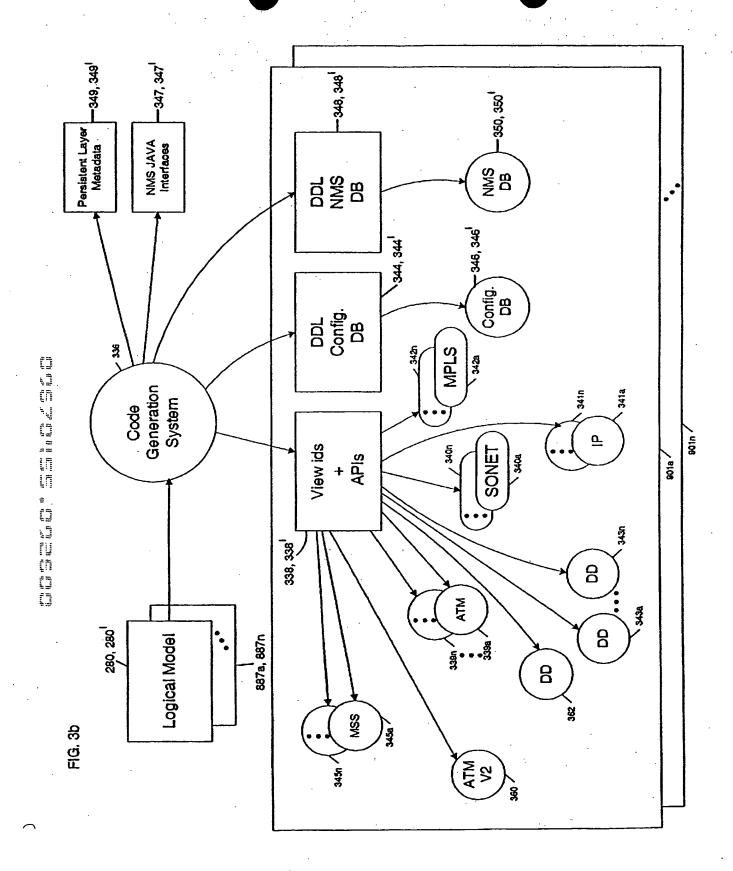
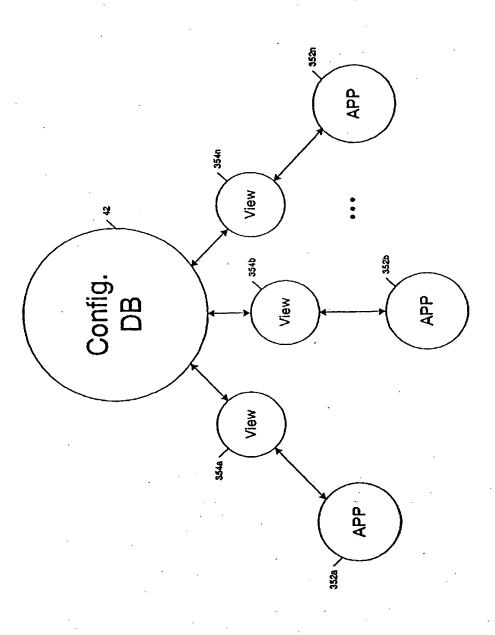


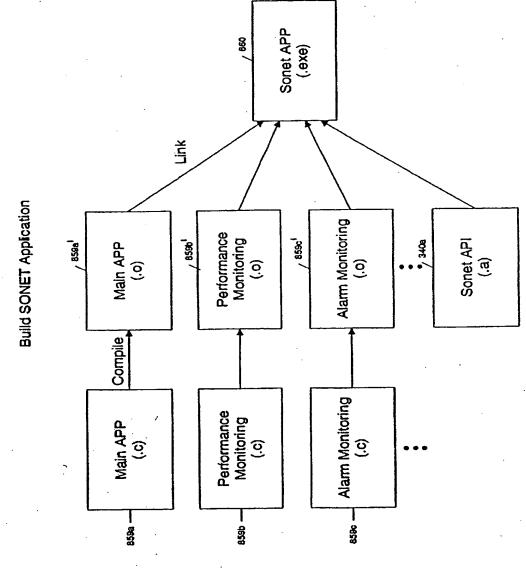


FIG. 30



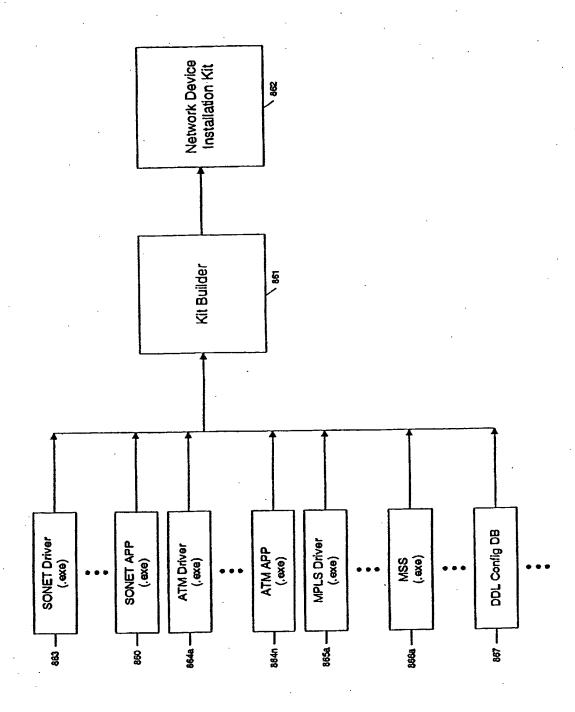


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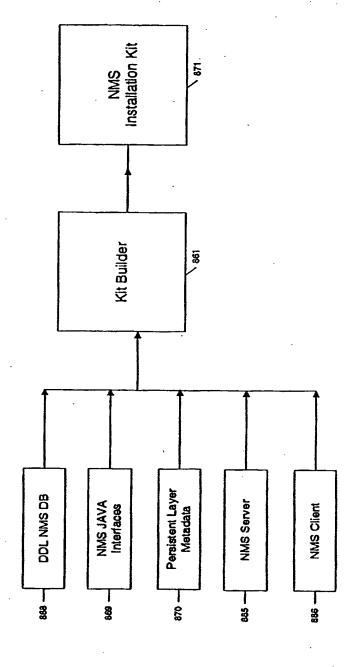
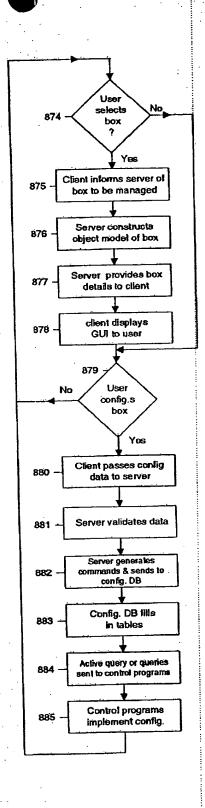


Fig. 35

Fig. 3g

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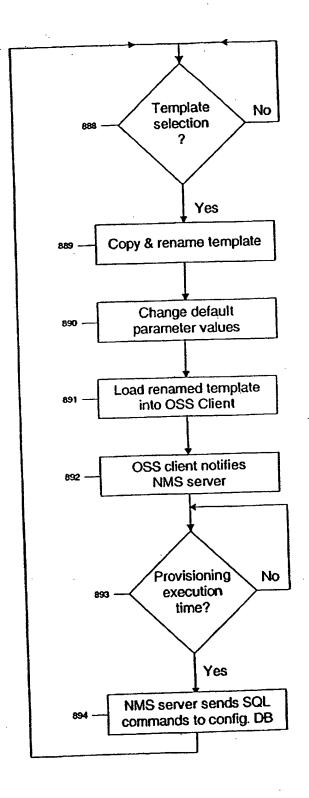


Fig. 3h

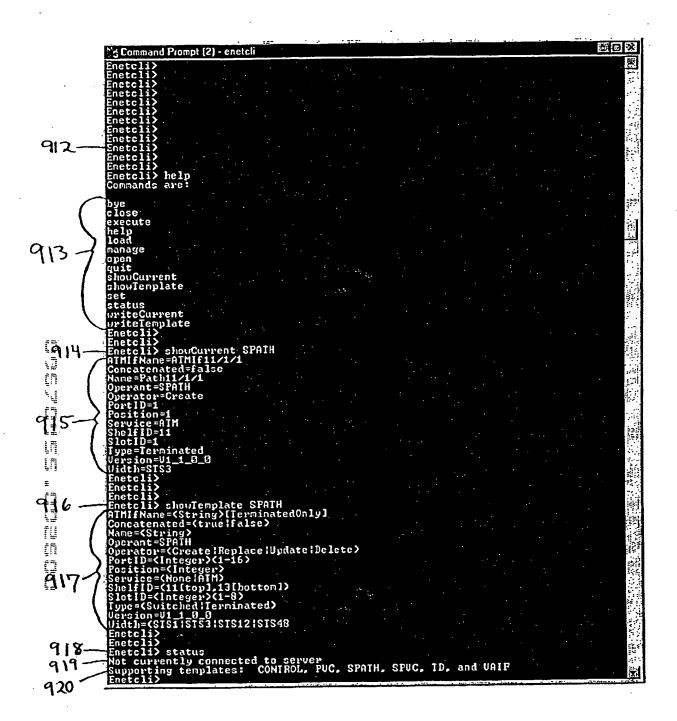
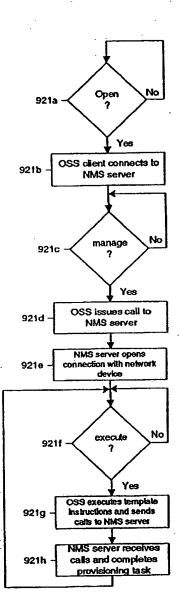


Fig.3i

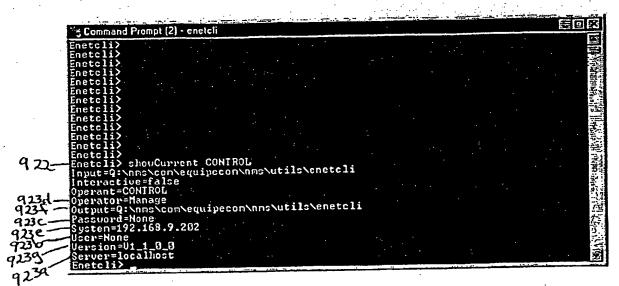
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Fig. 31

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1924 BATCH.

Operant=BATCH
Operator=Execute
Version=V1\_1\_0\_0

1244 — Task1=execute-SPATH
1245 — Task2=execute-PVC
1246 — Task3=execute-SPVC
1246 — Task4=load-SPVC-spvc1
1246 — Task6=load-SPVC-spvc2
1246 — Task6=load-SPVC-spvc2

9249 — Task50=sct-SPATH-PortID-3
9244 — Task51=execute-SPATH
924i — Task52=set-SPATH-SlotID-2
Task53=execute-SPATH

Task53=execute-SPATH

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Operant=BATCH Operator=Execute - Version=V1\_1\_0\_0 925a—Task1=execute-CONTROL 9255 -Task2=execute-SPATH 925C Task3=set-SPATH-PortID-3 925d Task4=execute-SPATH

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125e \_\_\_\_Task61=set-CONTROL-System-192.168.9.201 9255 -- Task62=execute-CONTROL Task63=execute-SPATH

925h-Task108=close Task109=sct-CONTROL-Server-Scrver1 Task111-examts CONTROL \_Task111=execute-CONTROL 925K -Task112=execute-SPATH



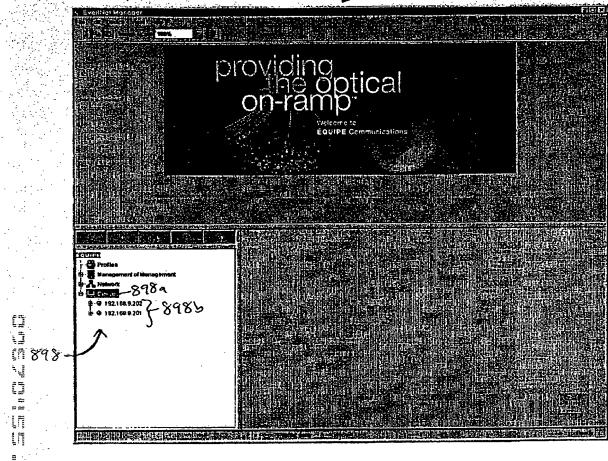


Fig. 4a

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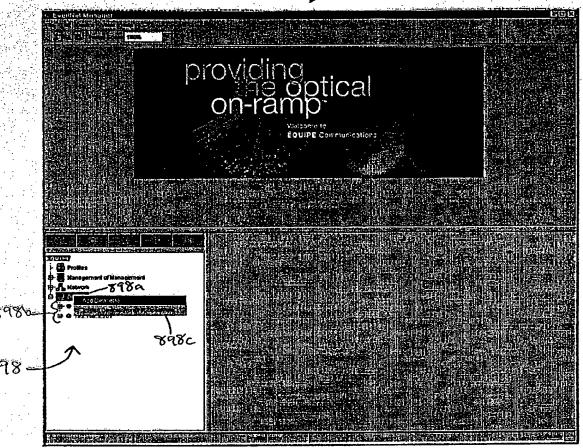


Fig.45



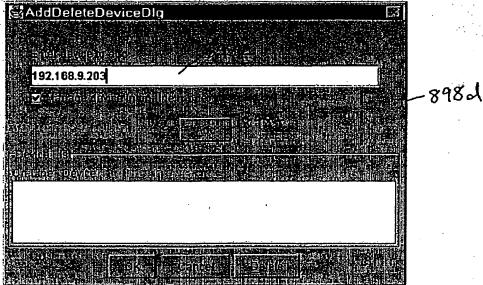
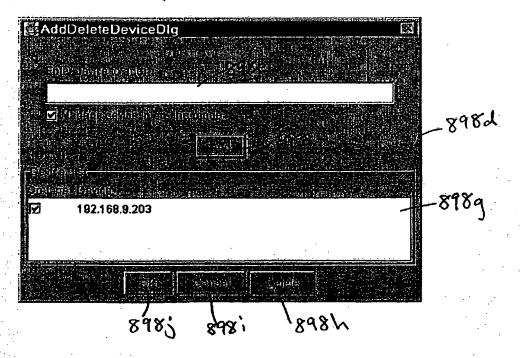


Fig. 4d





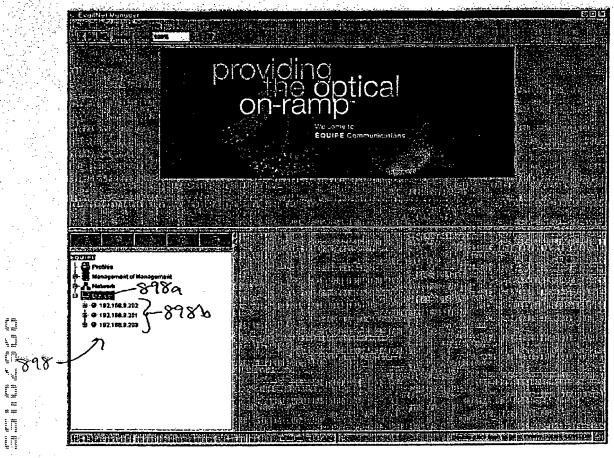
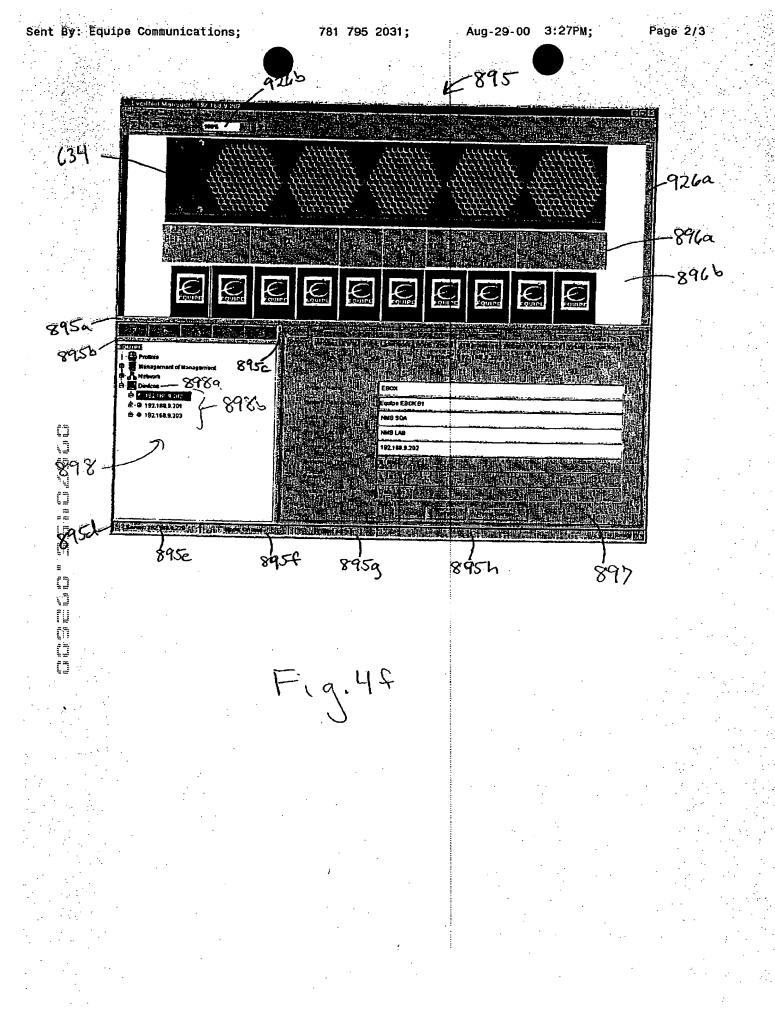


Fig.4e



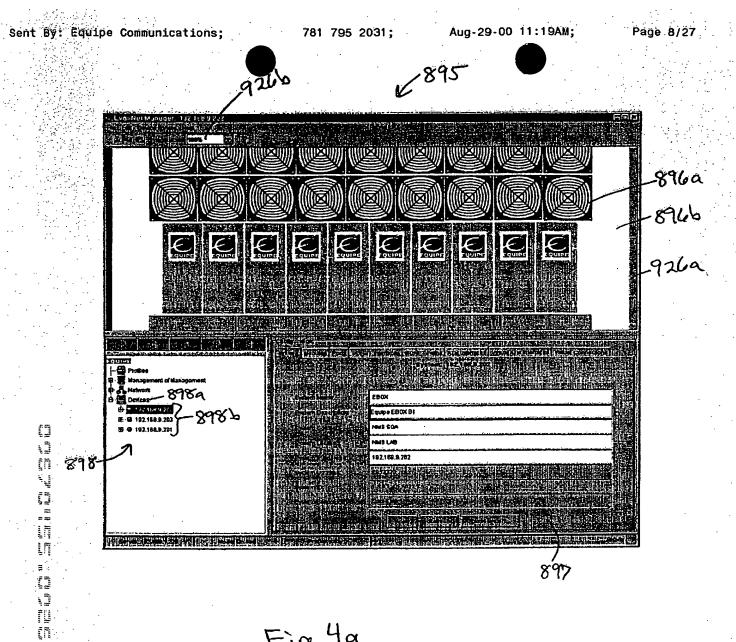
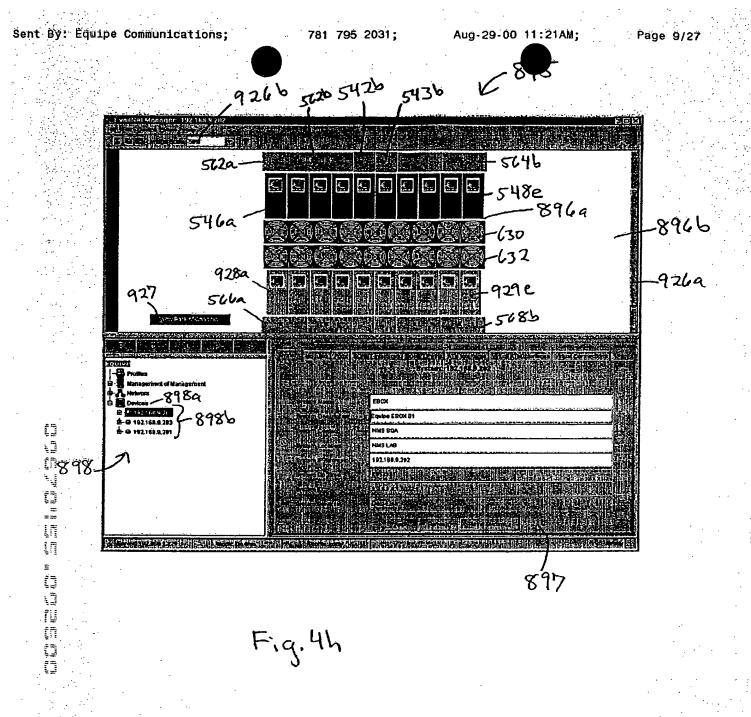
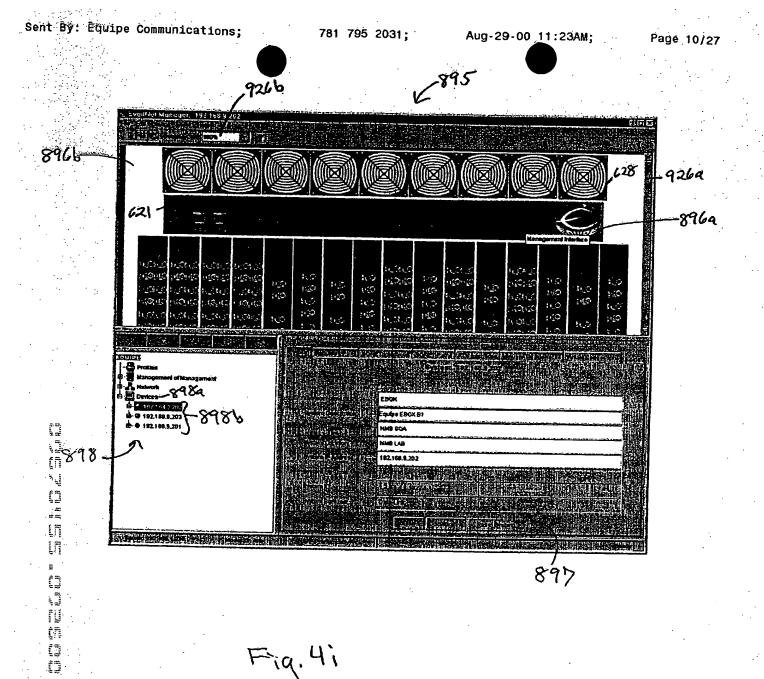
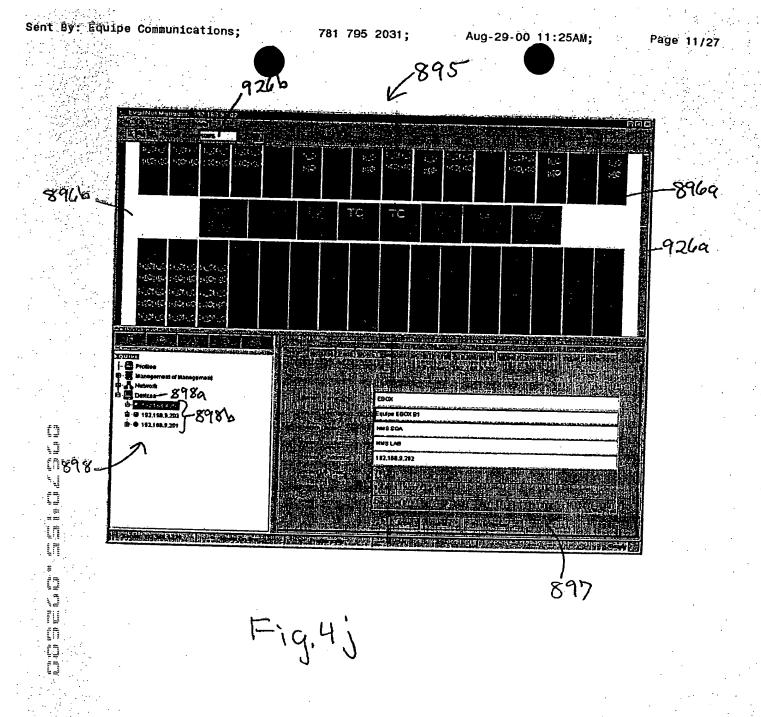
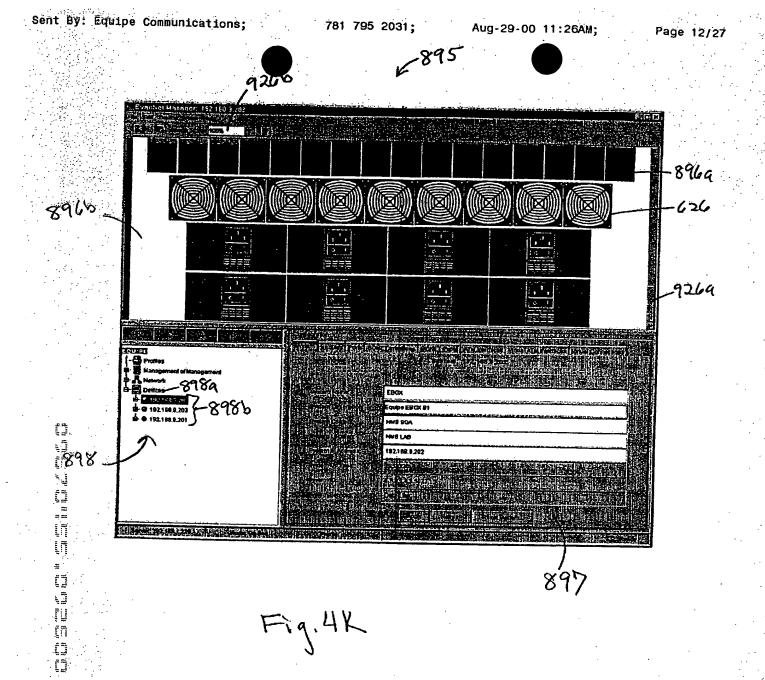


Fig.49









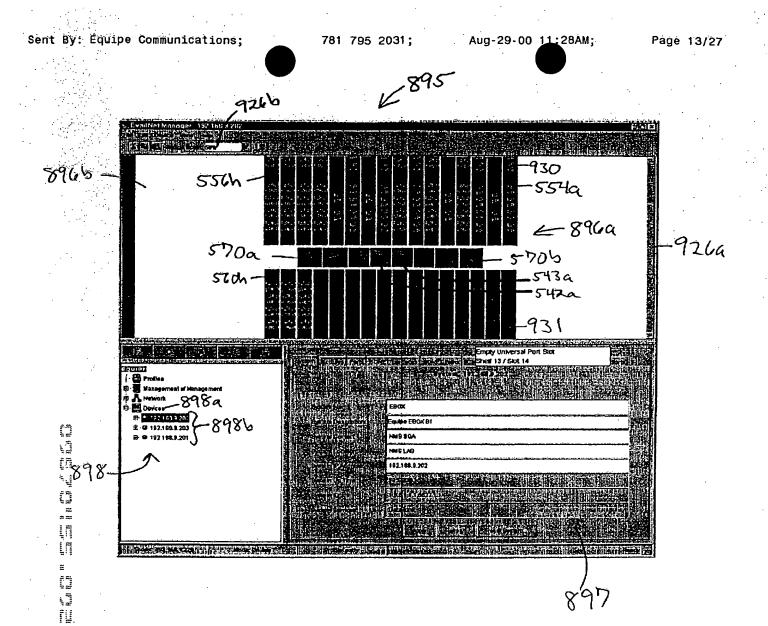


Fig. 4L

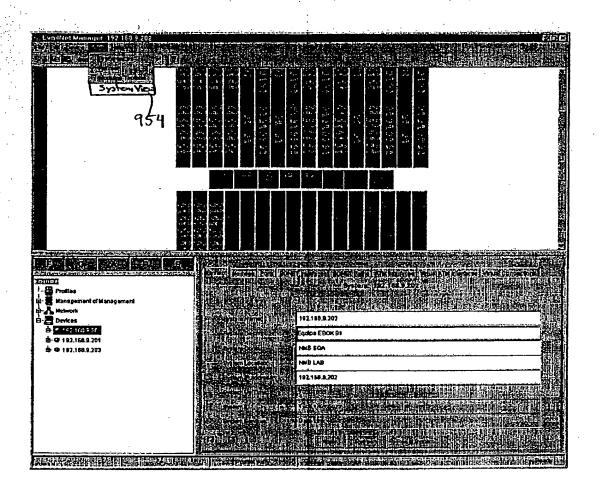


Fig. 4m



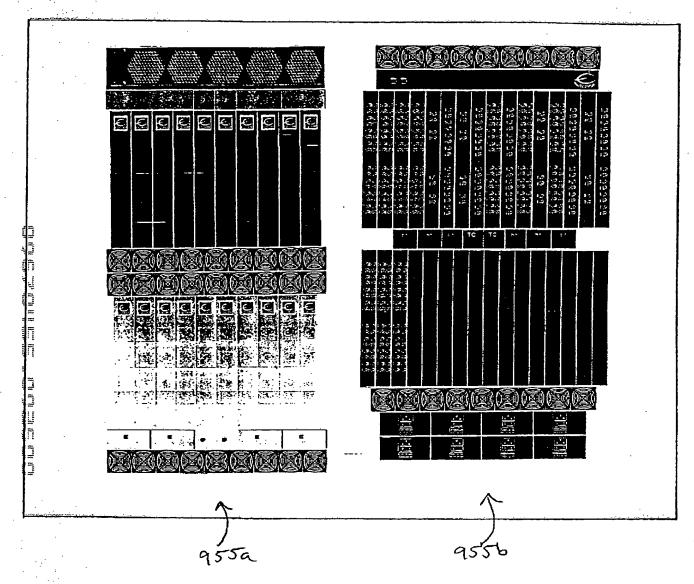


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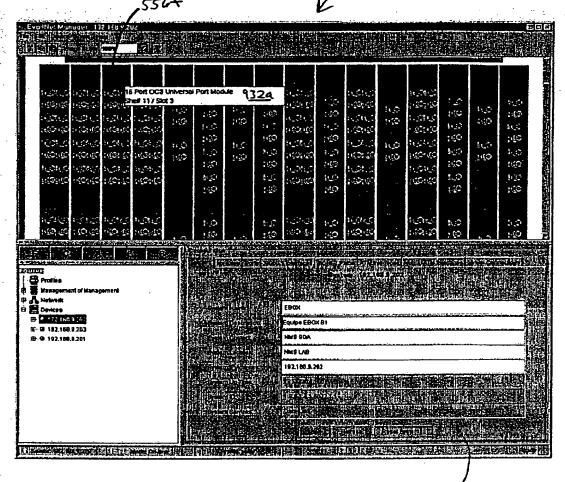


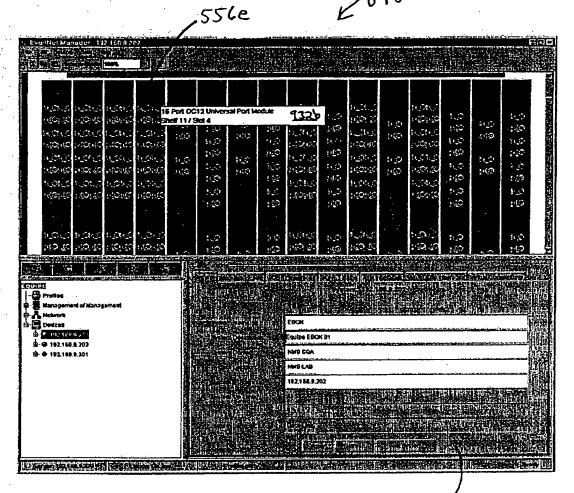
Fig.40

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Fig.4P

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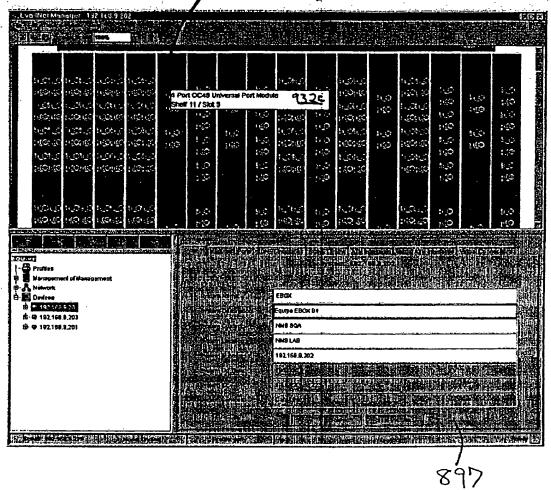


Fig. 48

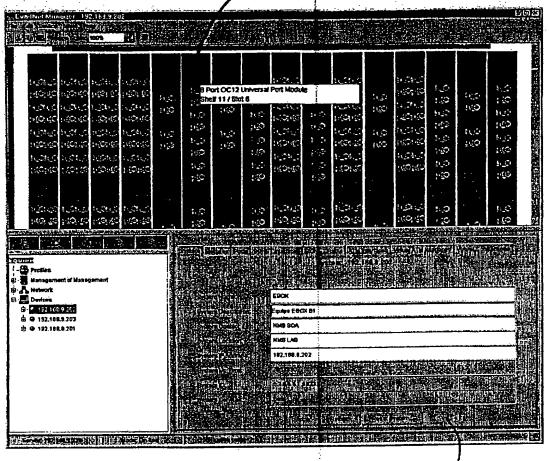
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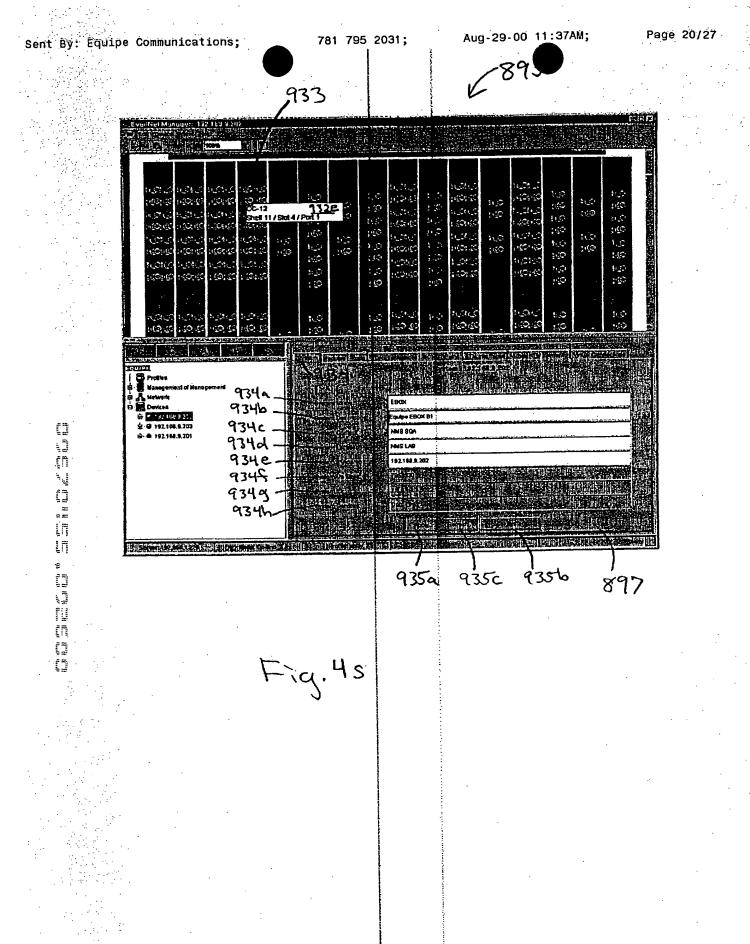
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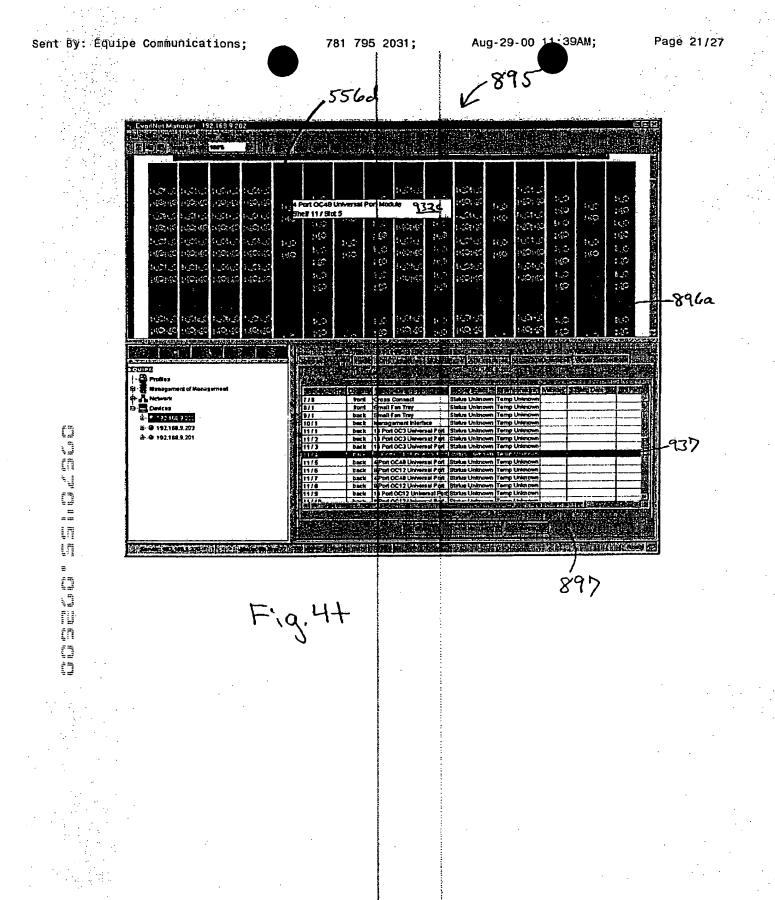
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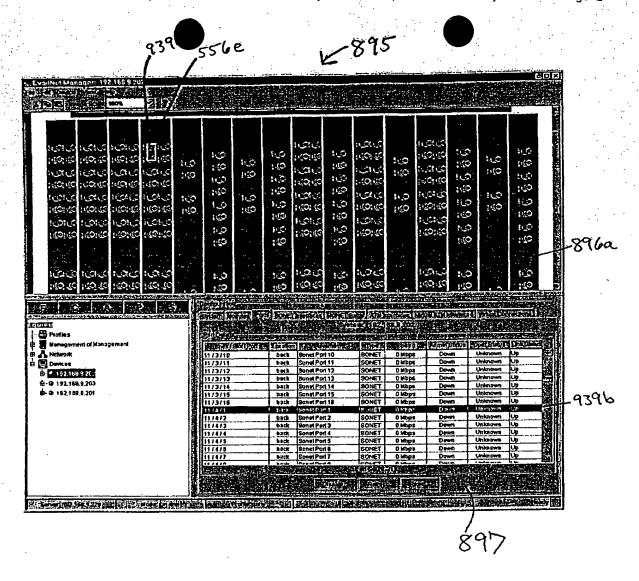
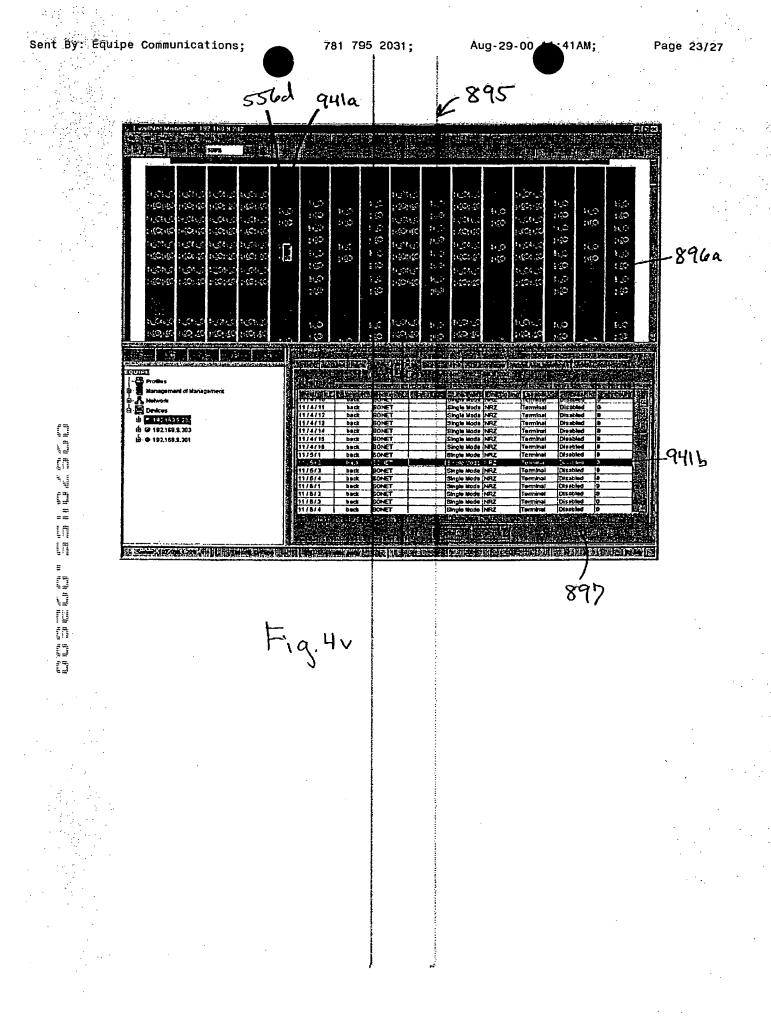
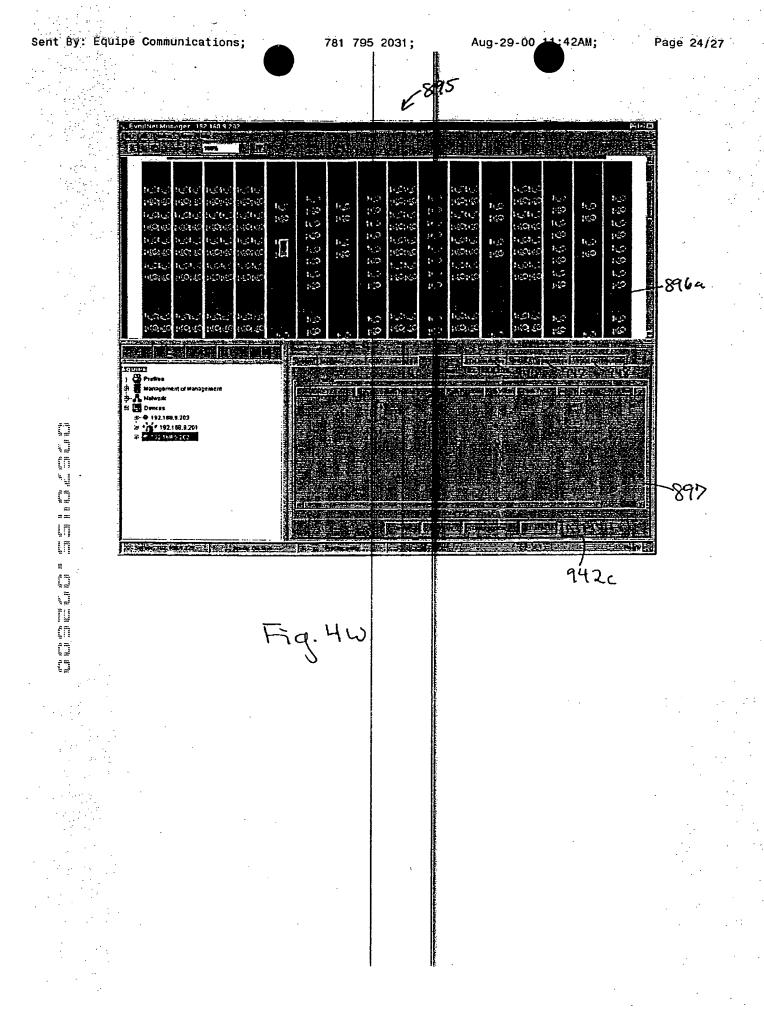
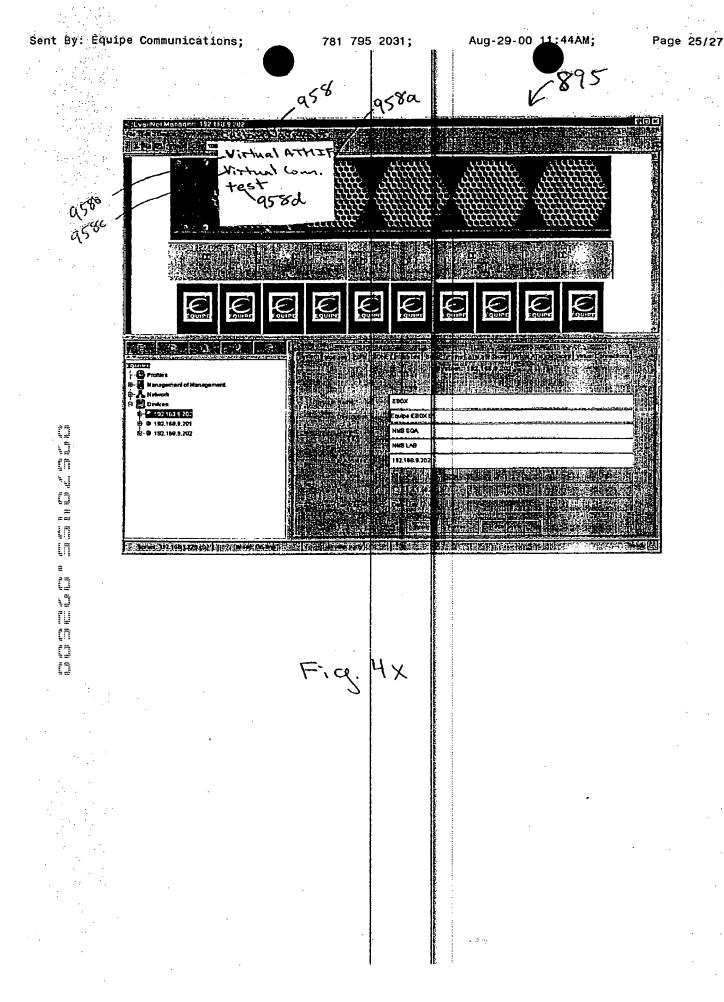
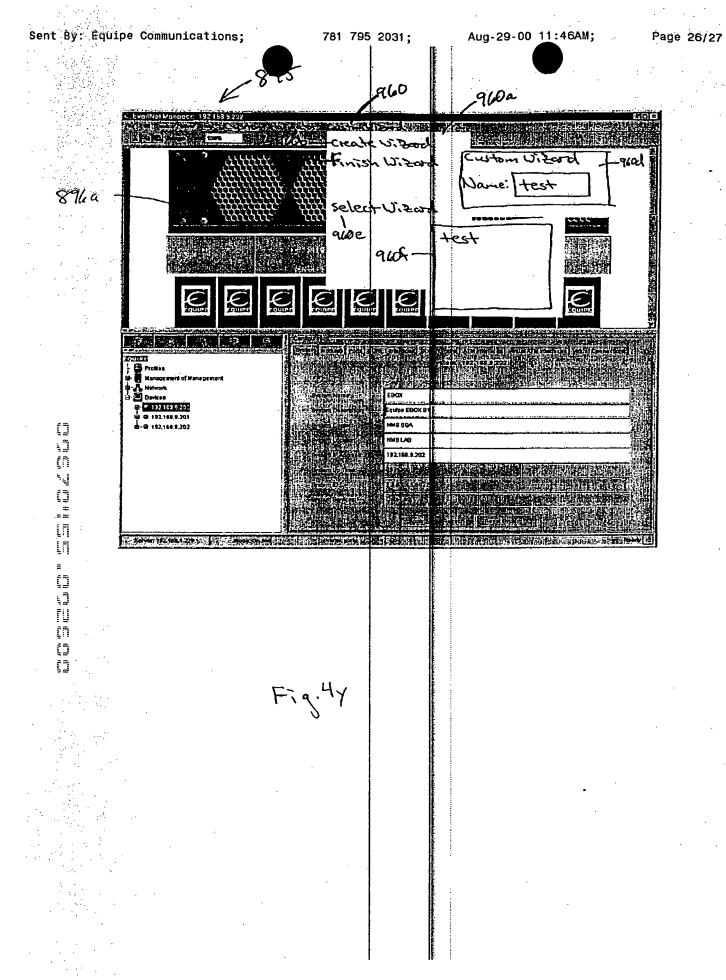


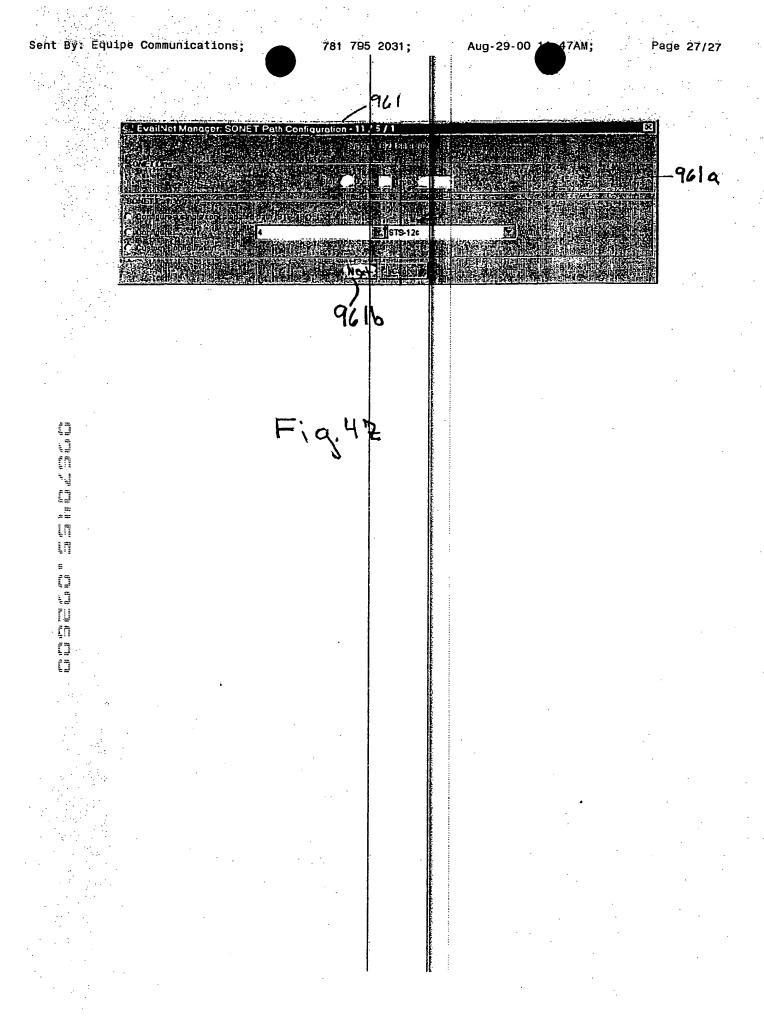
Fig.4u

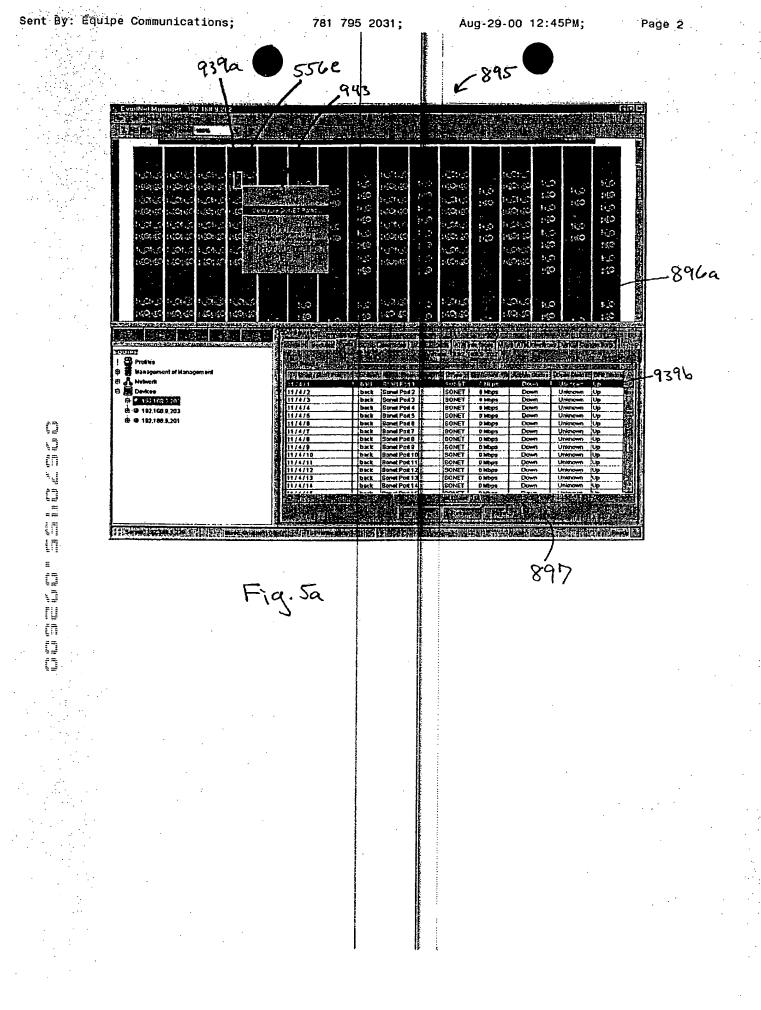


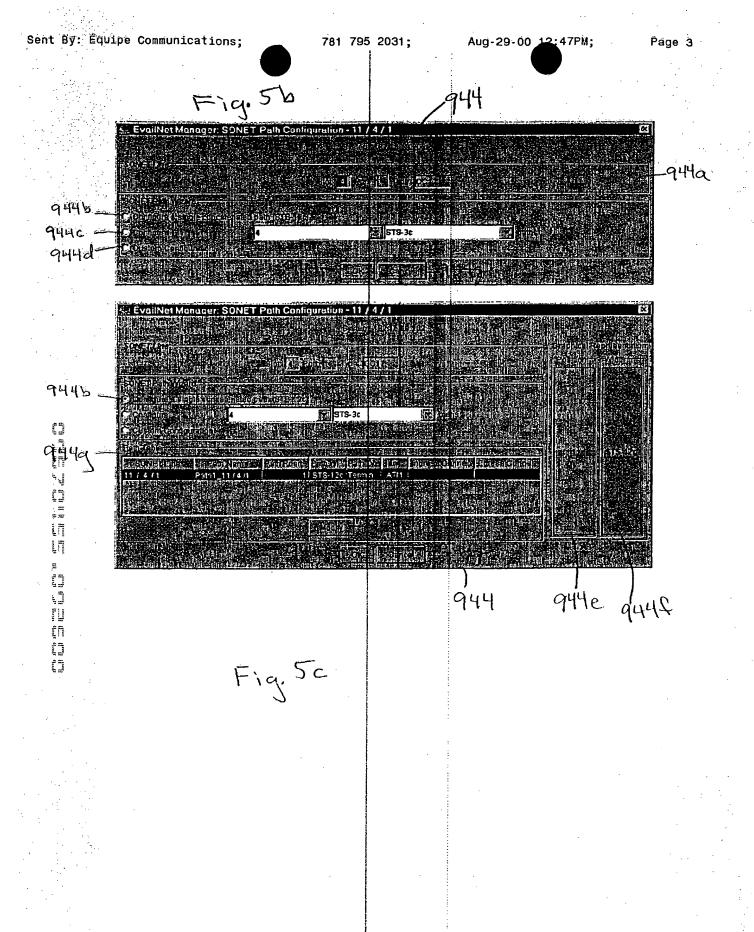


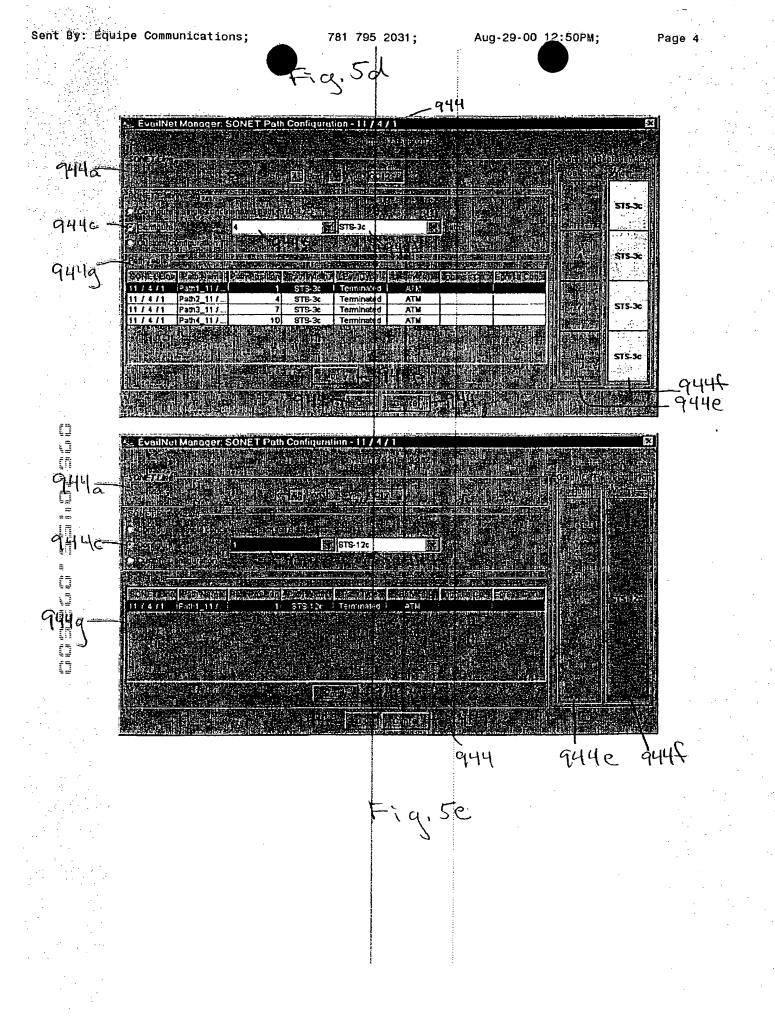


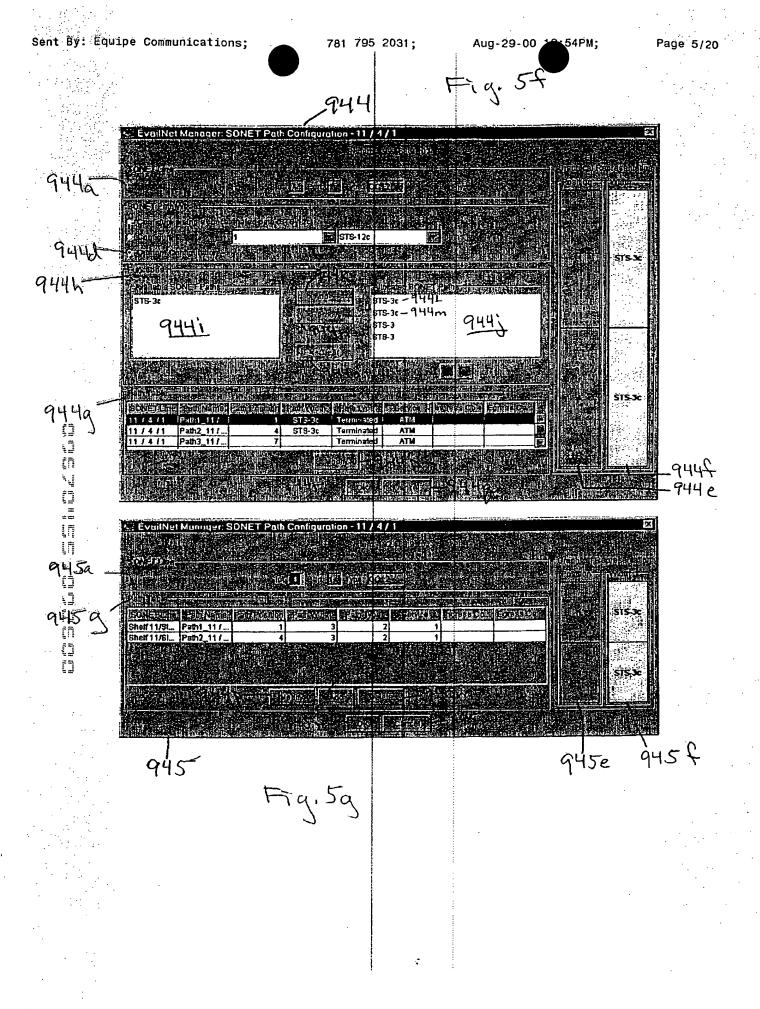


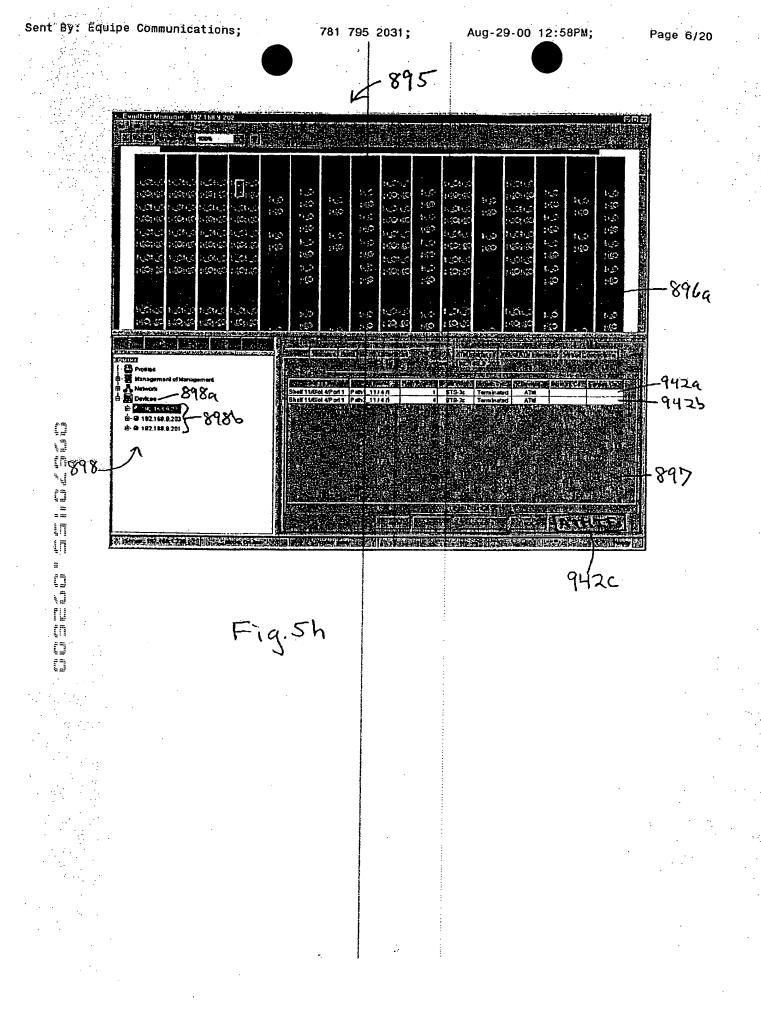


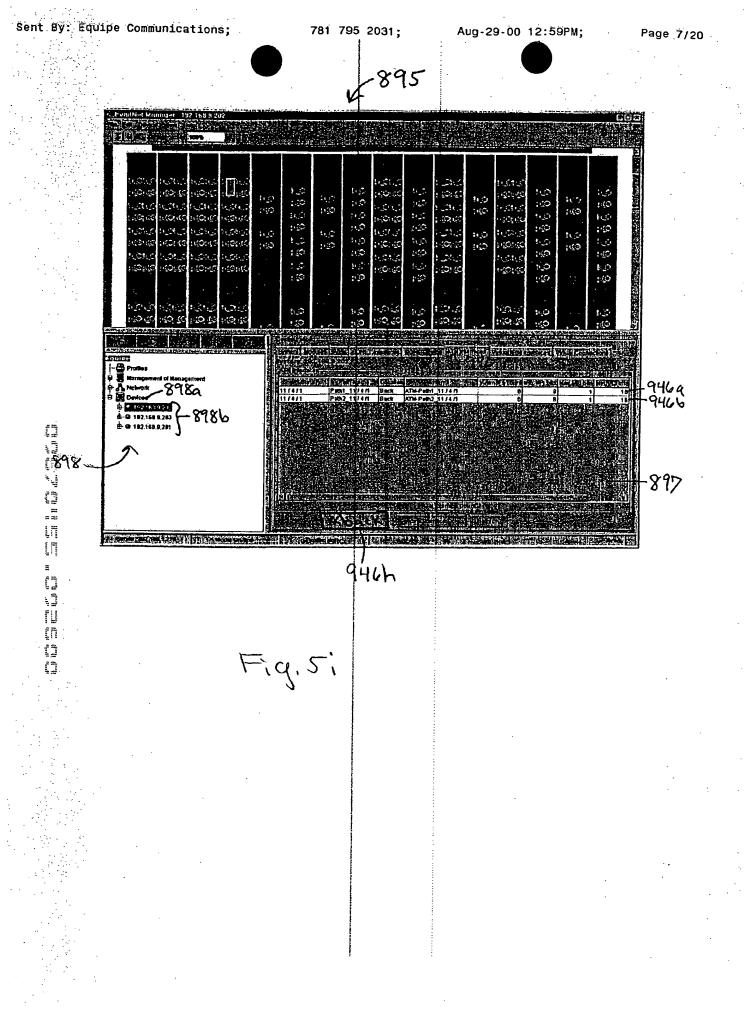


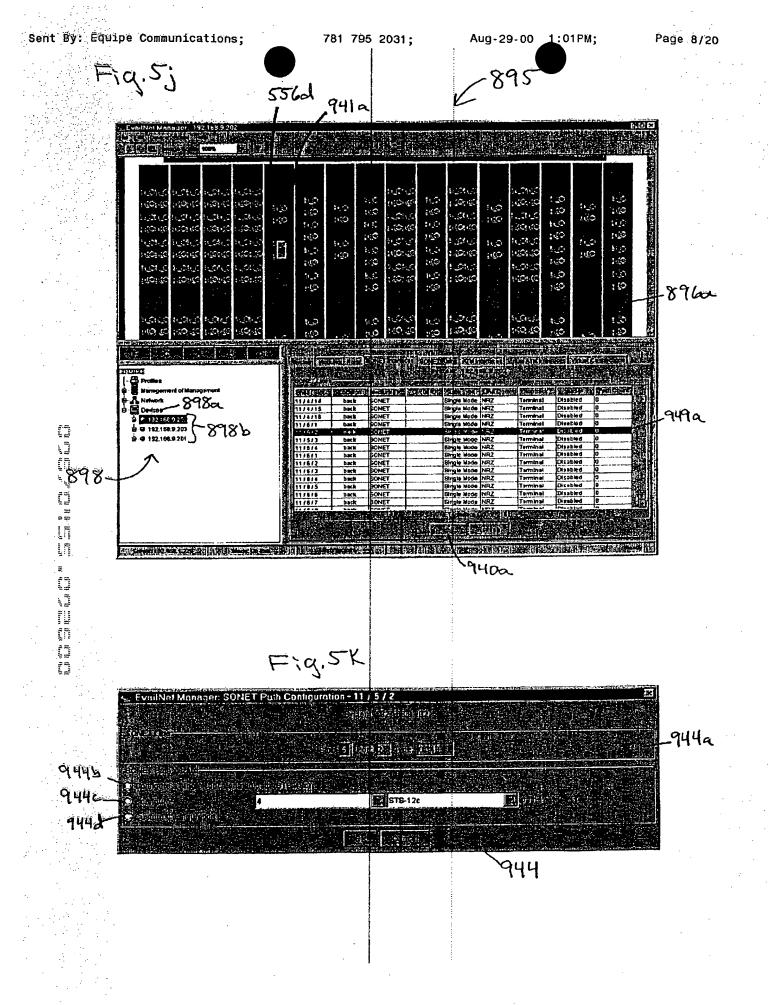


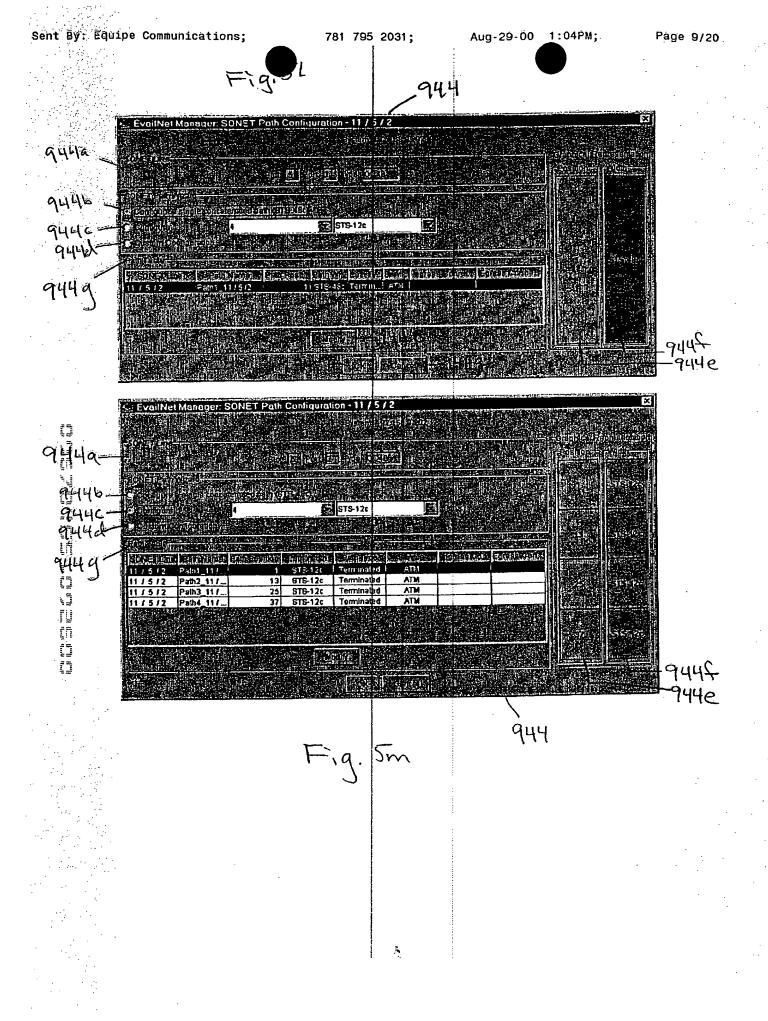


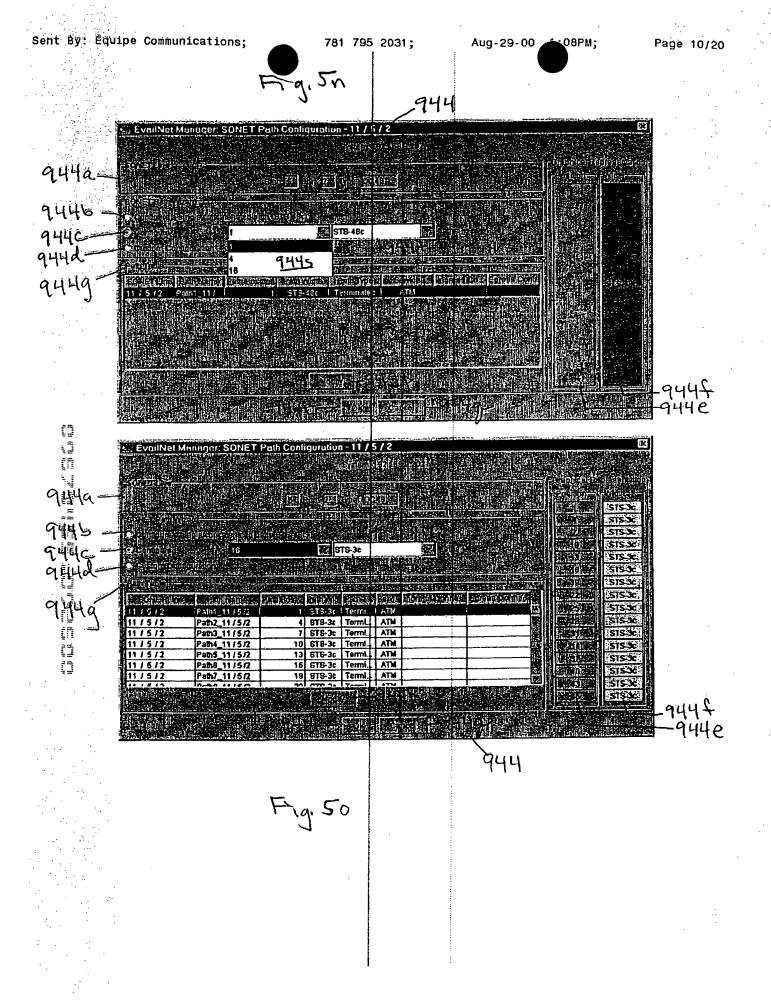


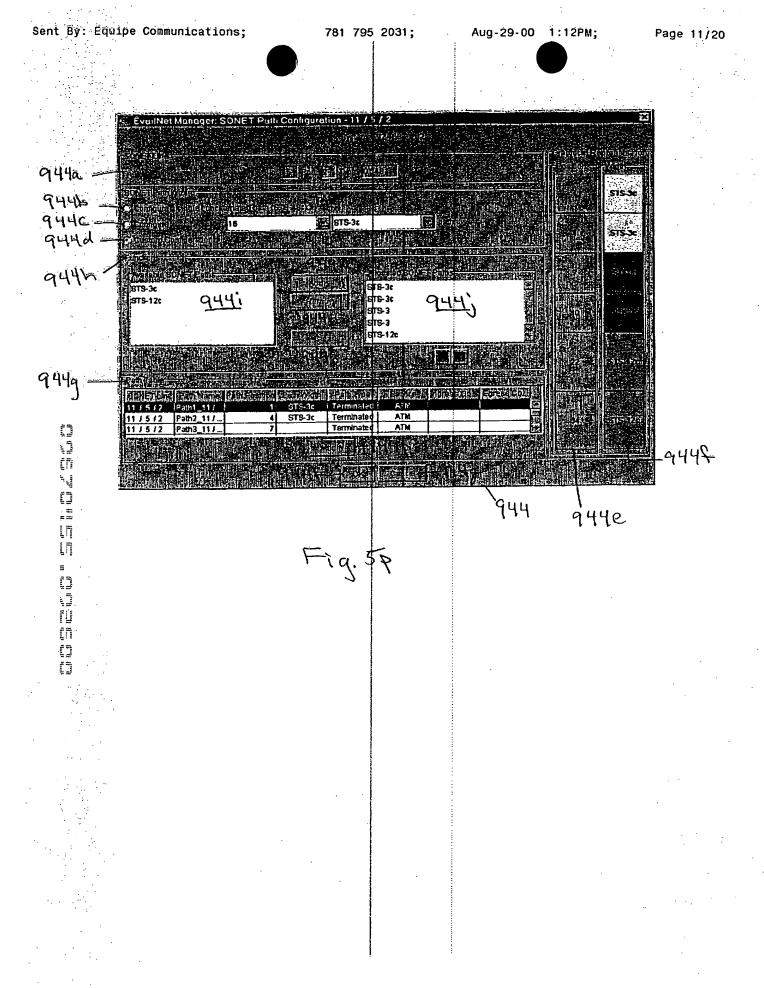


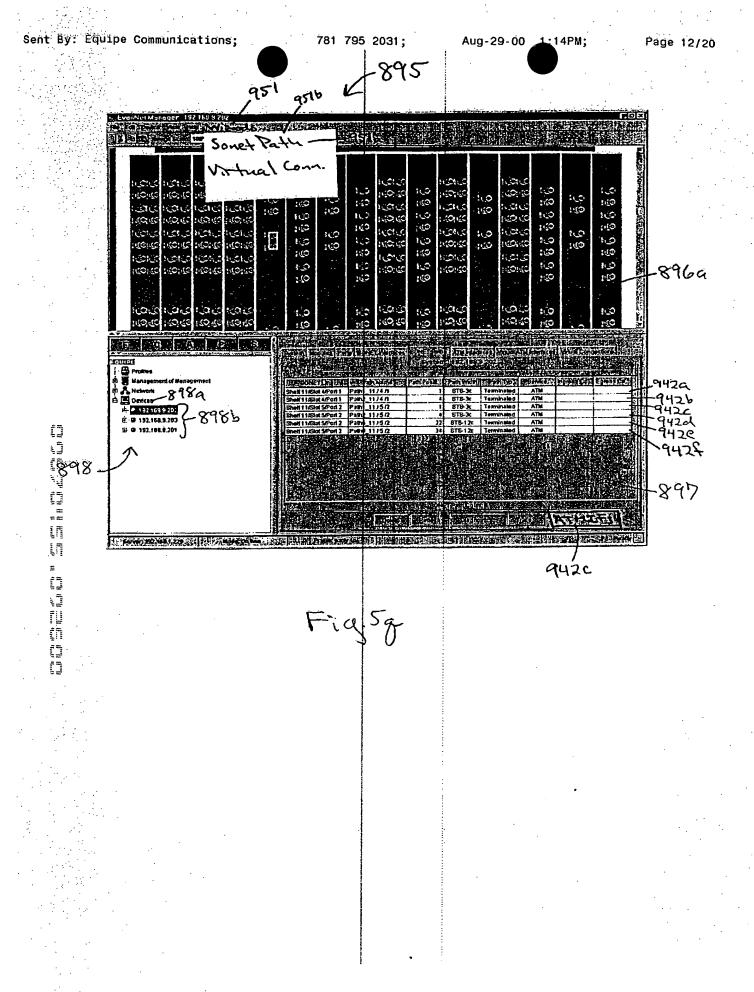


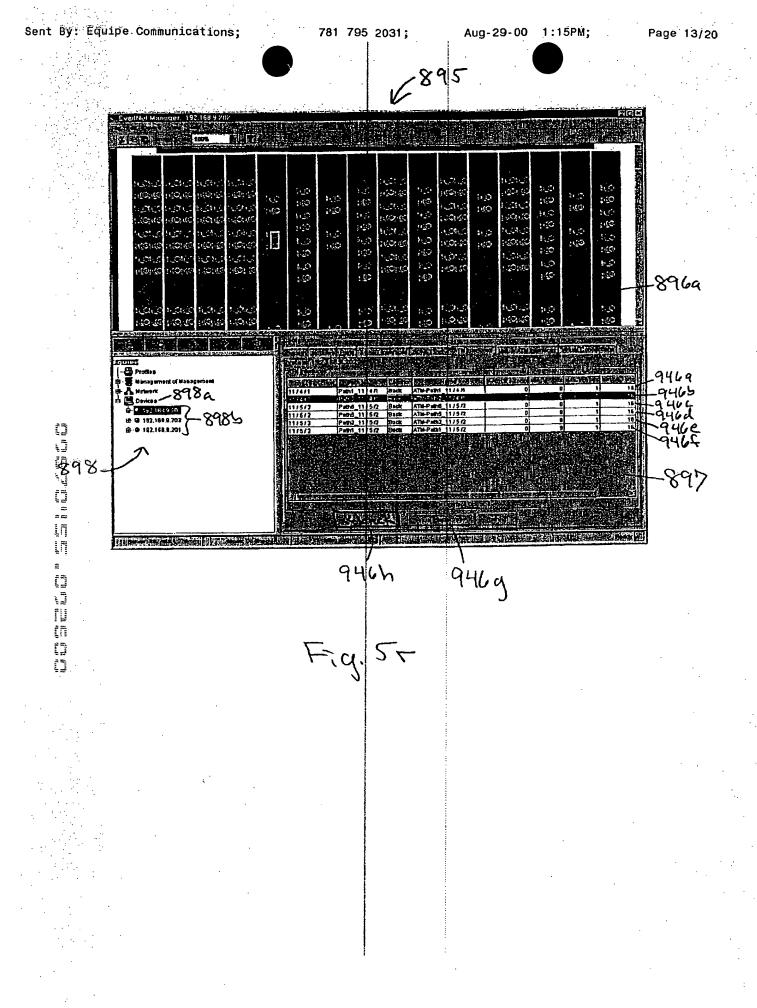


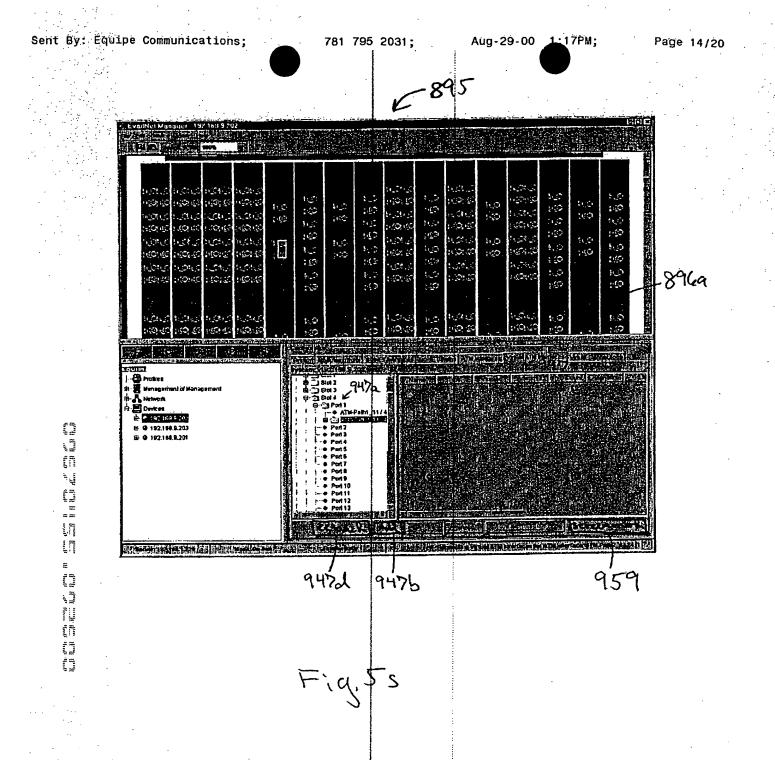


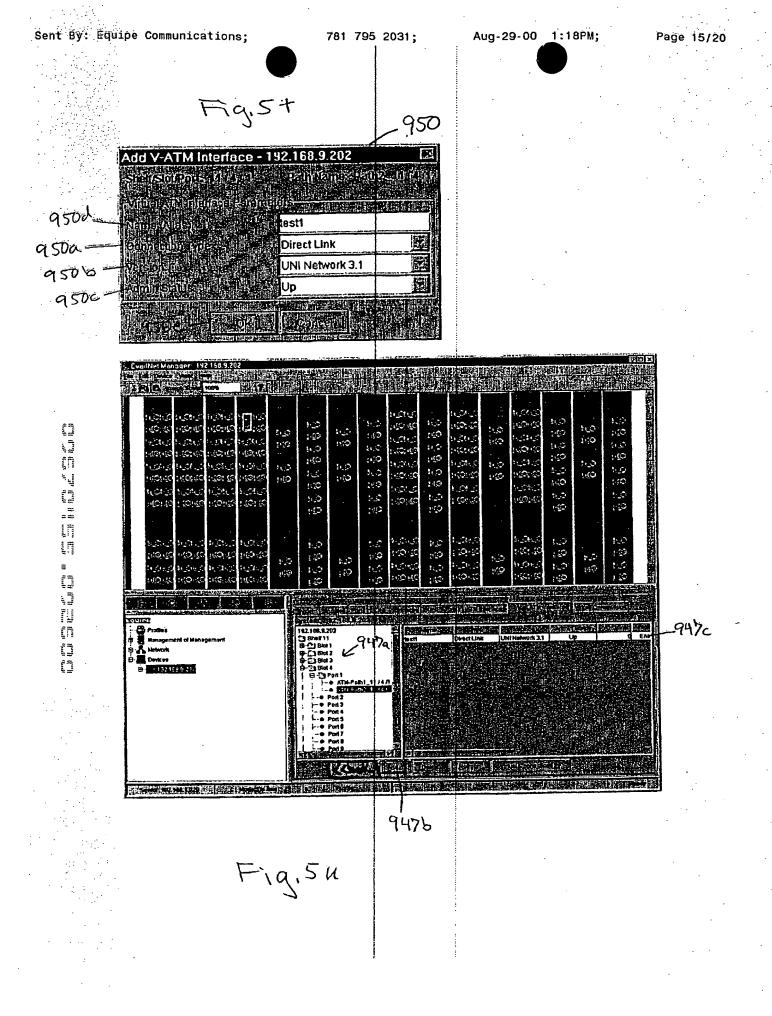


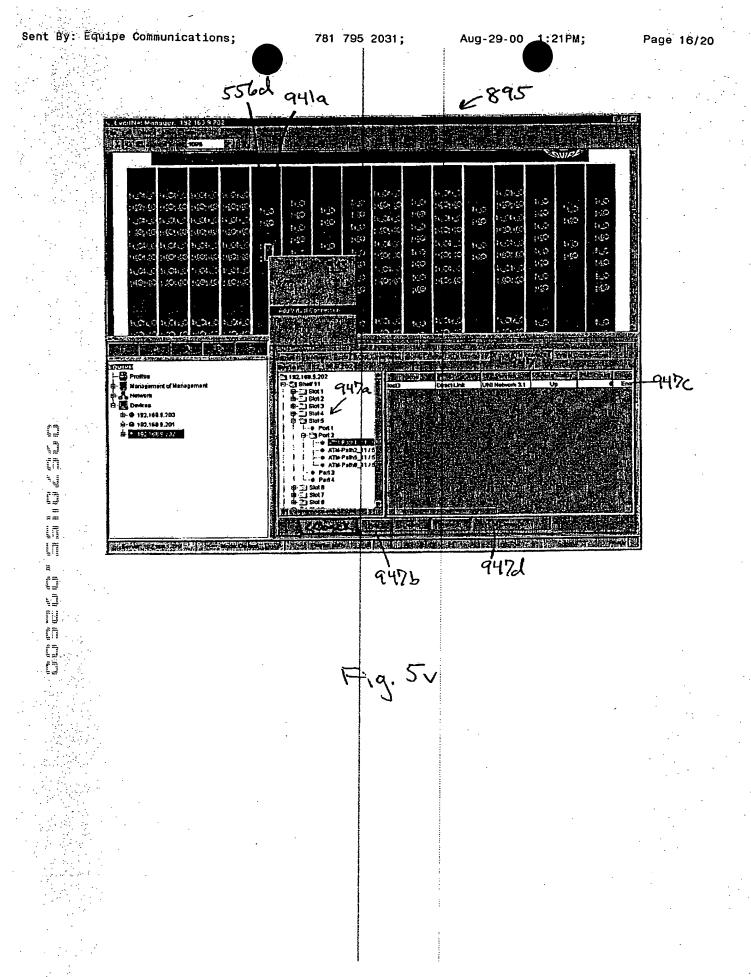


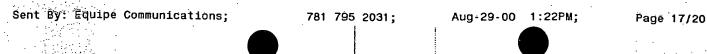












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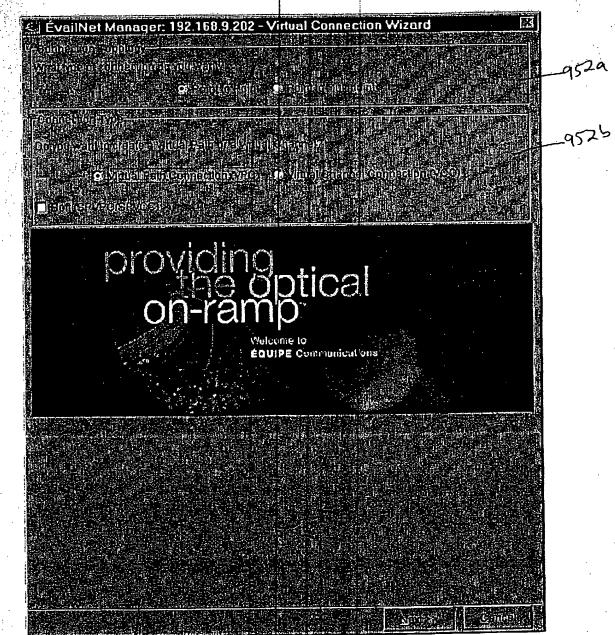
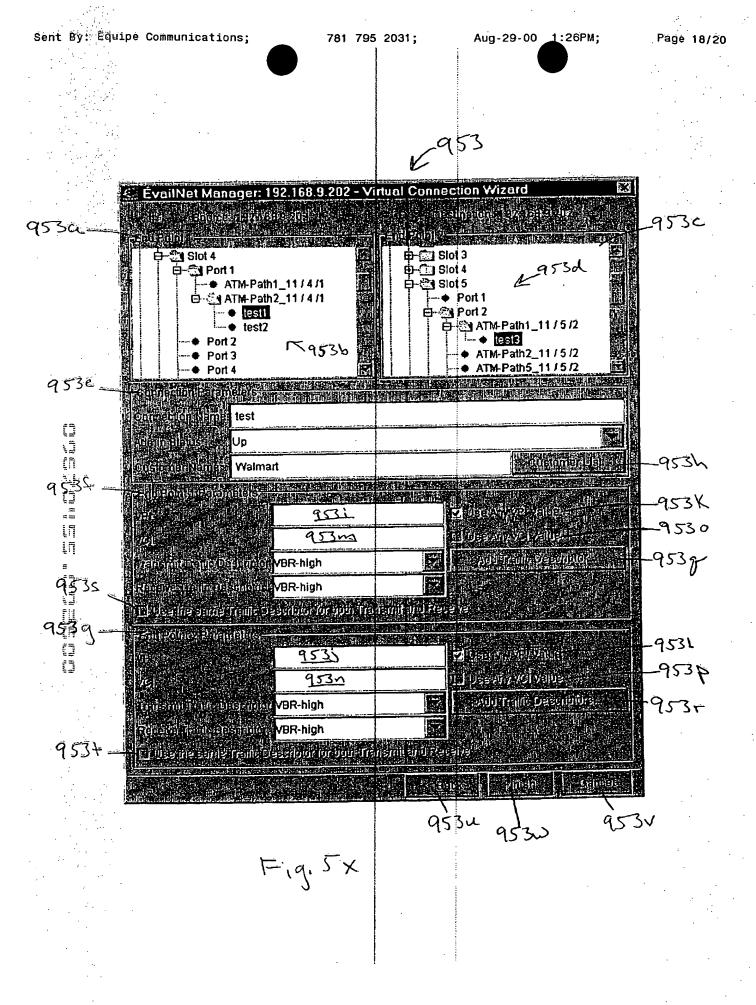
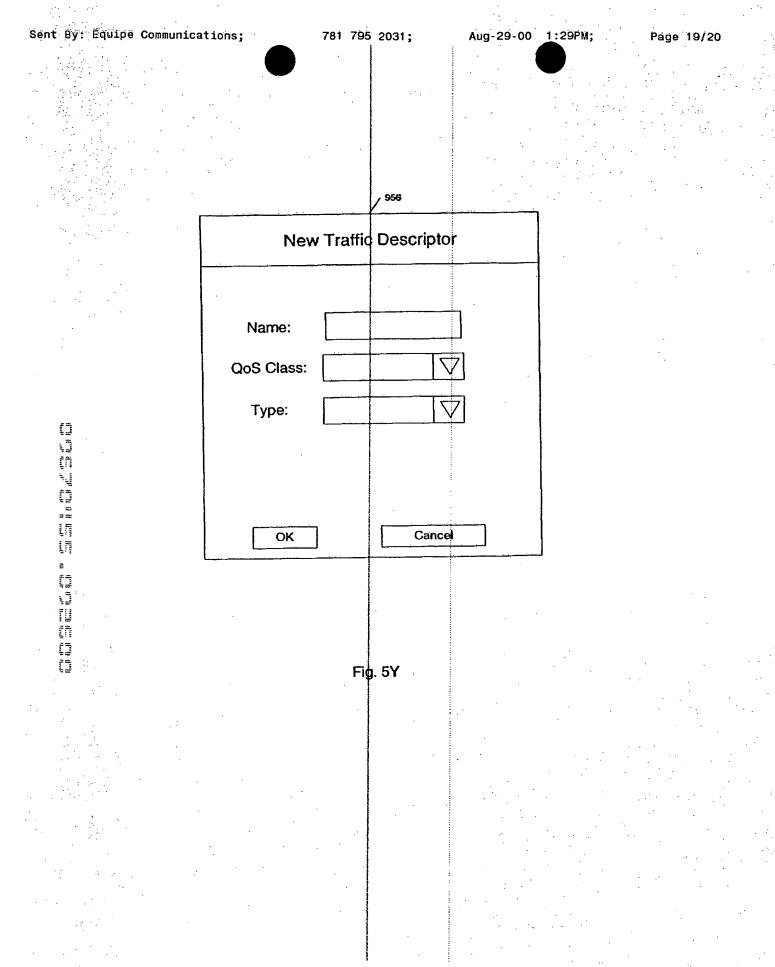
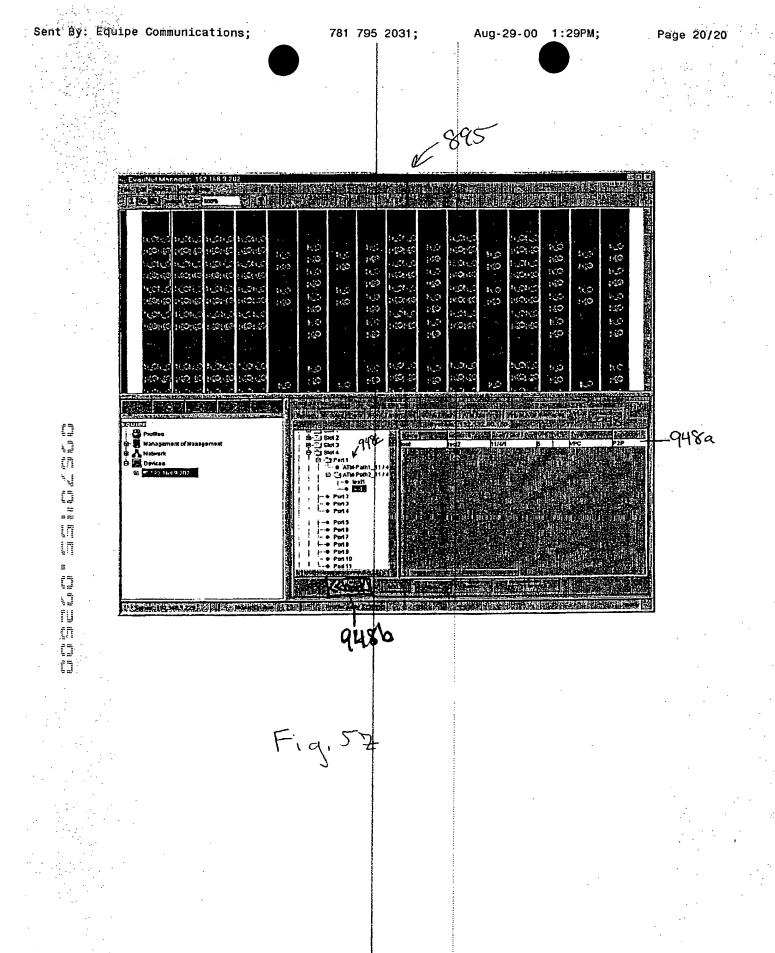


Fig. 5w







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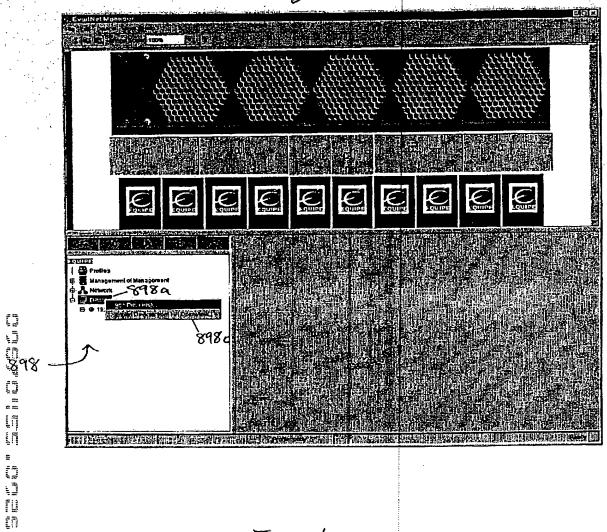


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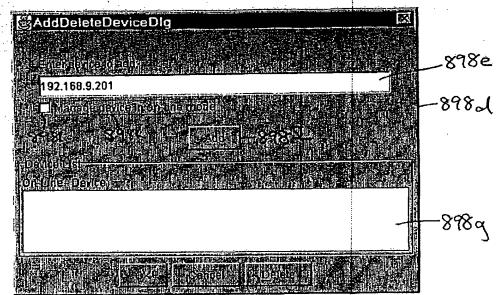
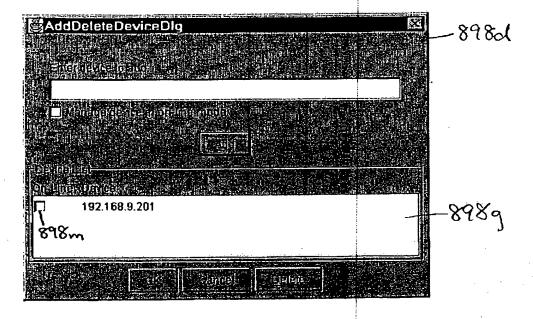


Fig.6c



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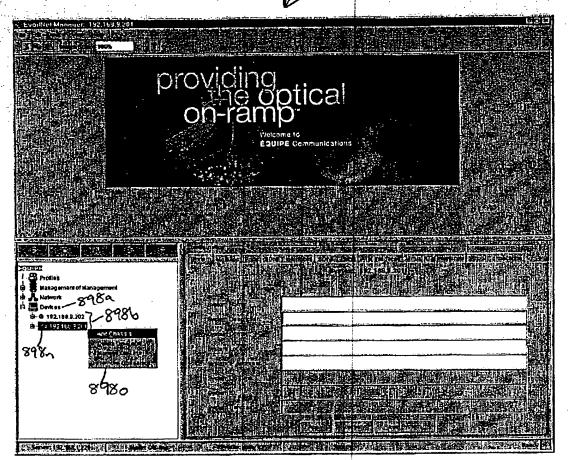


Fig.6d



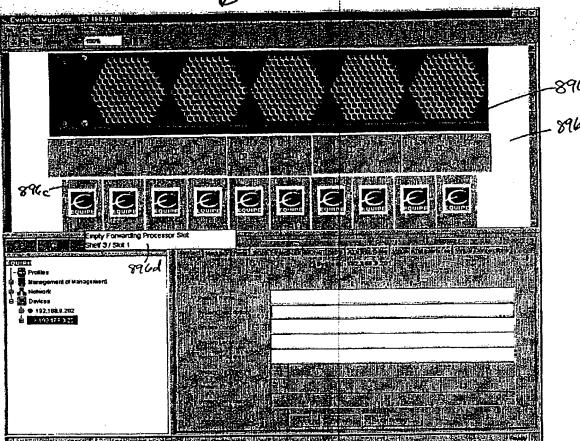


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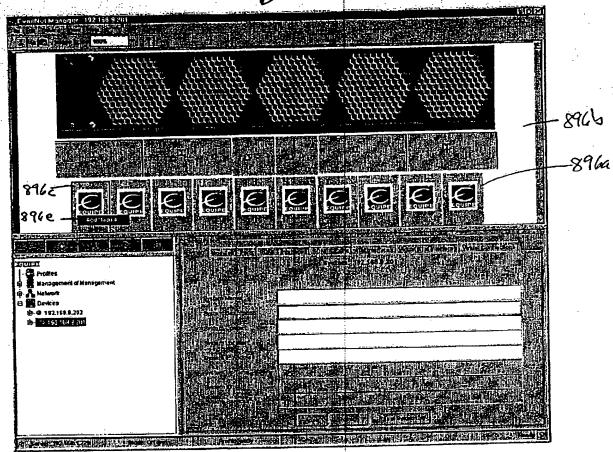


Fig.6f

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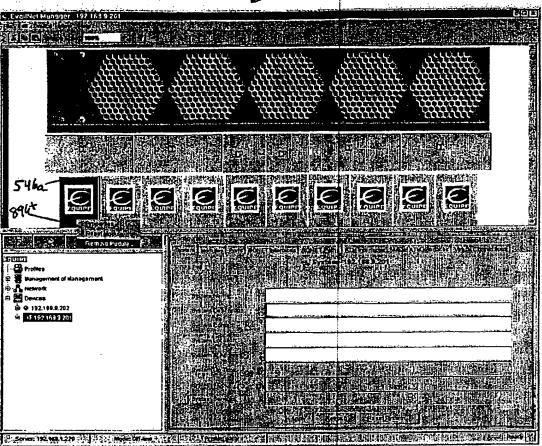


Fig. 69

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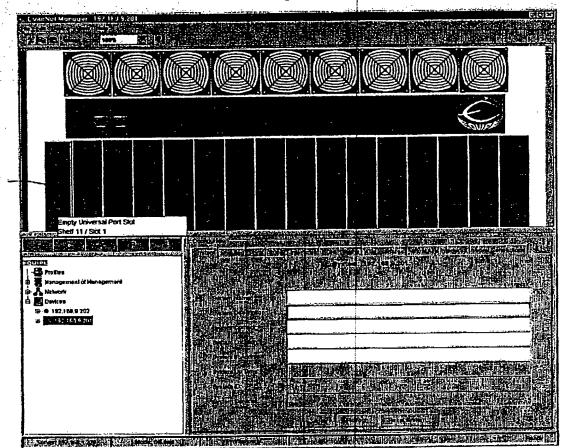
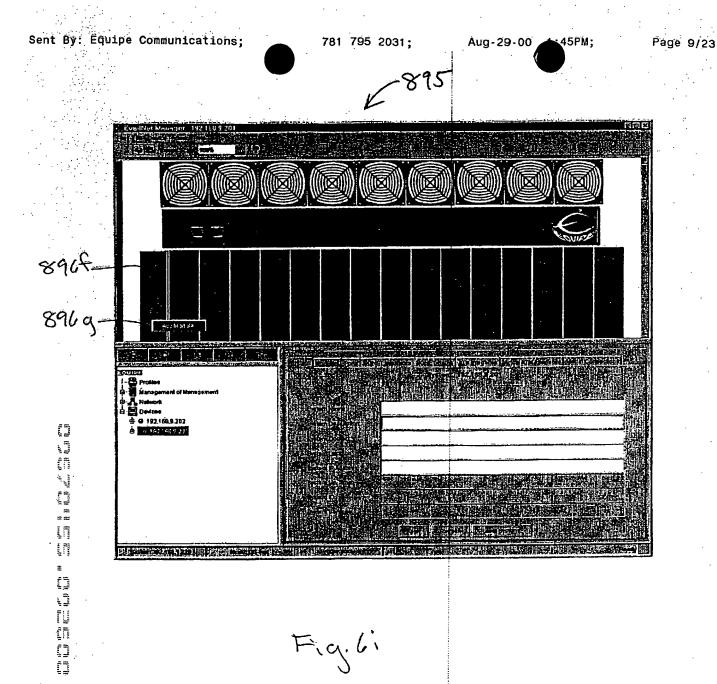


Fig.6h

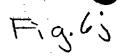


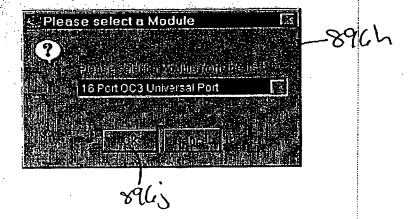
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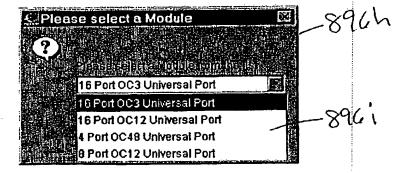


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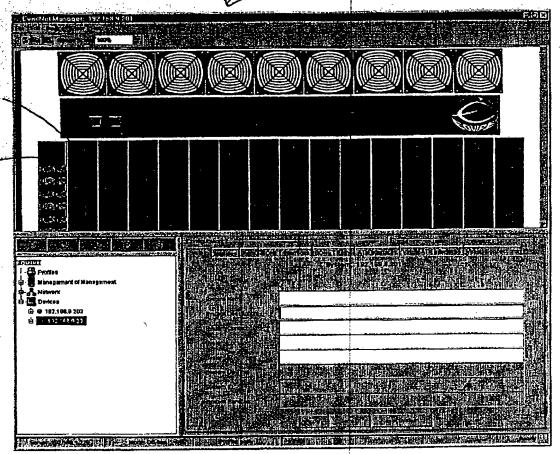


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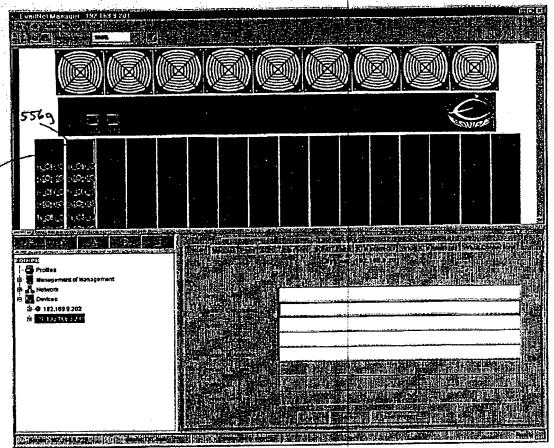


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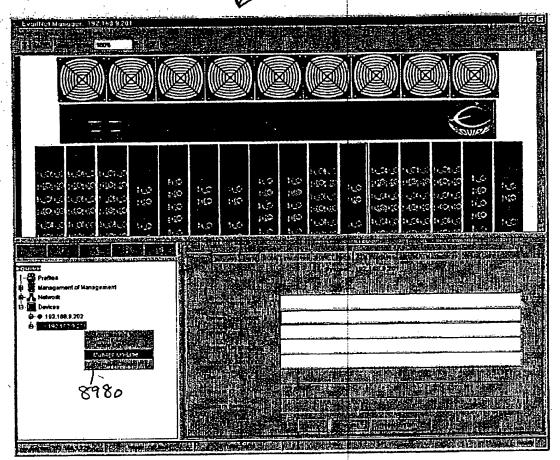


Fig. 6n

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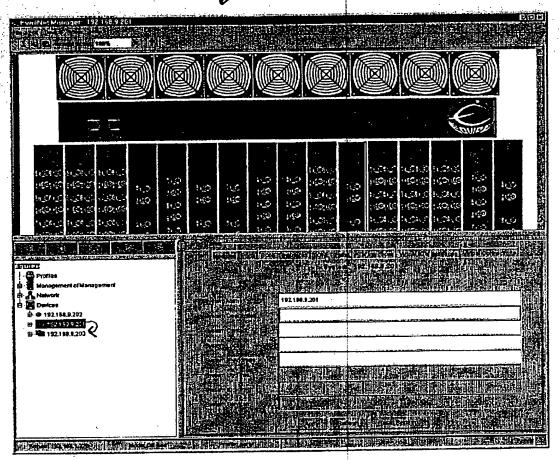
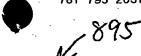
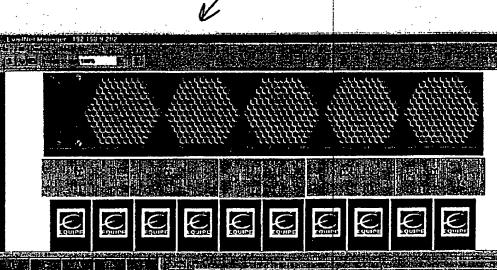


Fig. 60





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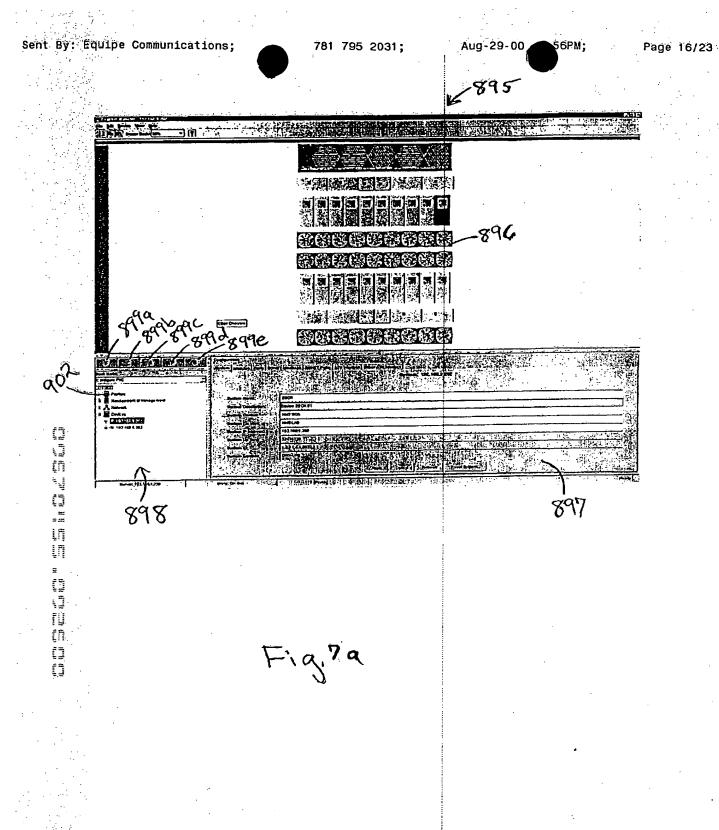
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Fig. 6P



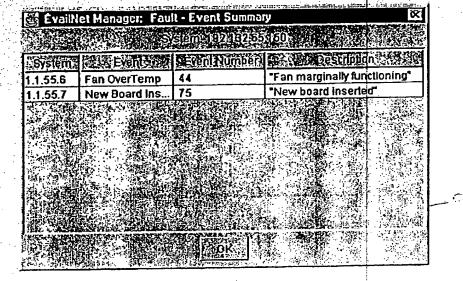
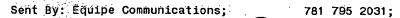


Fig.7b



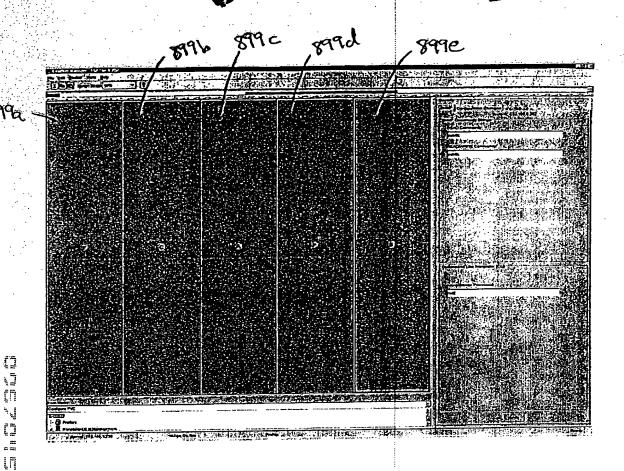
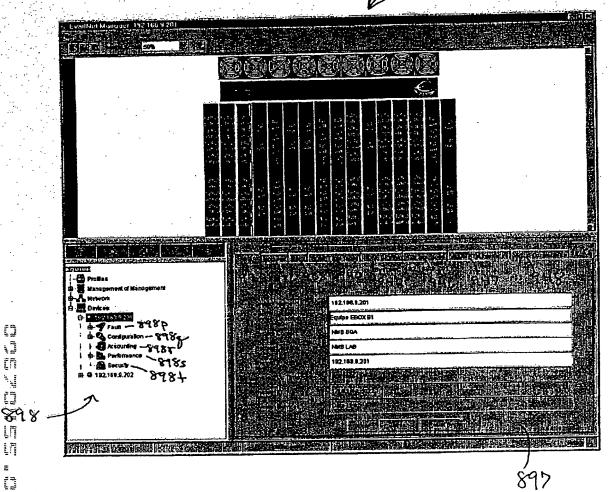


Fig.7c

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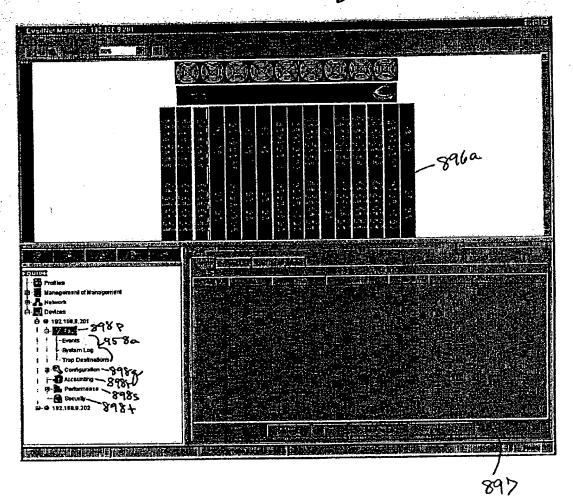
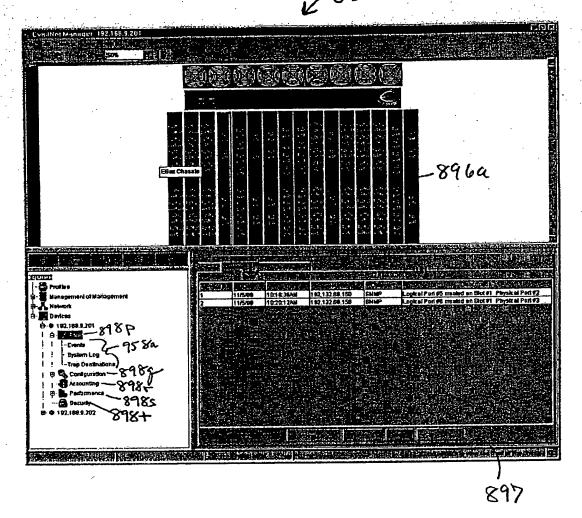


Fig. 7e

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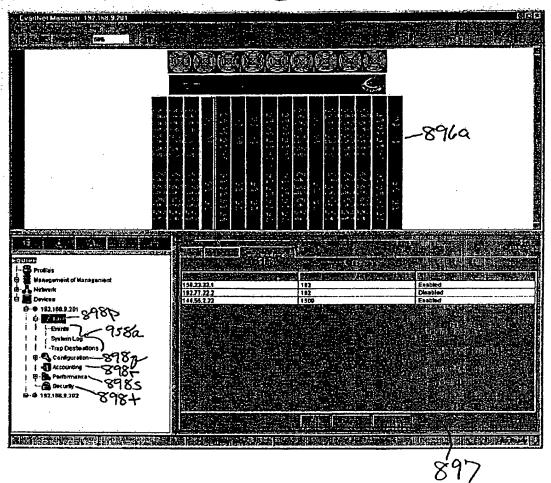


Fig. 79

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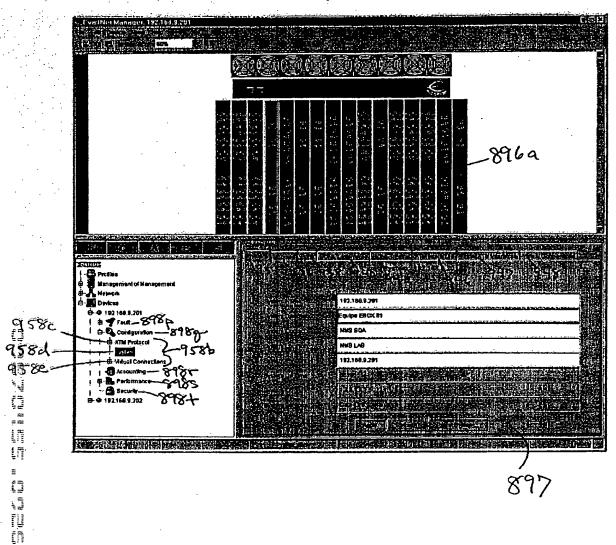
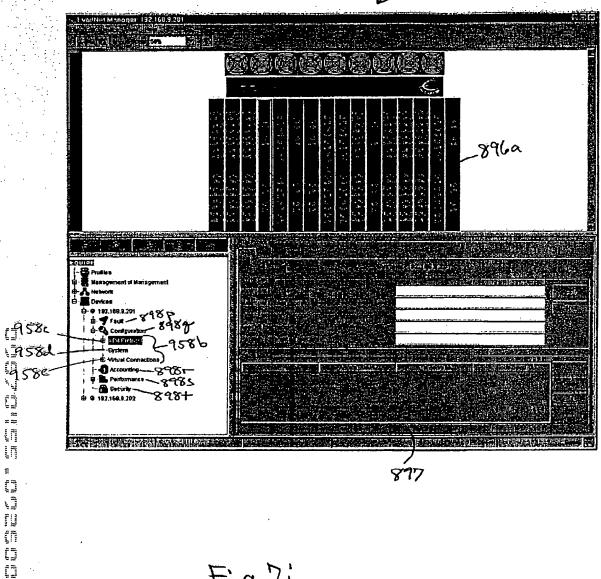


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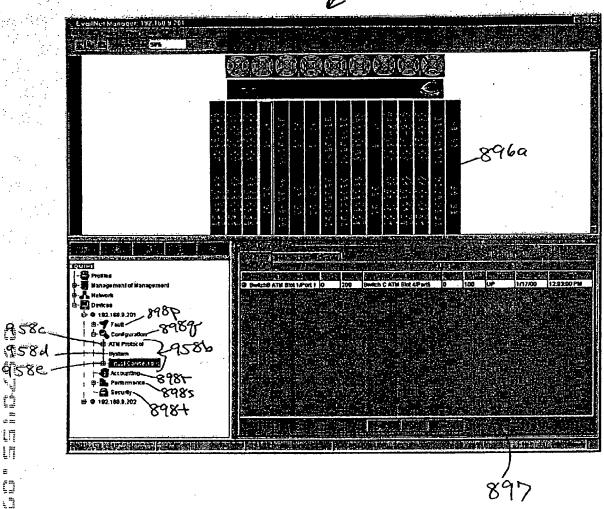


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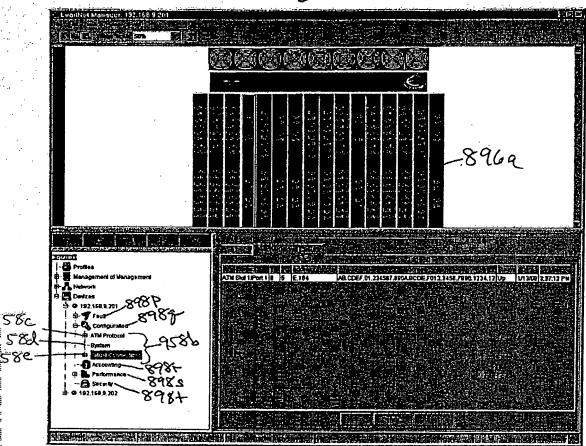


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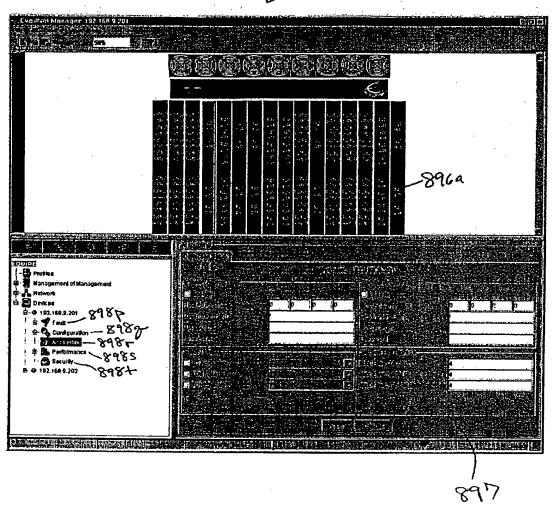


Fig. 7L

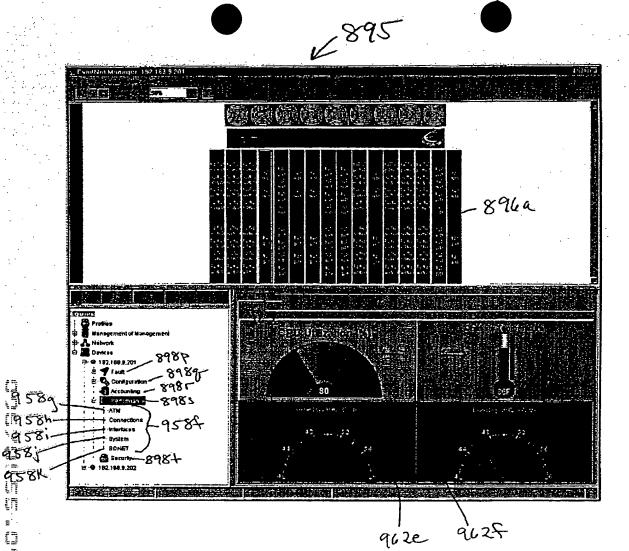
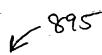


Fig.7m



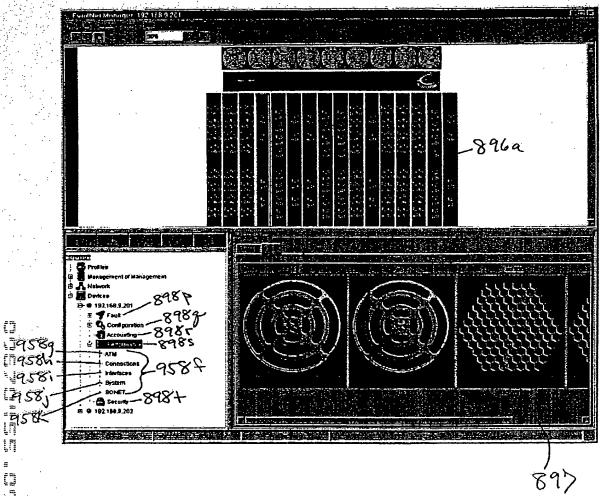
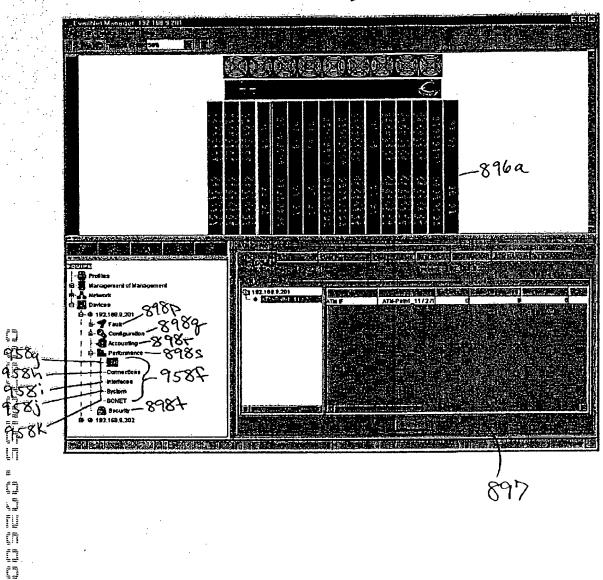


Fig. m



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Fig. 70

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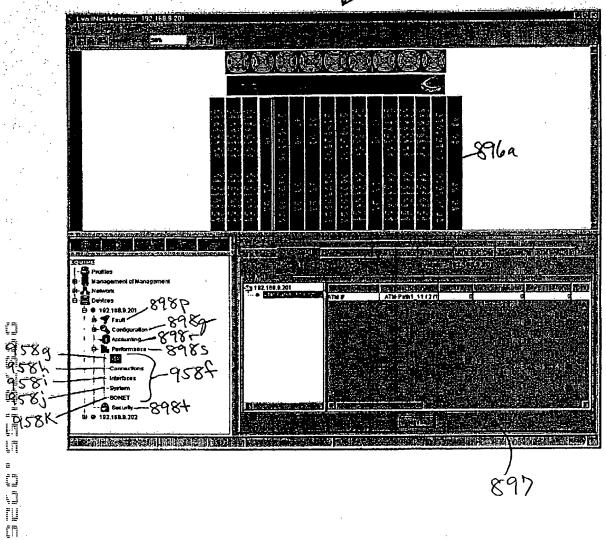


Fig. 7P

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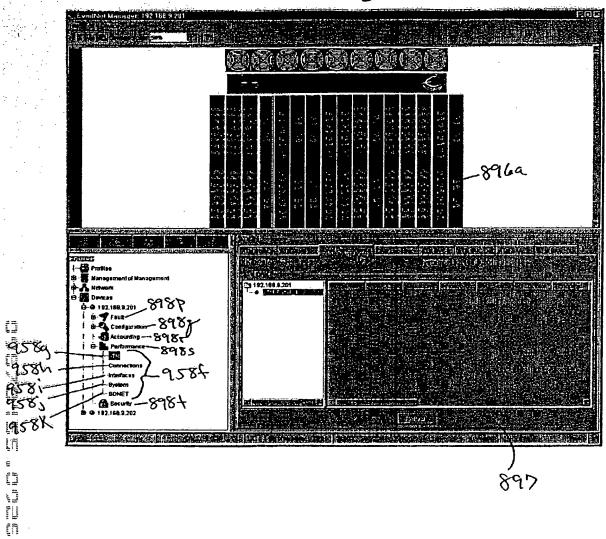


Fig. 7g

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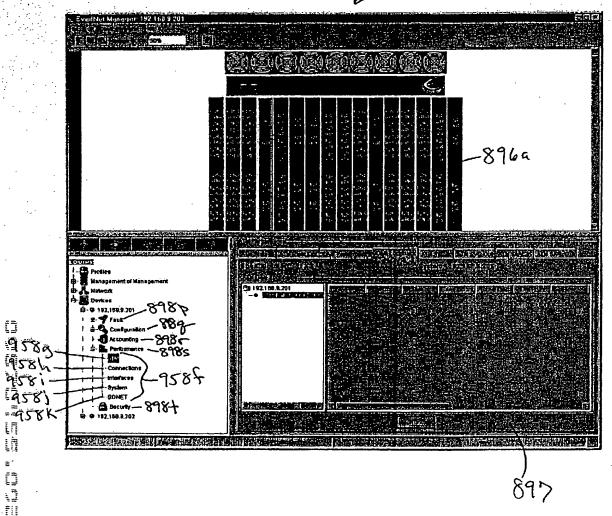


Fig. 7r

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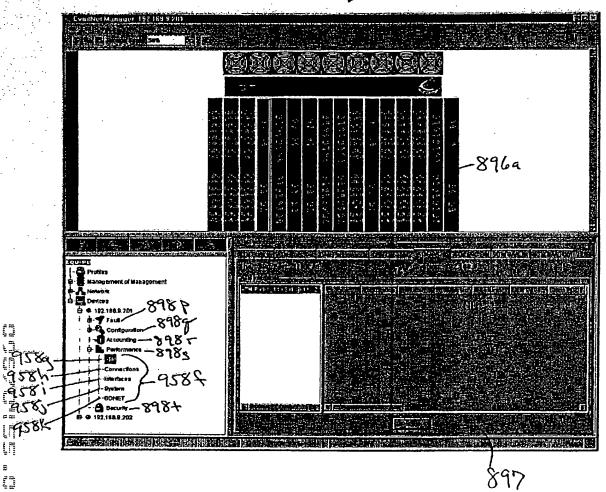


Fig. 7s

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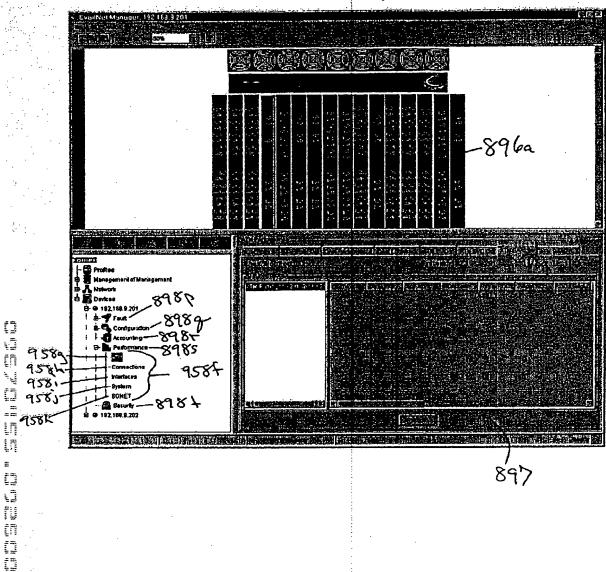
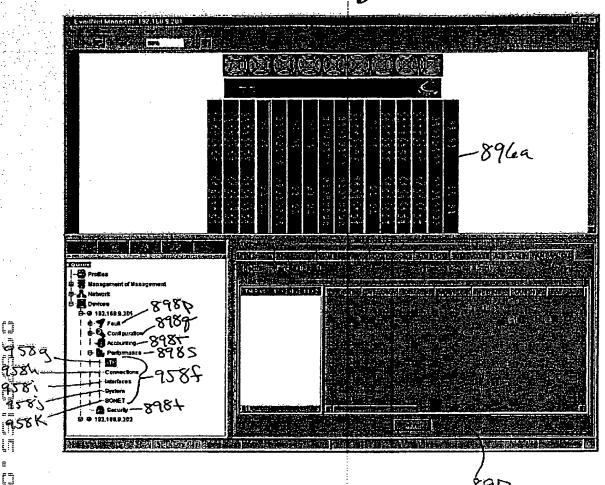
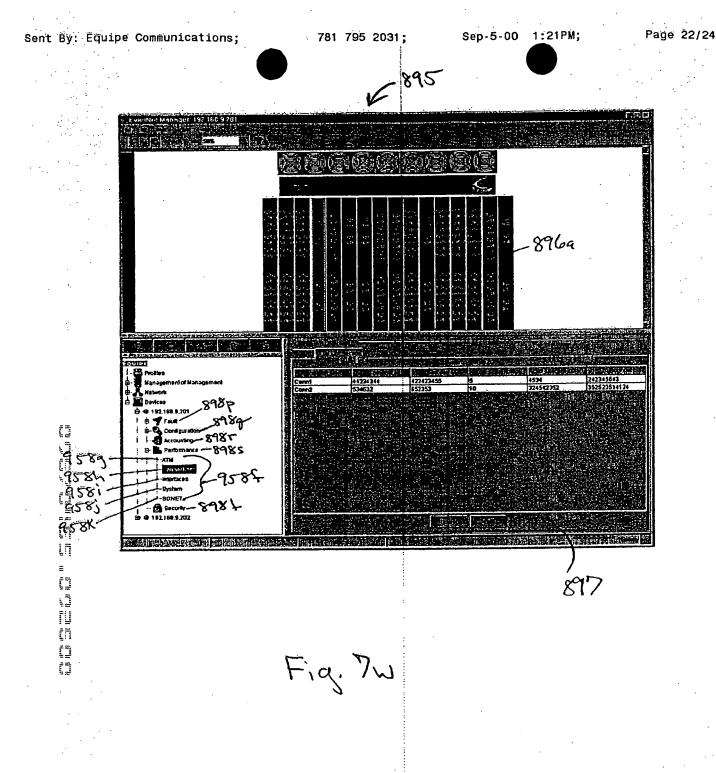


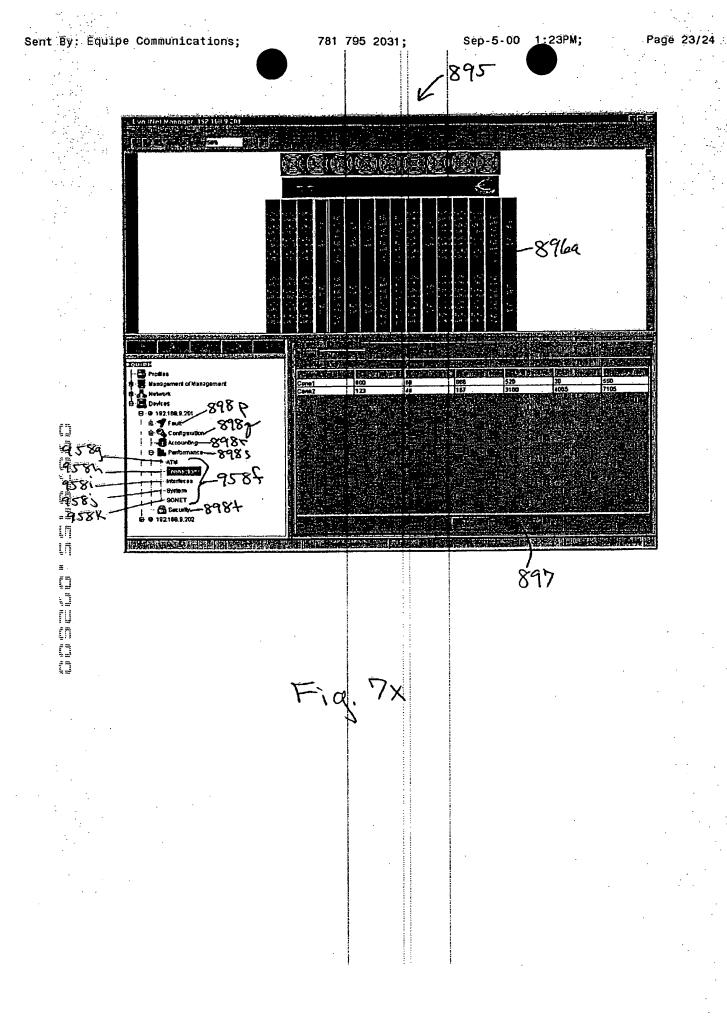
Fig. 7u

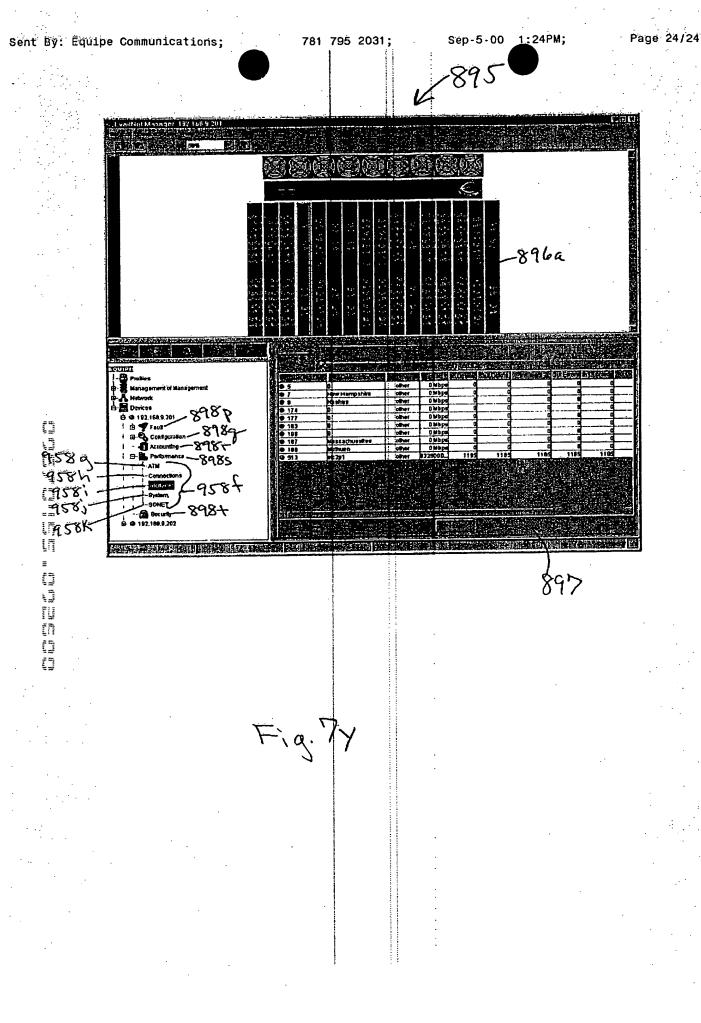
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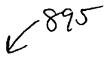


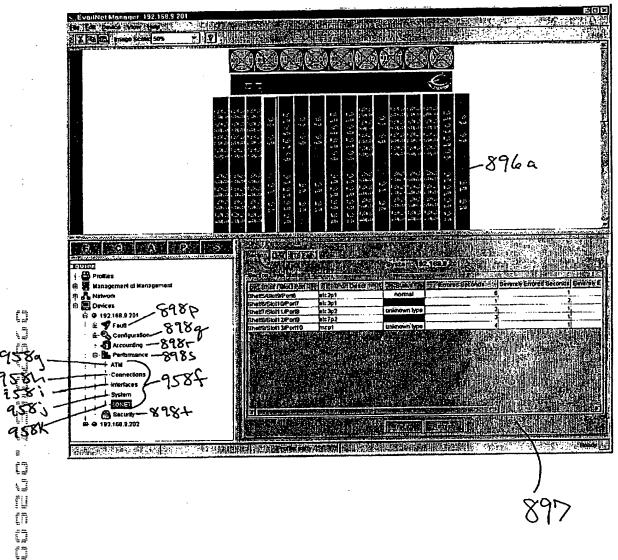
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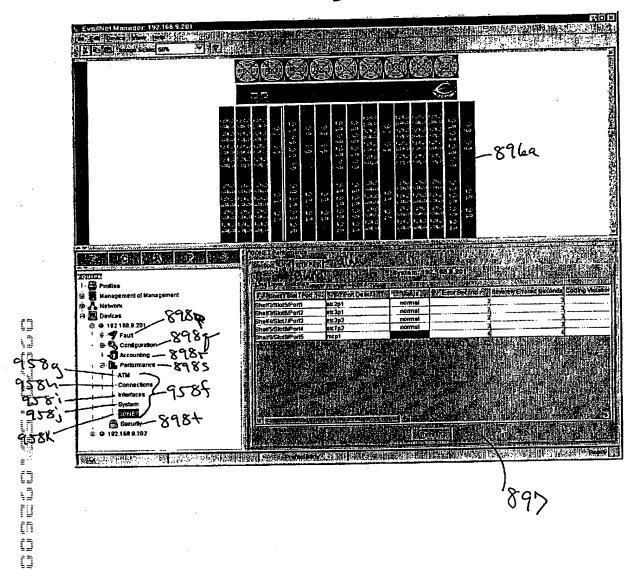




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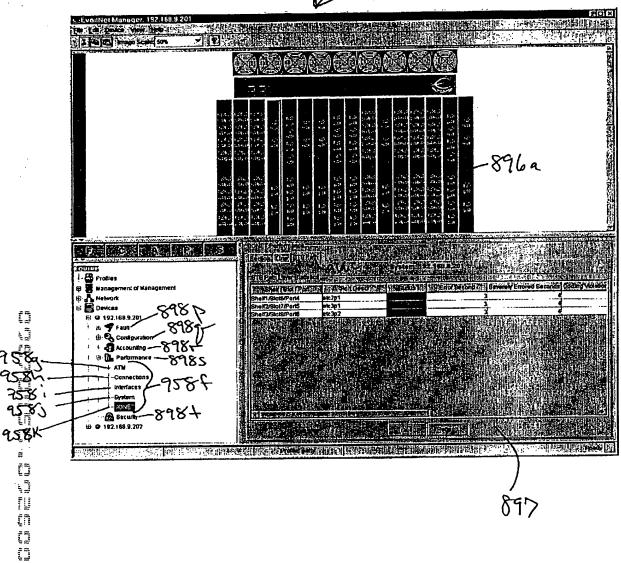
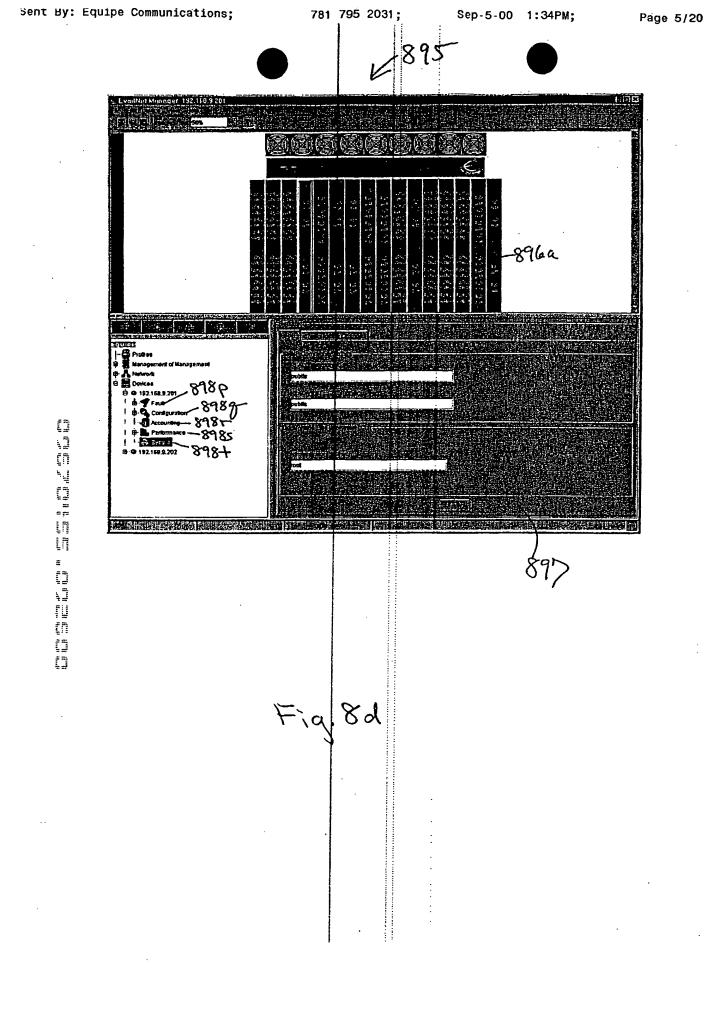
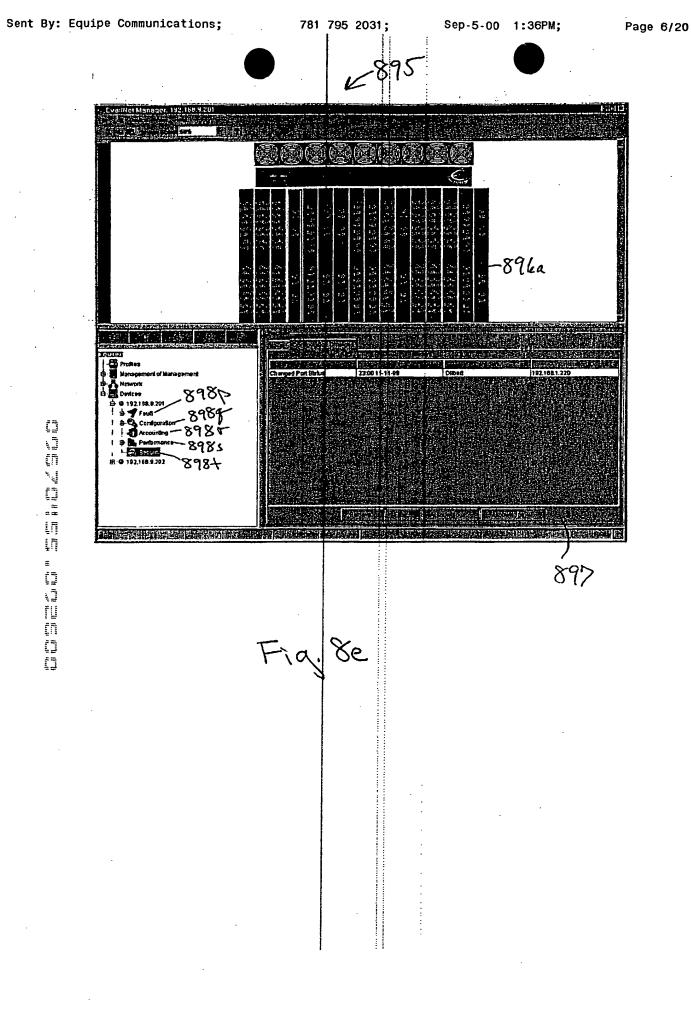
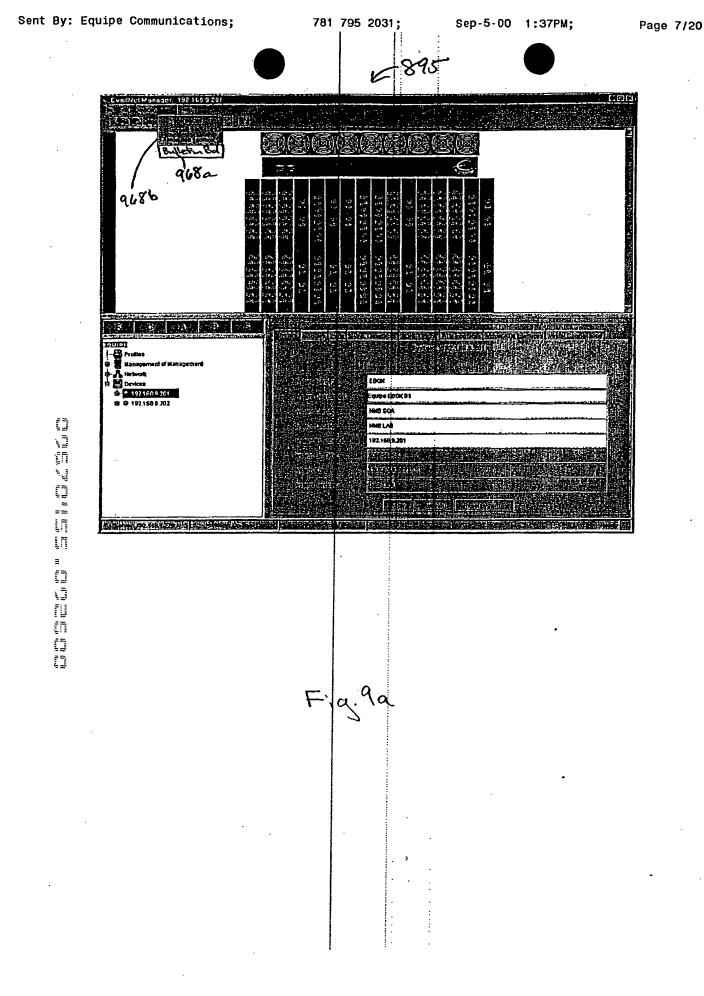
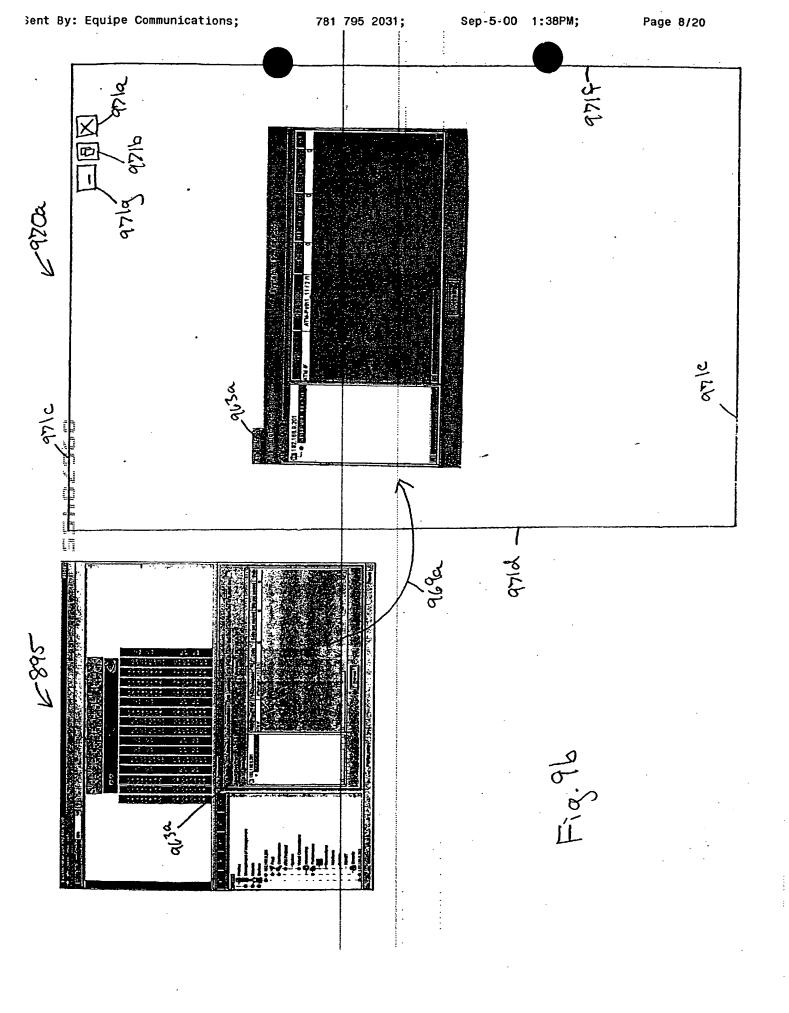


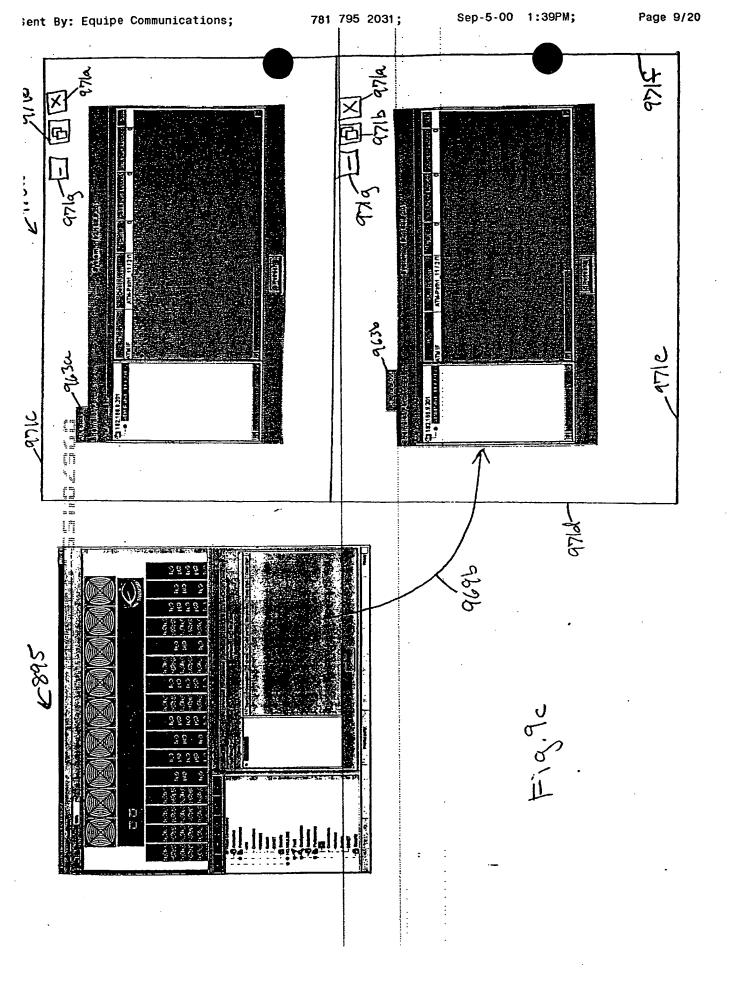
Fig. 8c

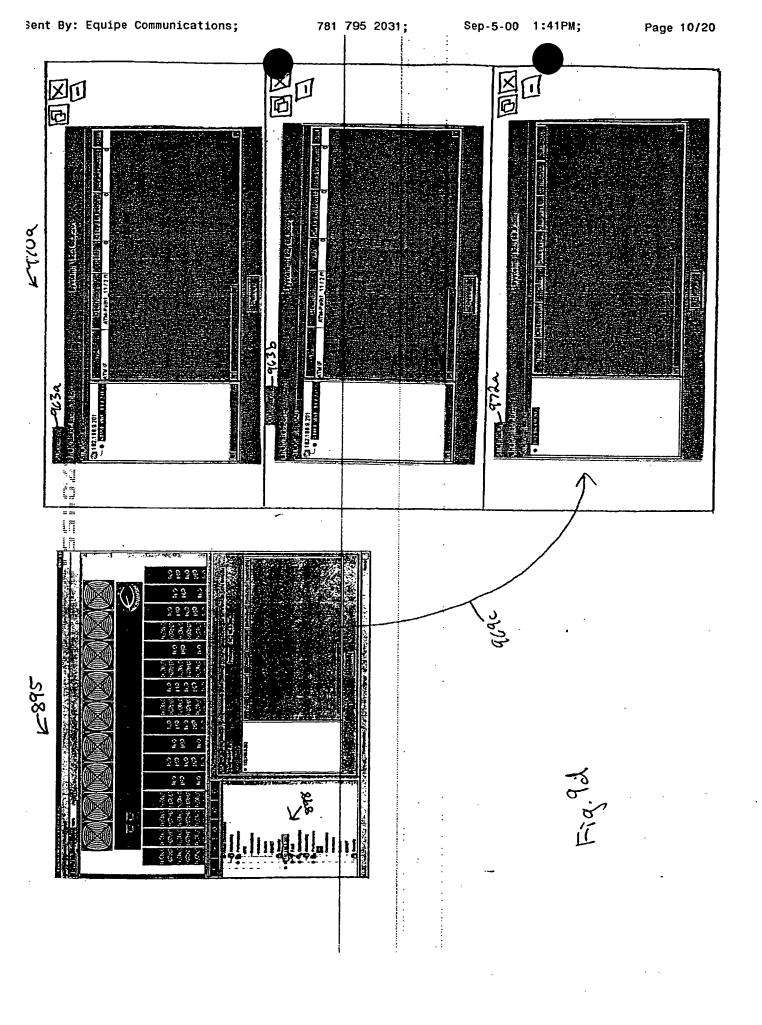


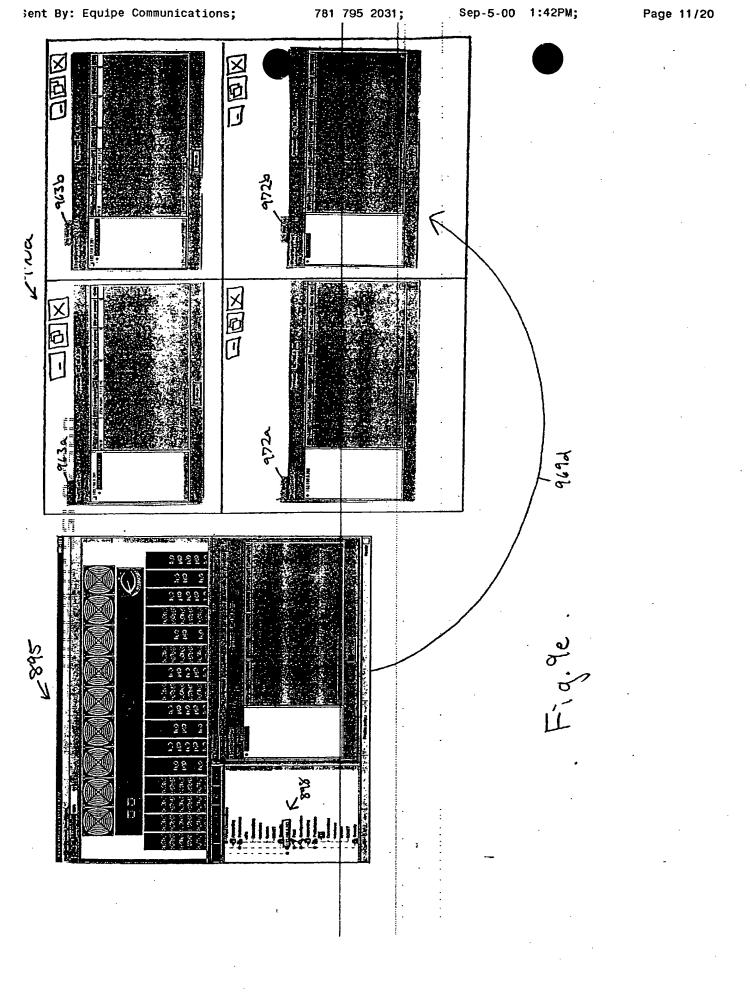


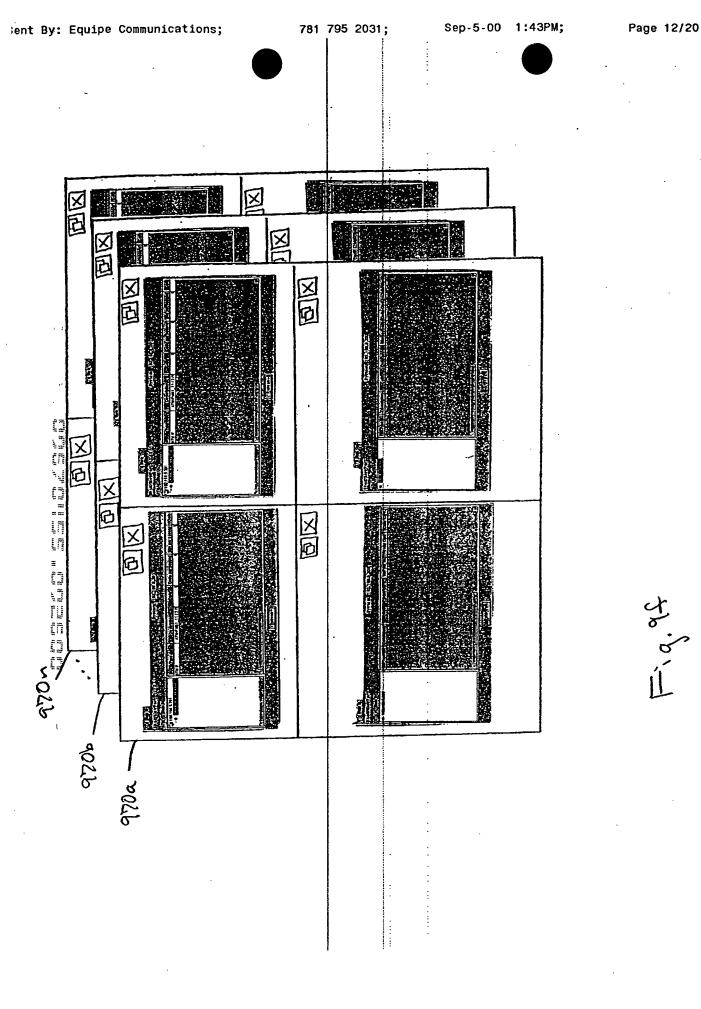


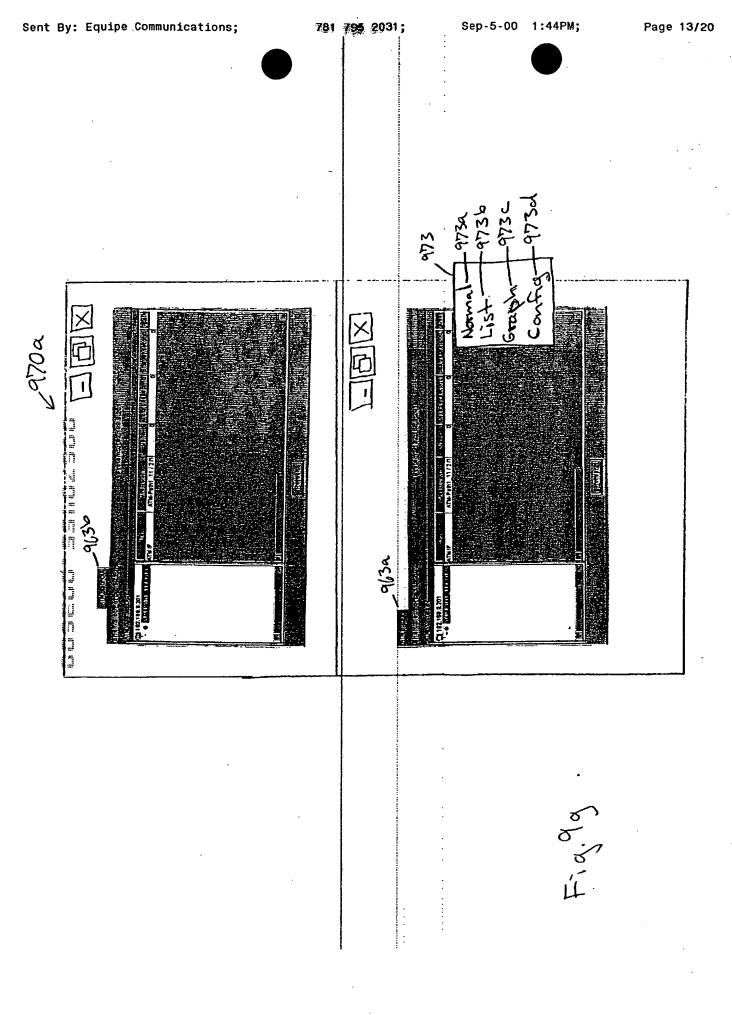


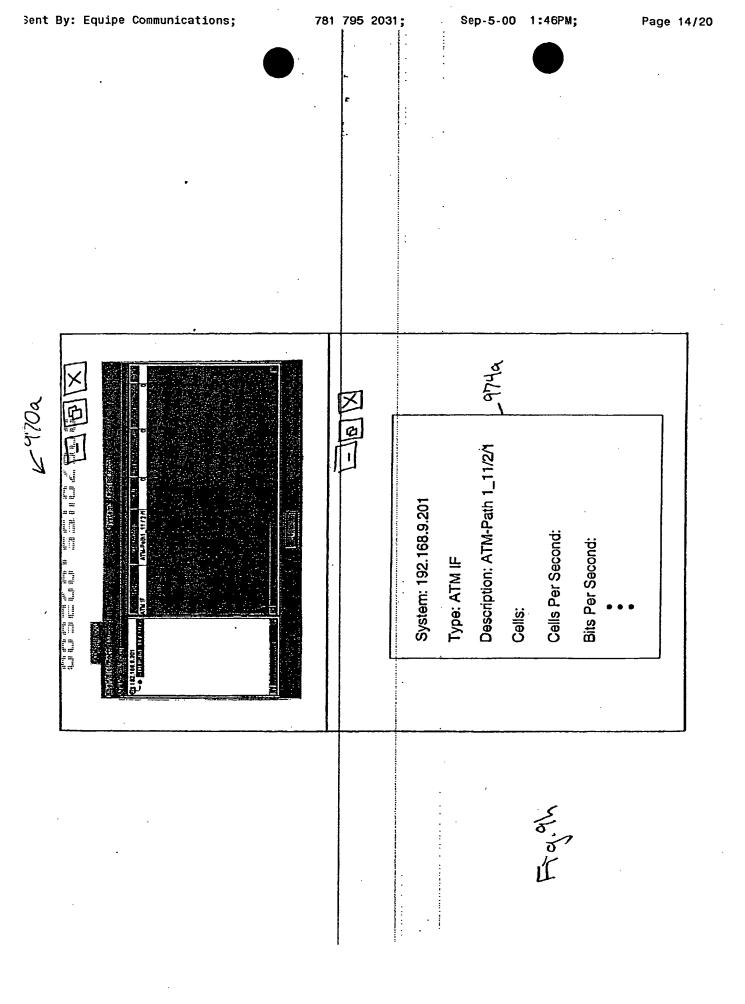


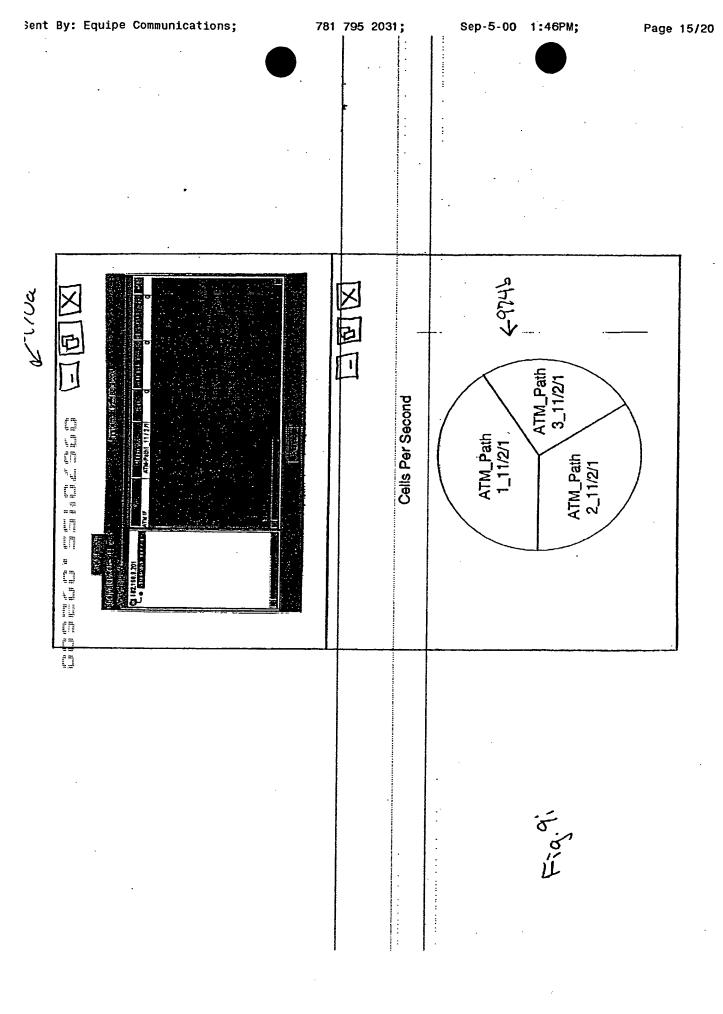


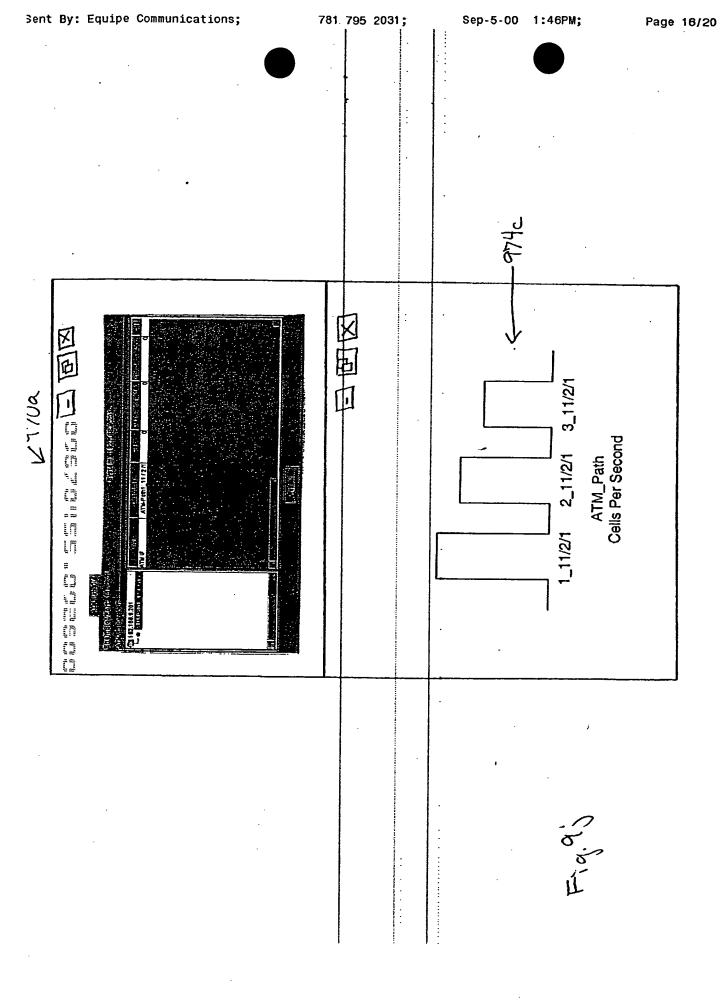


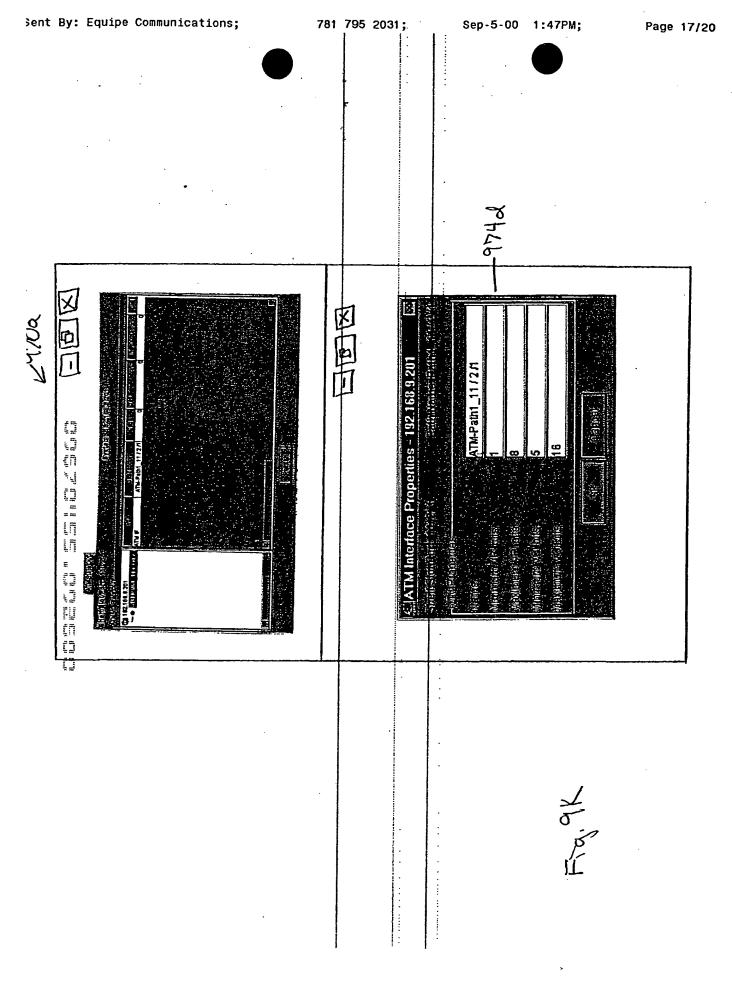


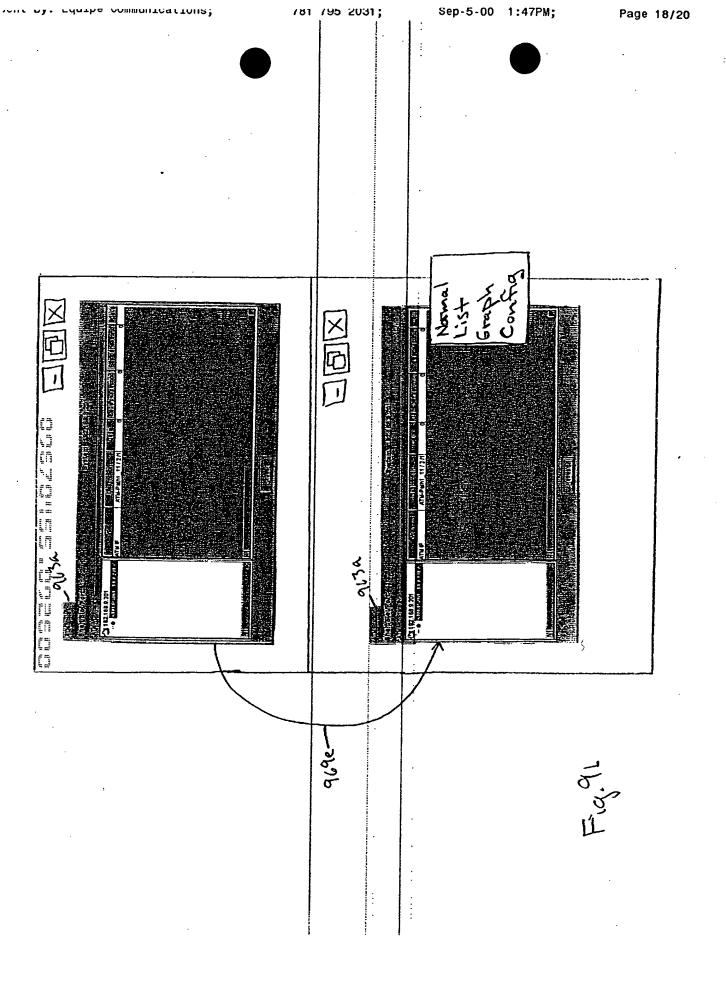


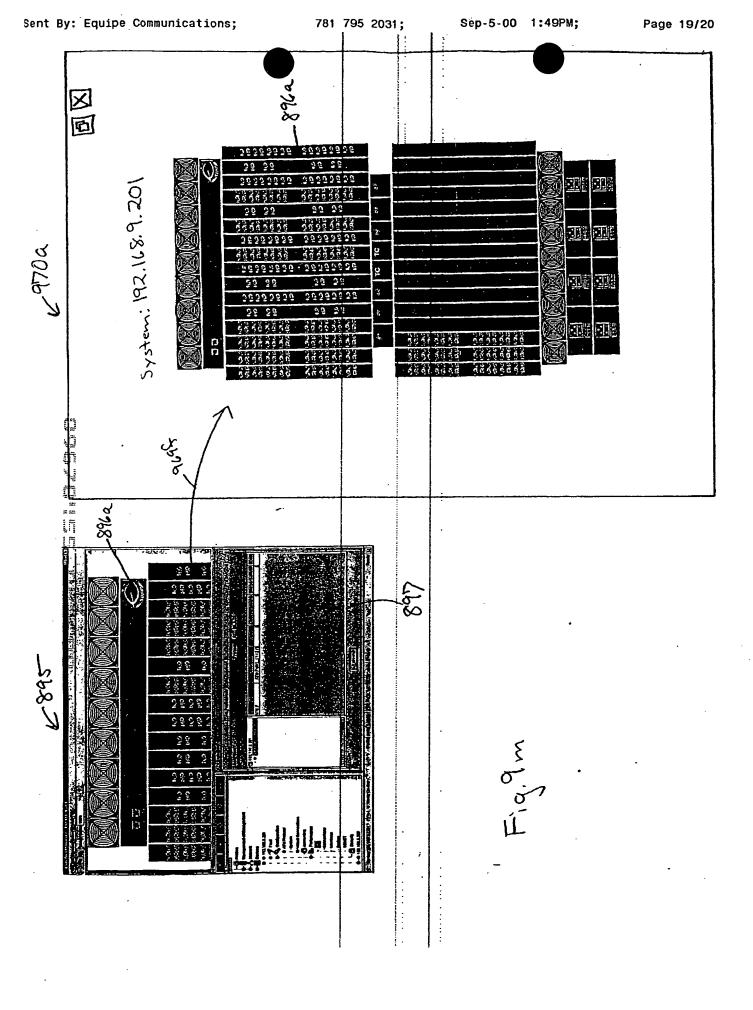


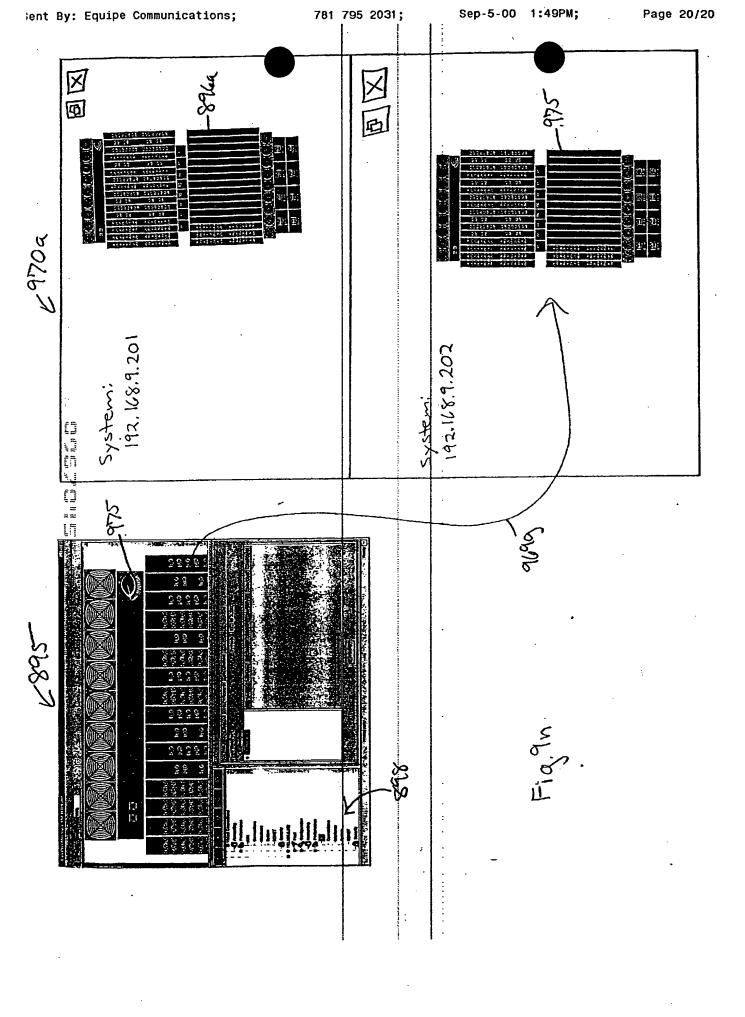


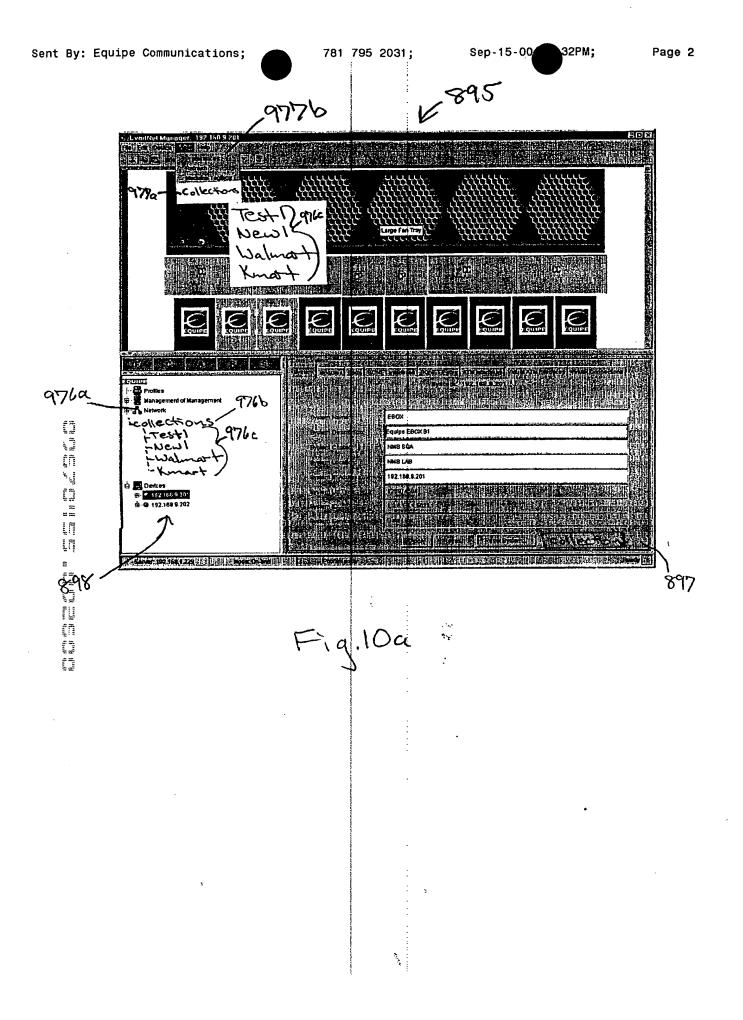












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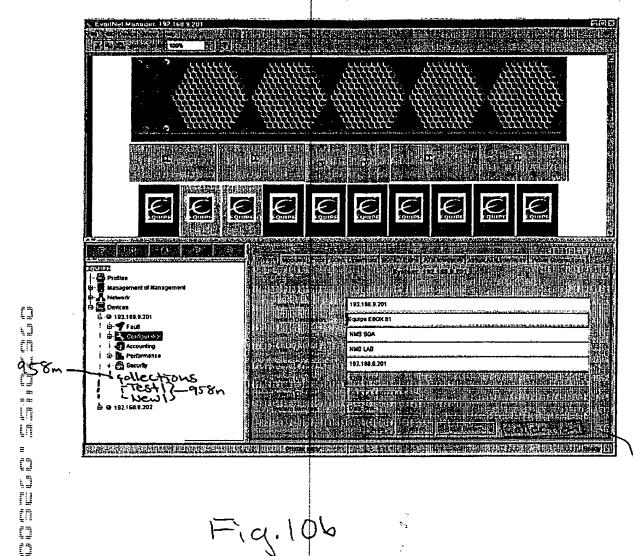
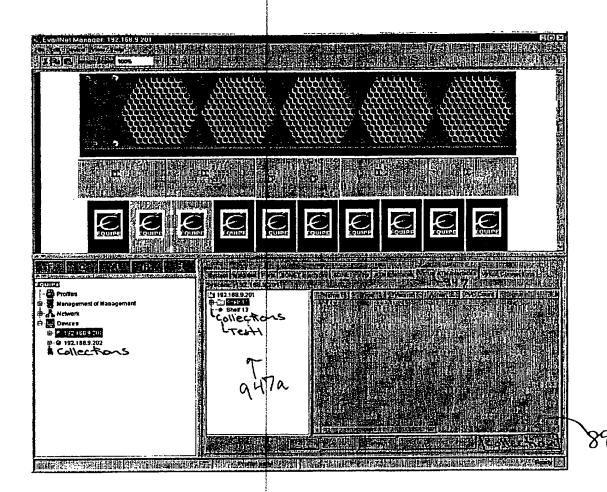


Fig. 106

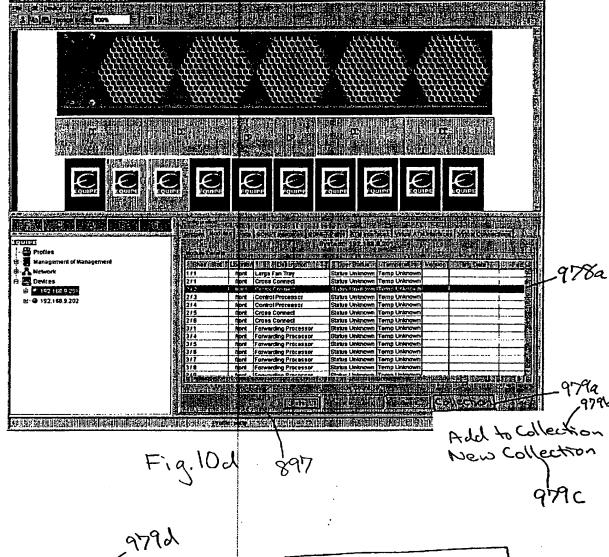


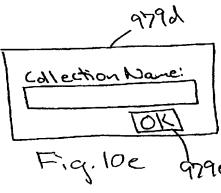
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Fig. 10c

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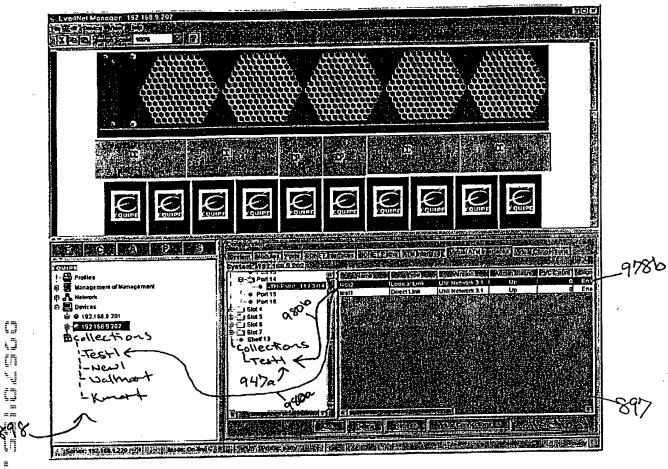


Fig. 109

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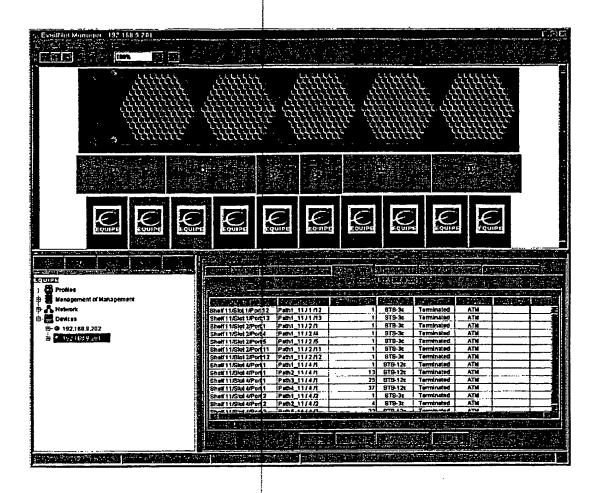
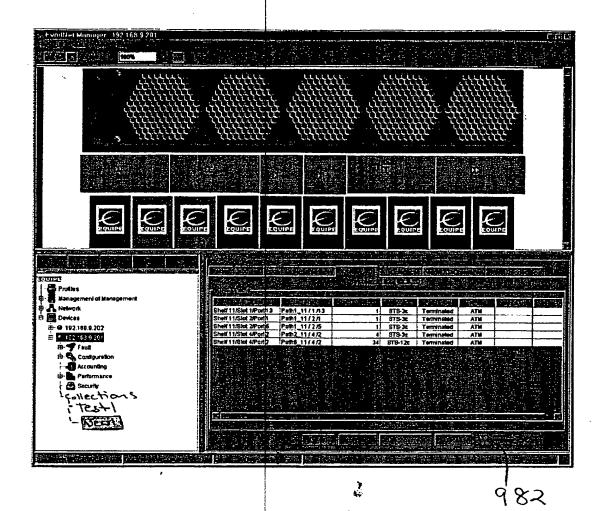
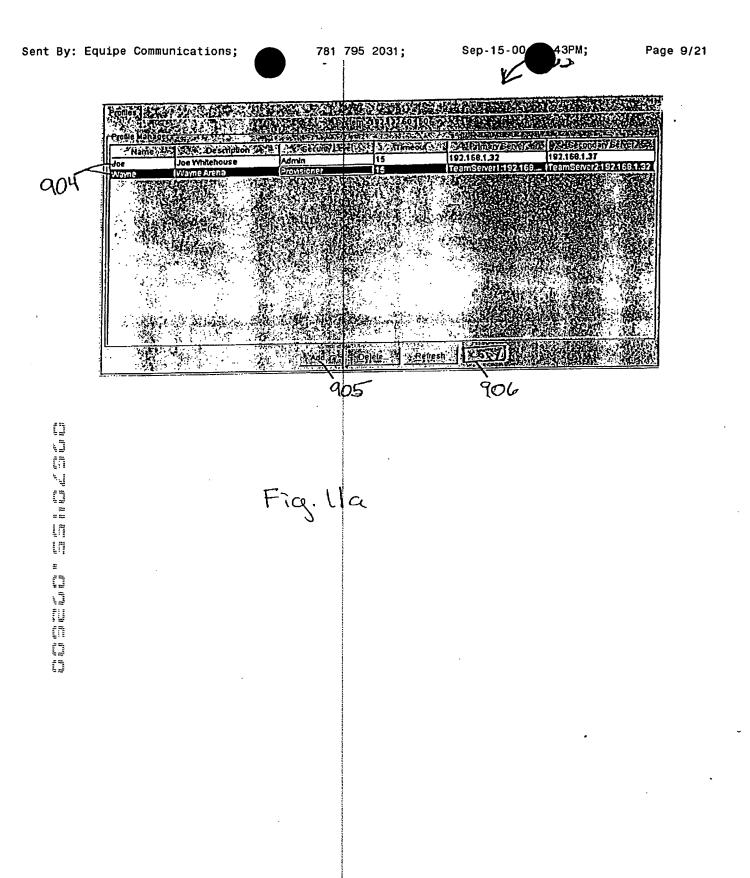


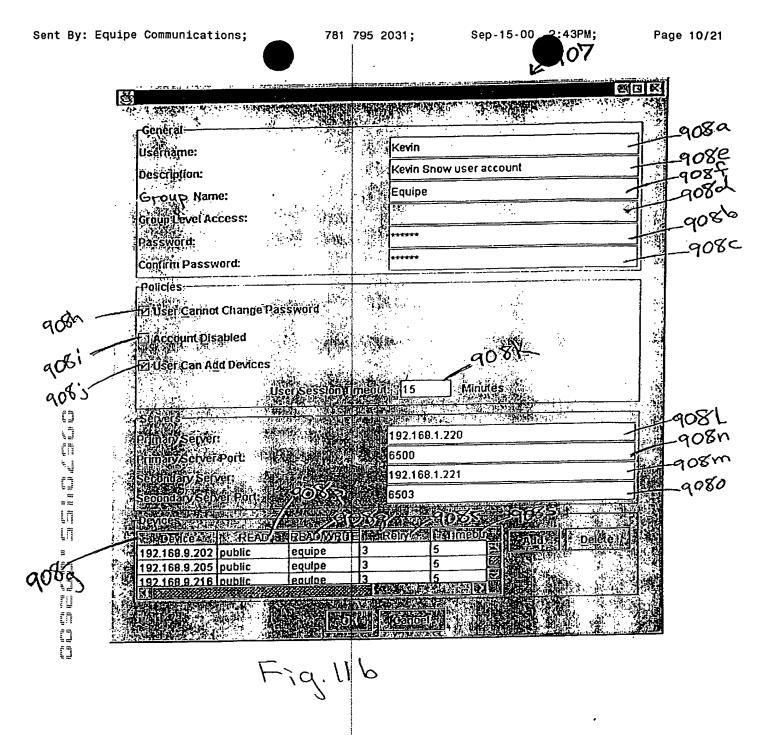
Fig. 10h



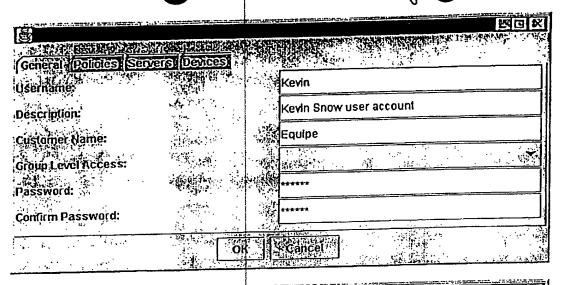
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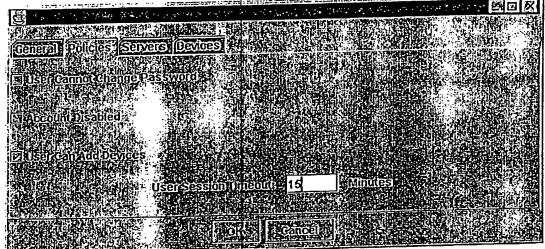


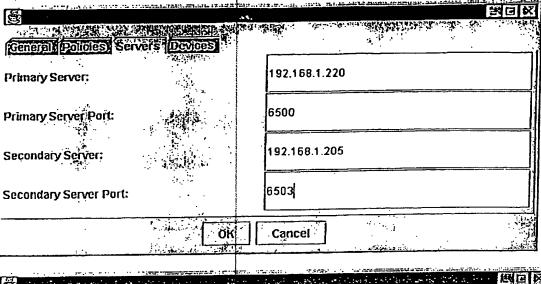
Fig. 111

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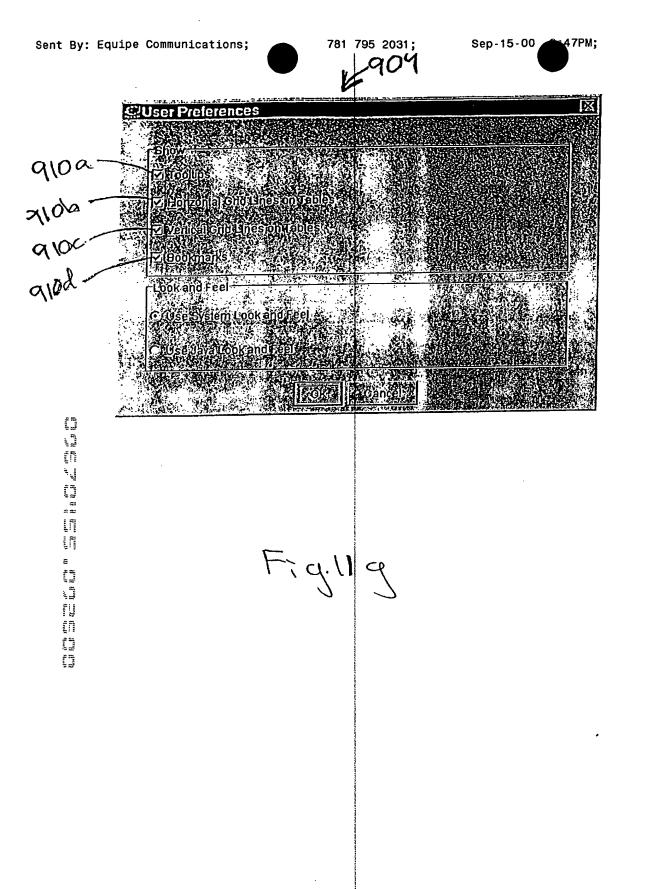
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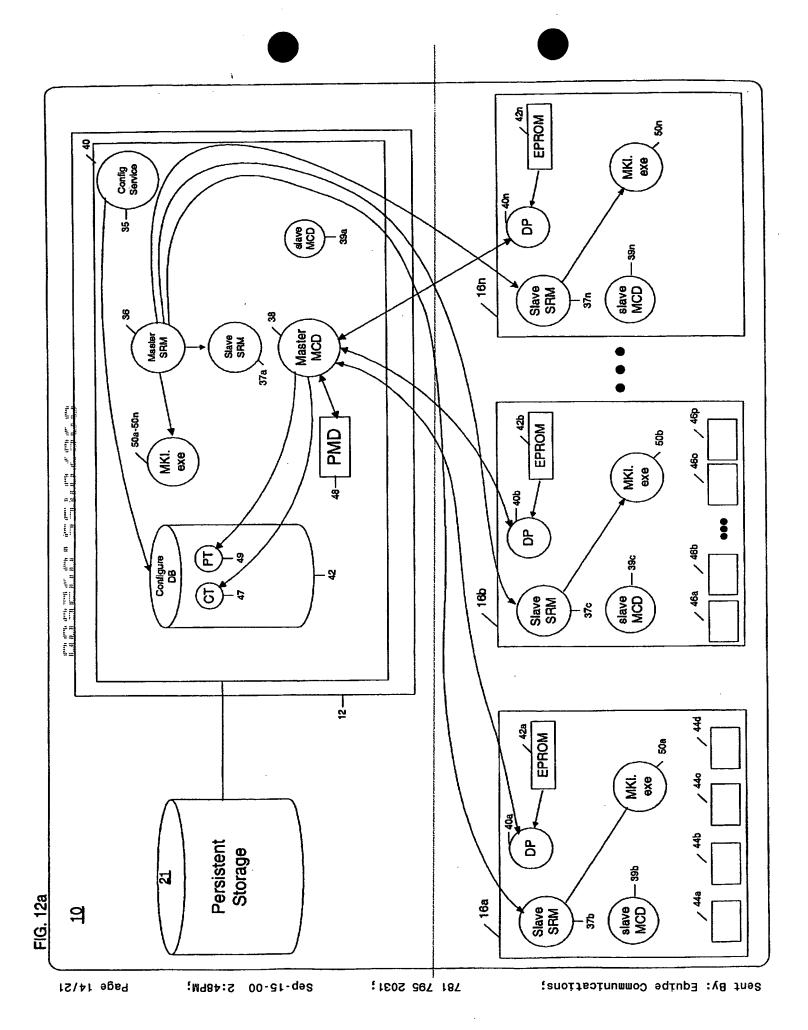


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Fig. 41f



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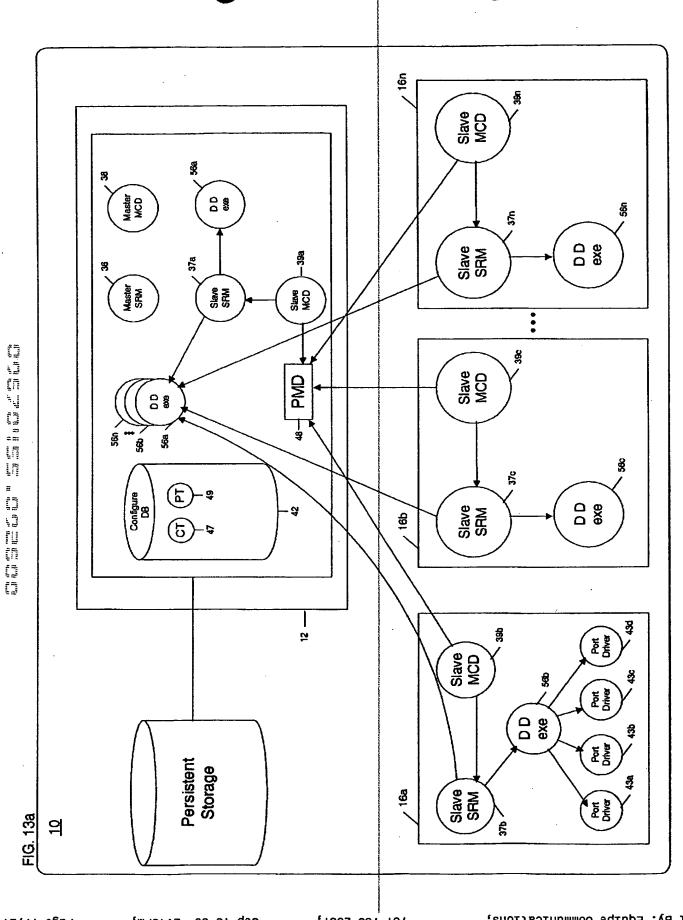


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	SLOT NO.	1	2	•	• •	•	5	•	••	12	•••	
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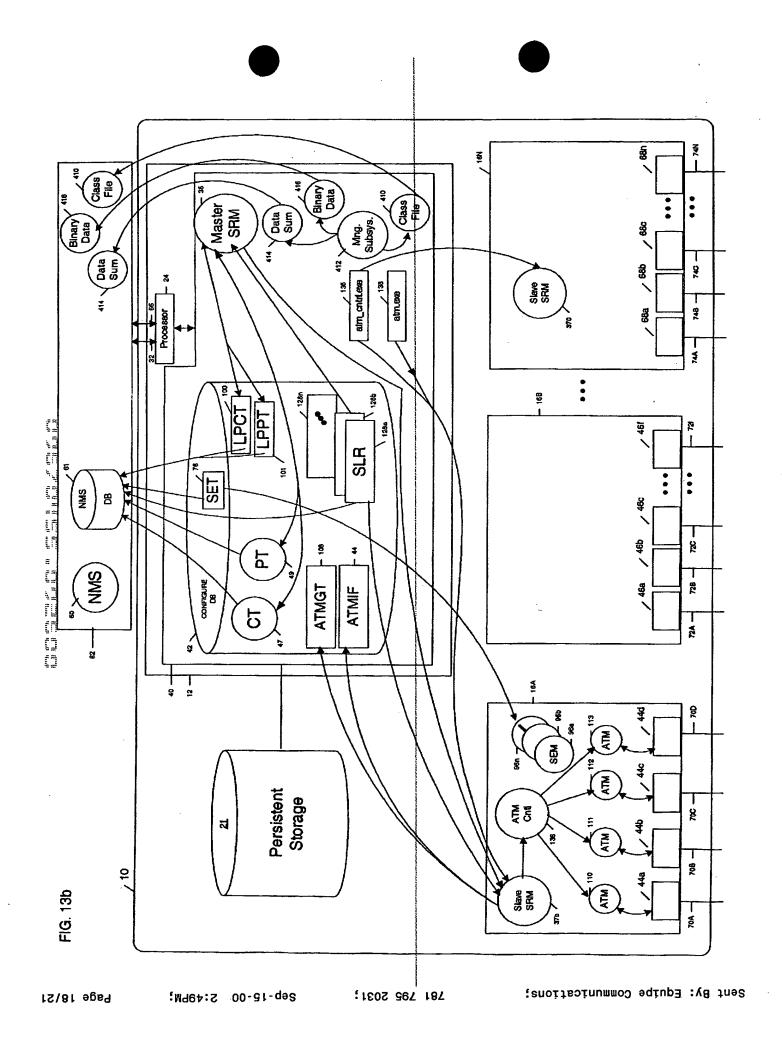
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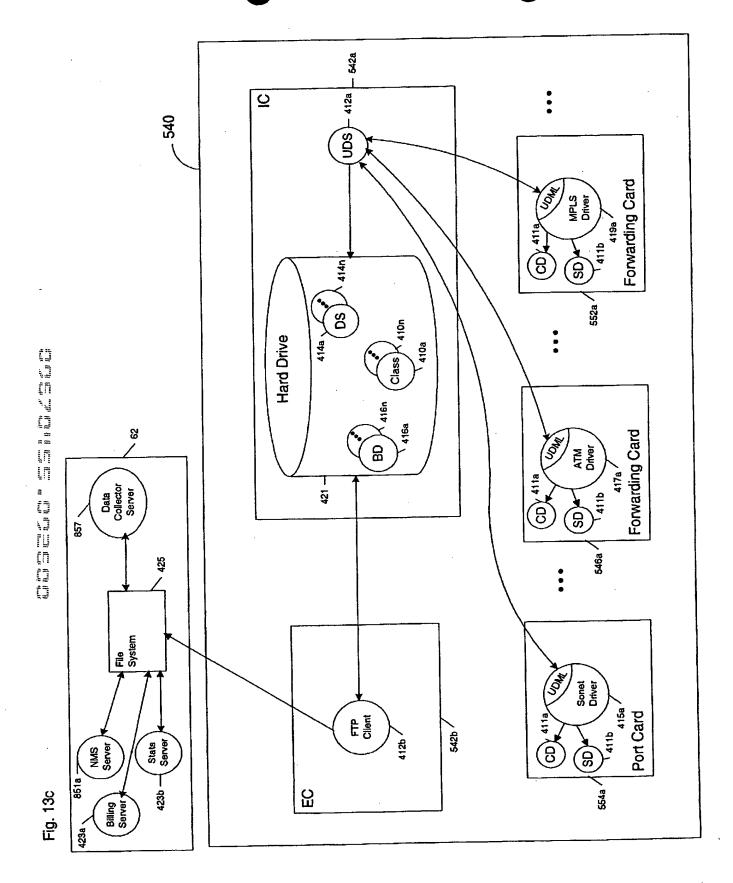
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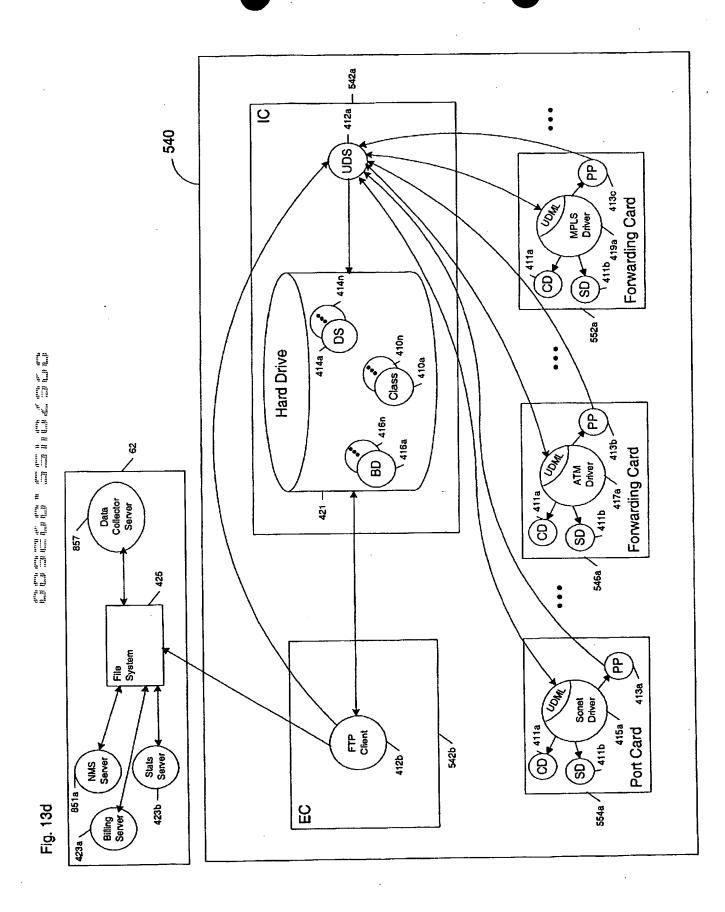


FIG. 14a

Service Endpoint Table 76

		 <u>.                                    </u>
	Servio Endpoir	Port PID
78	1	1500
80	2	1501
82 —	3	1501
84	4	1501
86	5	1502
88	6	1502
90	7	1503
92 —	8	1503
94 —	9	1503
168	10	1502
	•	. :

FIG. 14b

Logical to Physical Card Table 100

	98		102	104
		Pi	imary	Back-up
	LID		PID	PID
106	30		500	513
109	31		501	513
ĺ	į		•	•

FIG. 14c

Logical to Physical Port Table 101

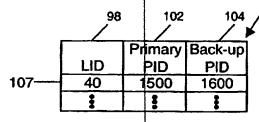




FIG. 14d

ATM Group Table 108

		K
Group	Card	
#	LID	
1	30	
2	30	
3	30	
4	30	

FIG. 14e

1.74 (T.1) (L.1) (L.1)

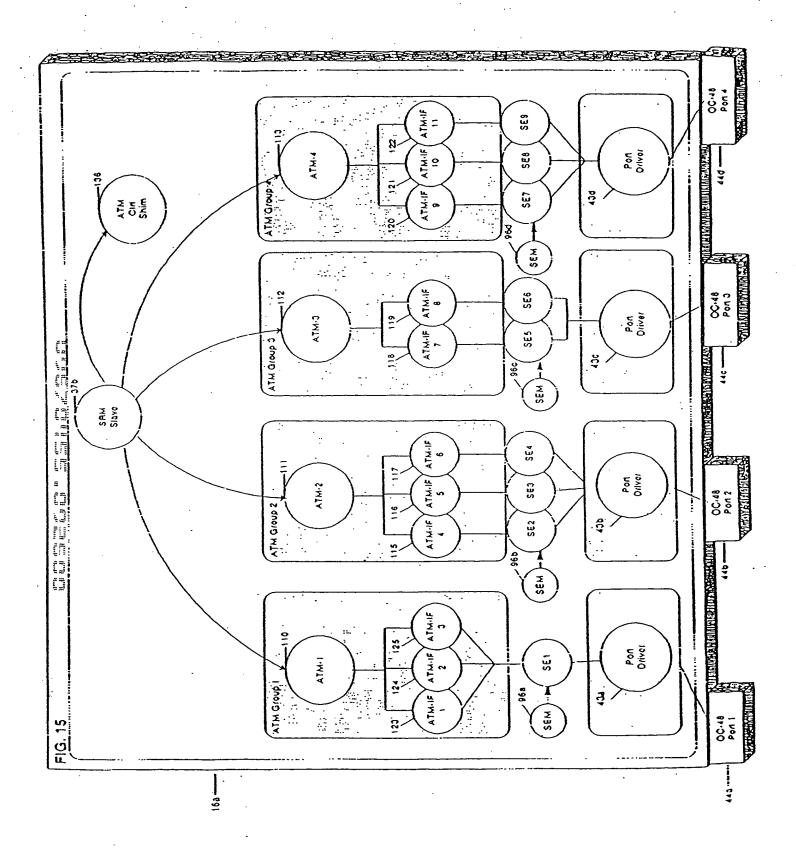
ATM Interface Table 114

_		l l	₩	
	ATM IF	ATM Group	SE	•••
	1	1	1	
	2	1	1	
	3	1	1	
	4	2	2	
	5	2	3	
- 1	6	2	4	
			•	1
170	12	3	10	
	•	•	<u> </u>	ŧ
170	6 12	3	4	

FIG. 14f

Software Load Record 128a

			<b>F</b>	
130-	Control Sh	im	LID	132
134	atm-cntl.	е	30	



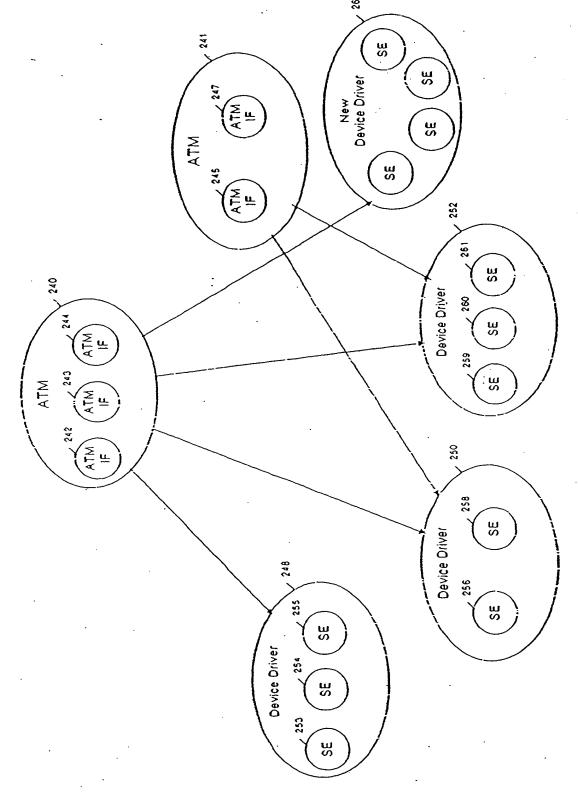
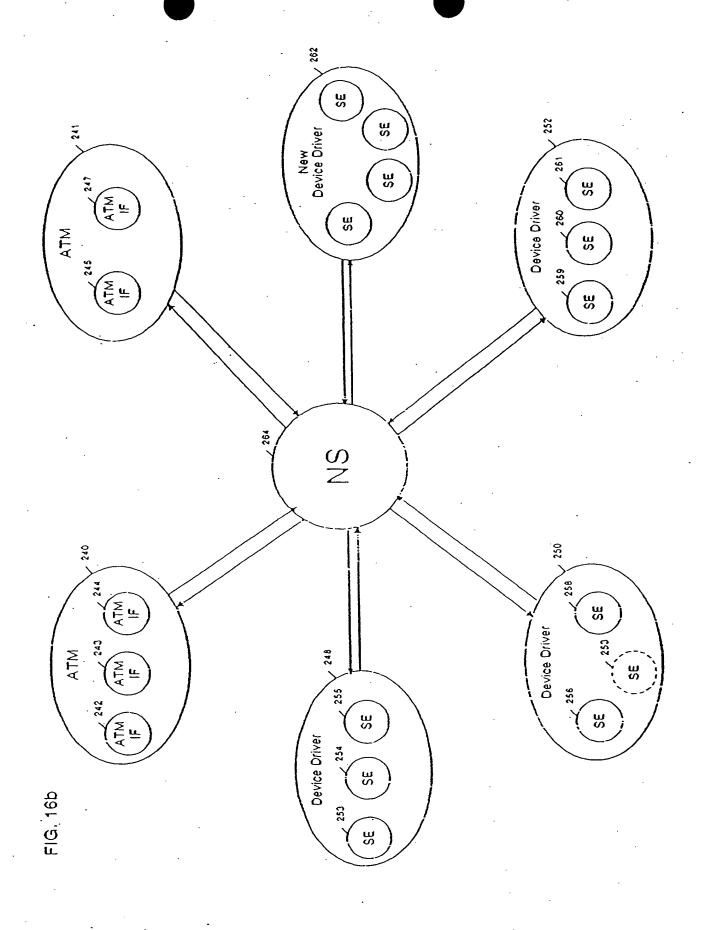
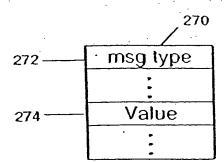
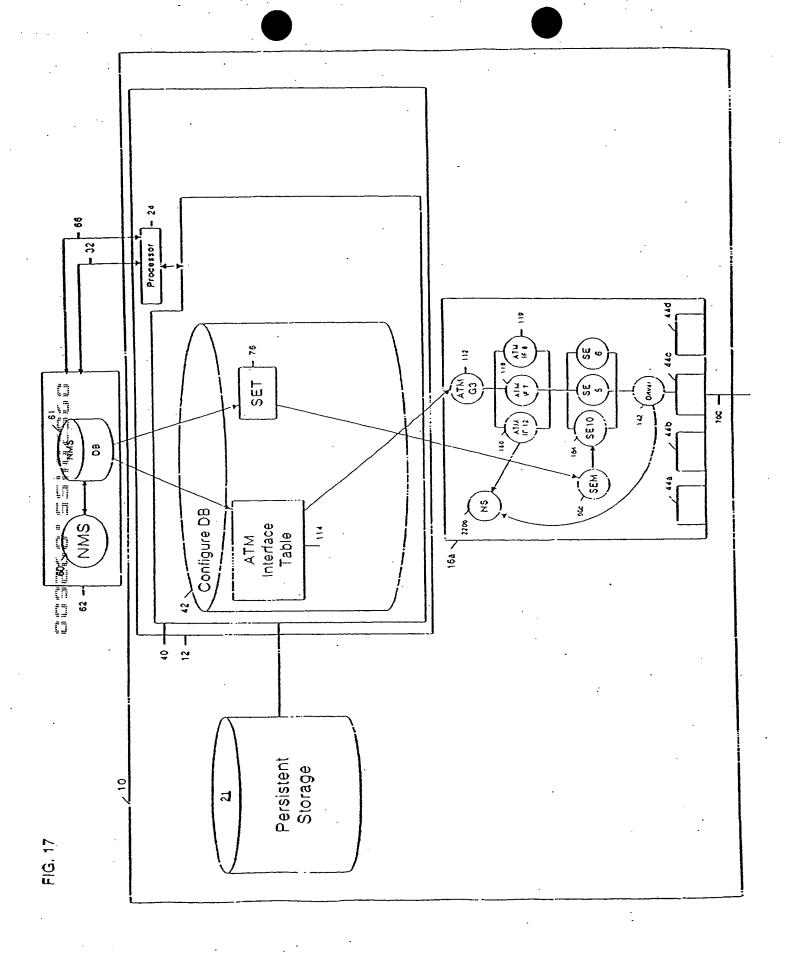


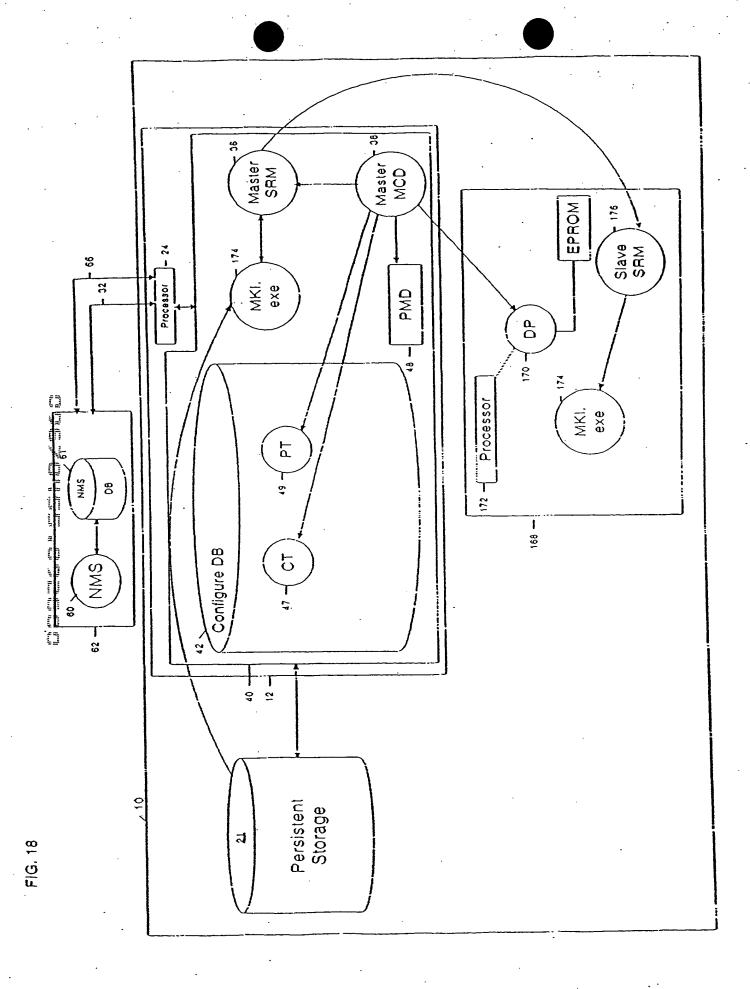
FIG. 16a

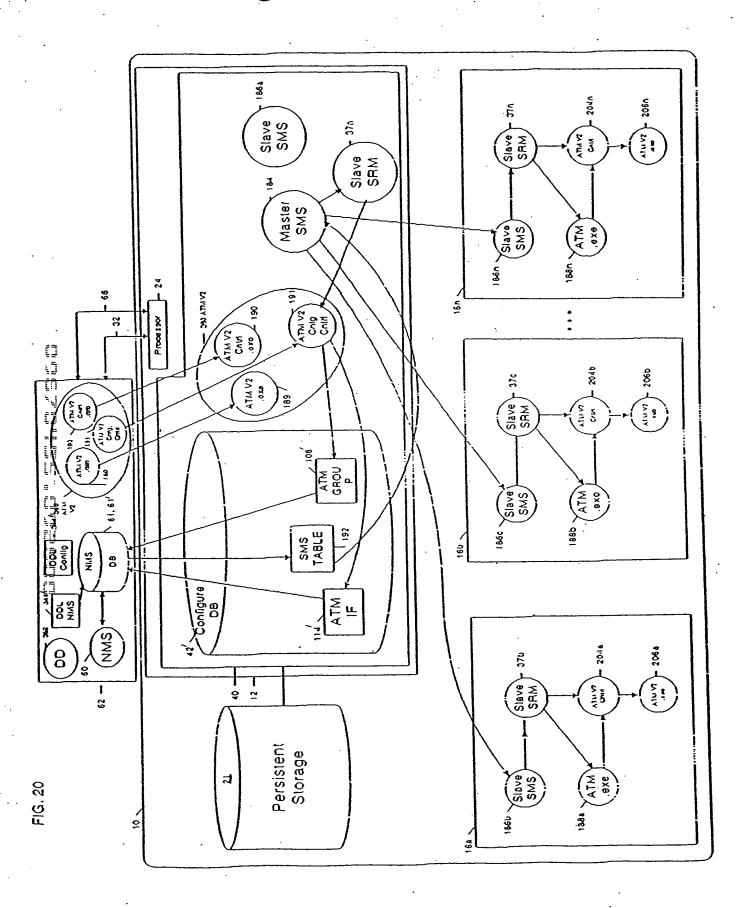


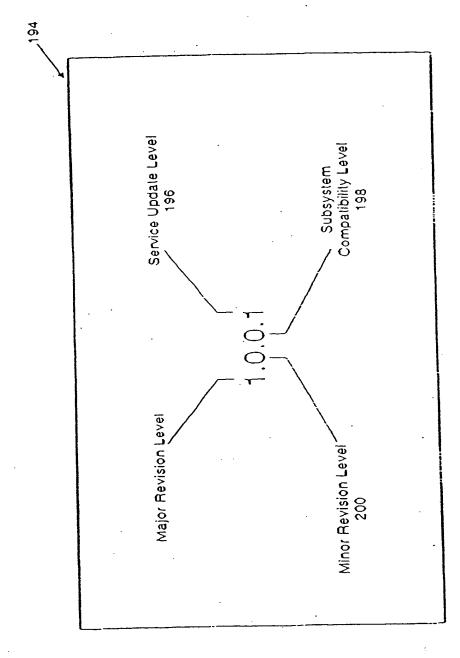


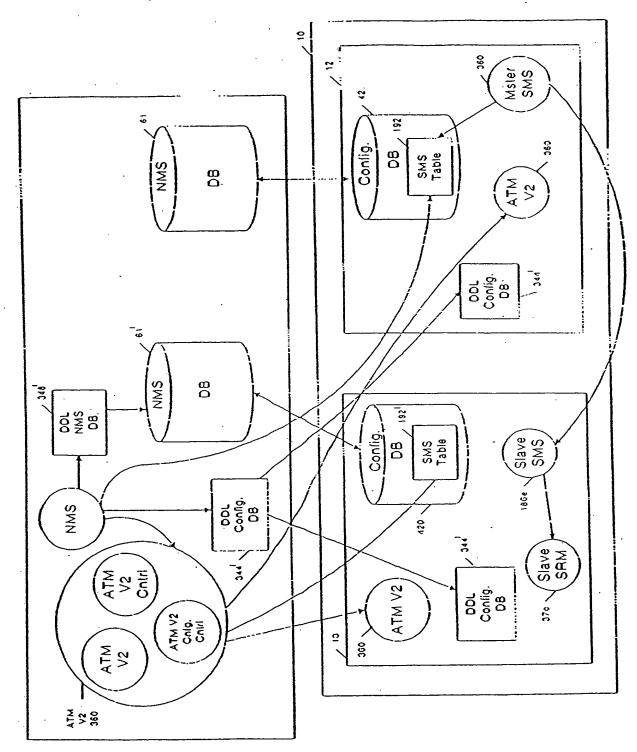
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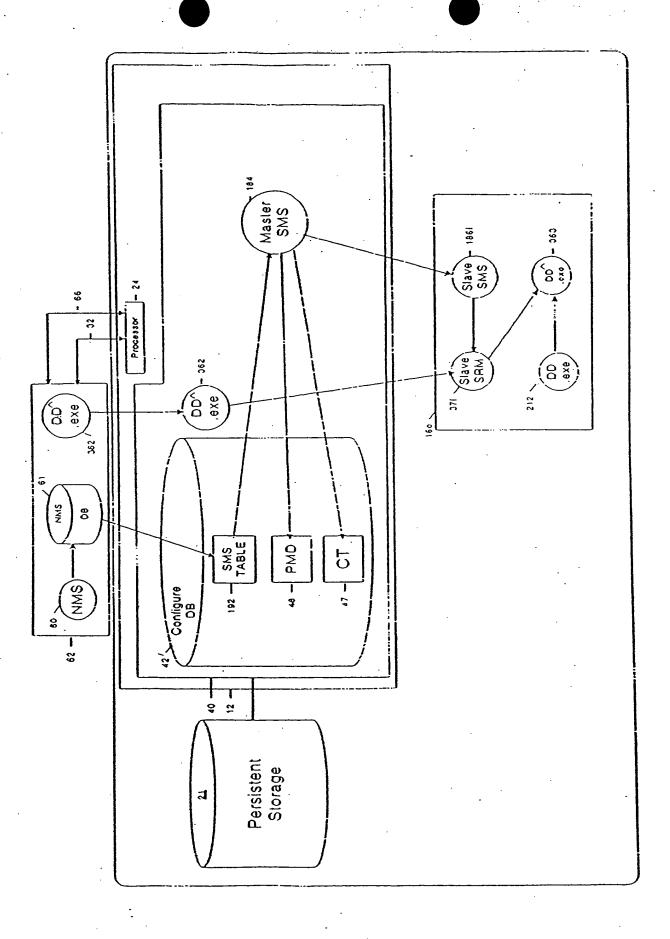


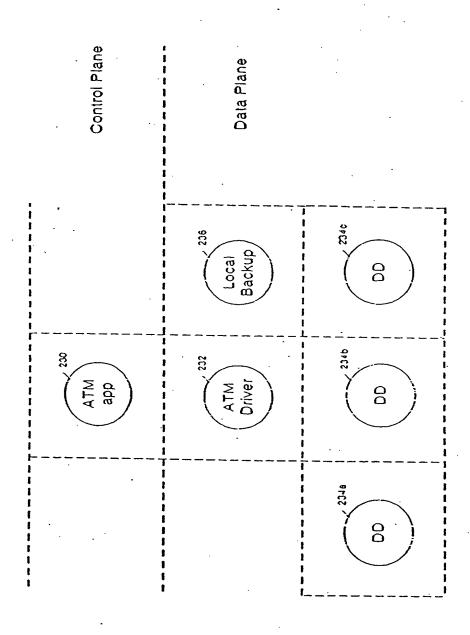


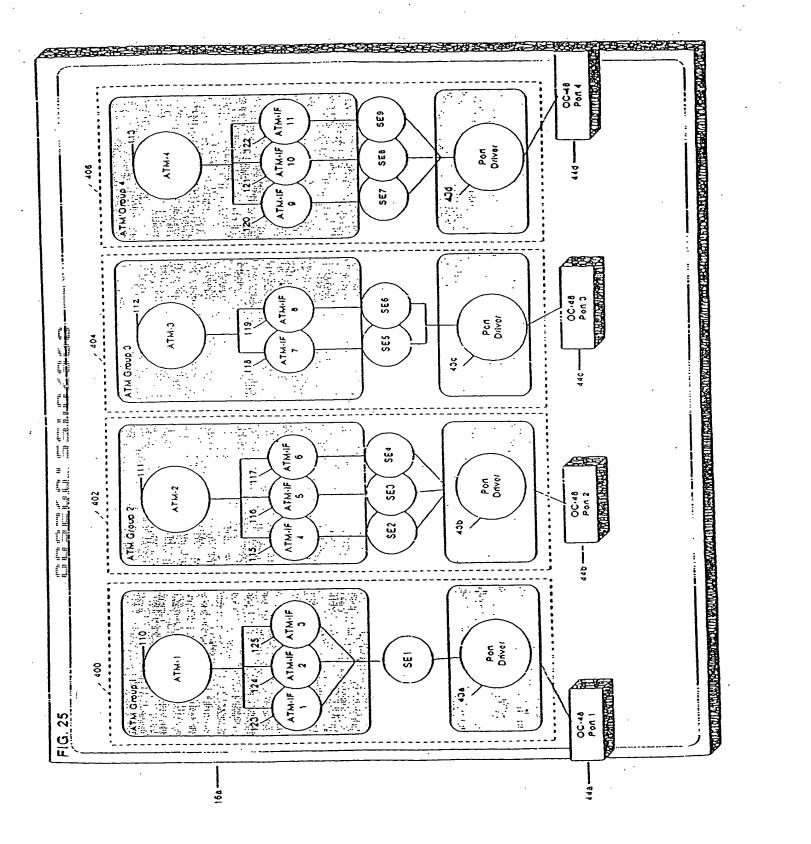


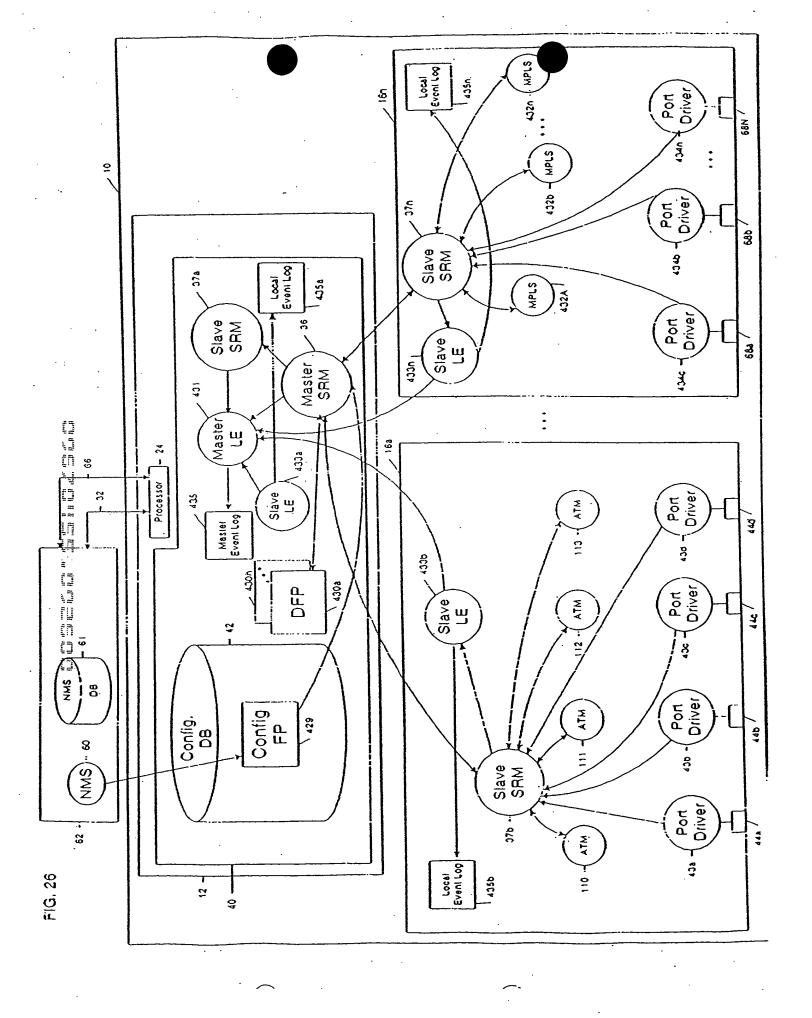


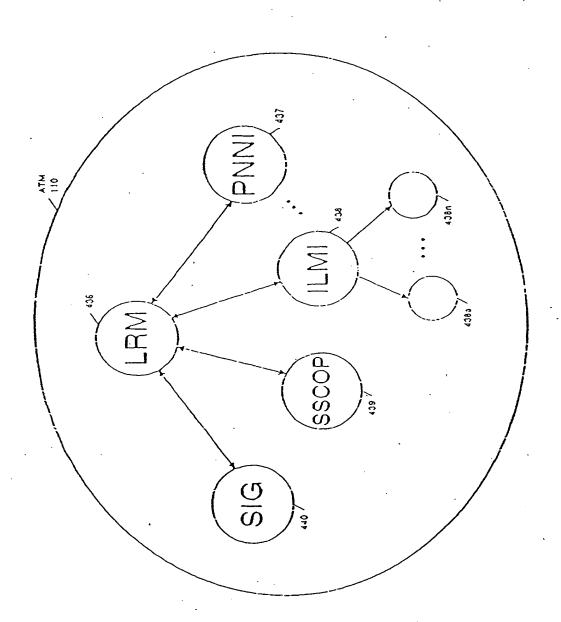


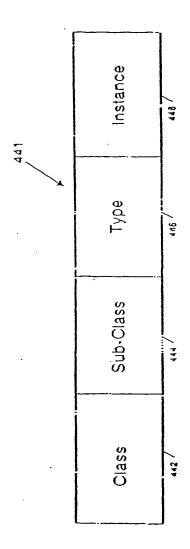


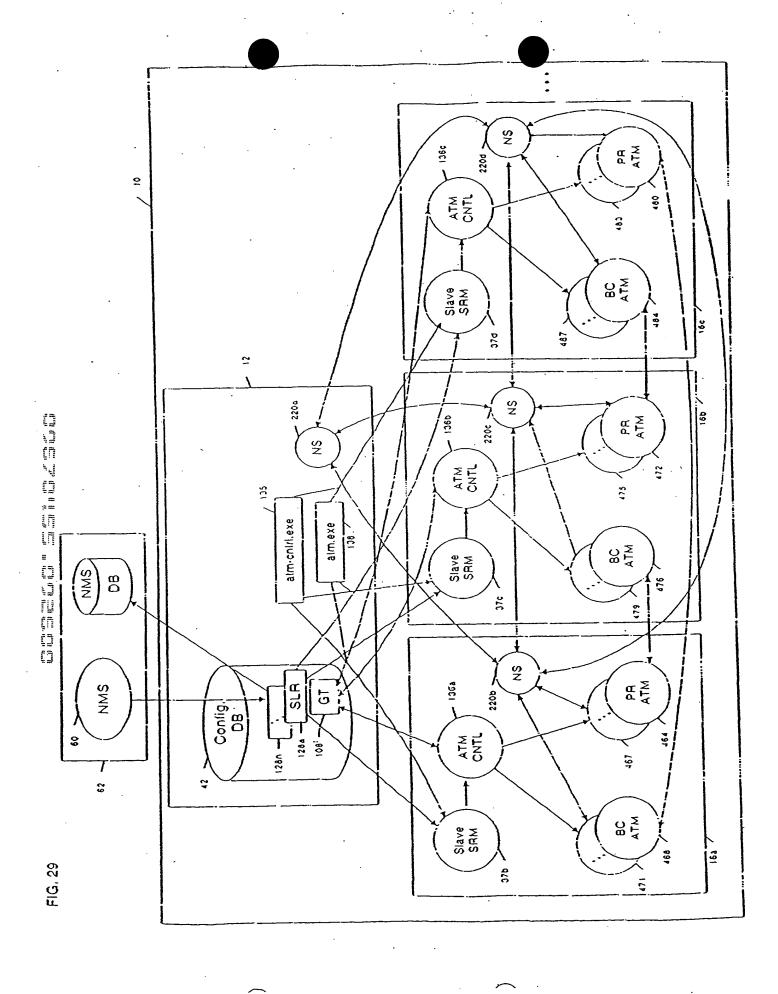












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oup Table 108'							
	•	447					
	·						
	Group	Primary	Backup				
	#	Card LID	Card	•••			
			LID				
450 —	1	30	31				
451 —	2	30	31				
452 —	3	30	31				
453	4	30	31				
454 —	5	31	32				
455 —	6	31	32				
456 —	7	31	32				
457 —	8	31	32				
458 —	9	32	30				
459 —	10	32	30				
460	11	32	30				
461	12	32	30				
		•	•				
•	·	<u> </u>	•	•			

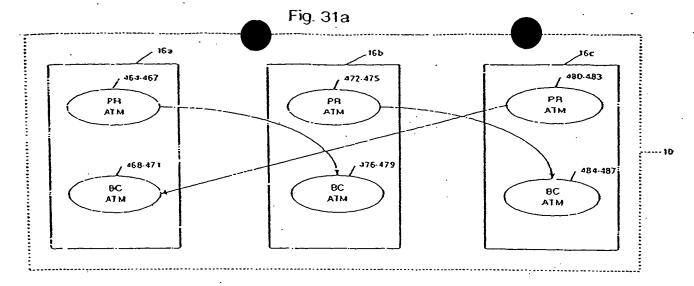
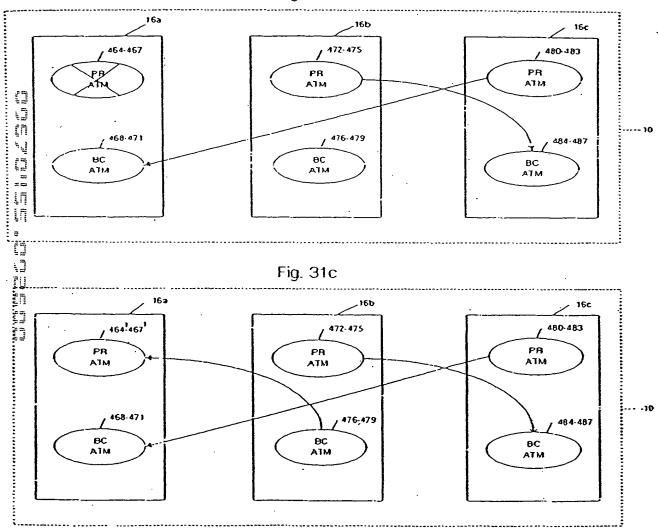


Fig. 31b



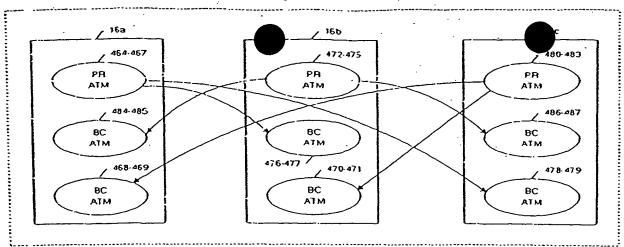
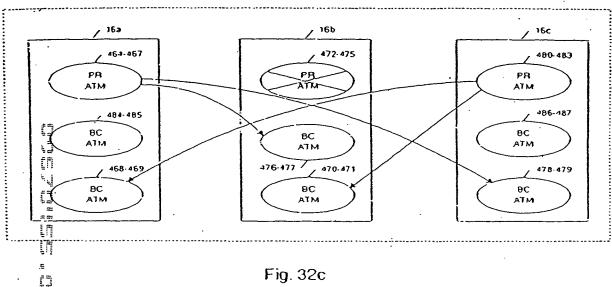
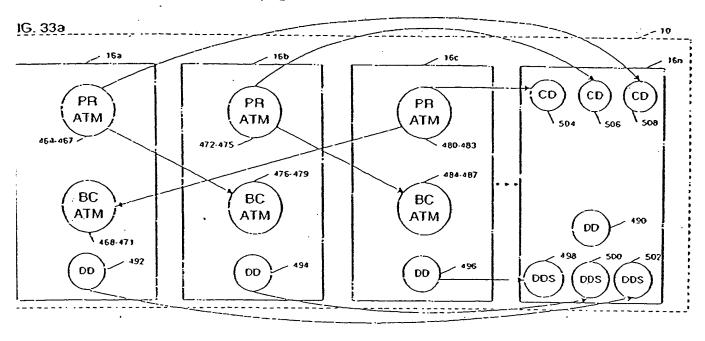
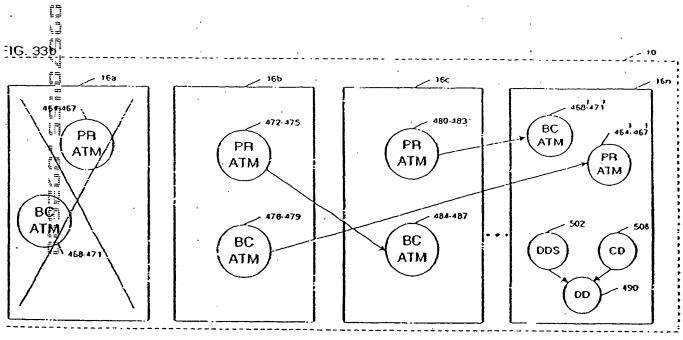


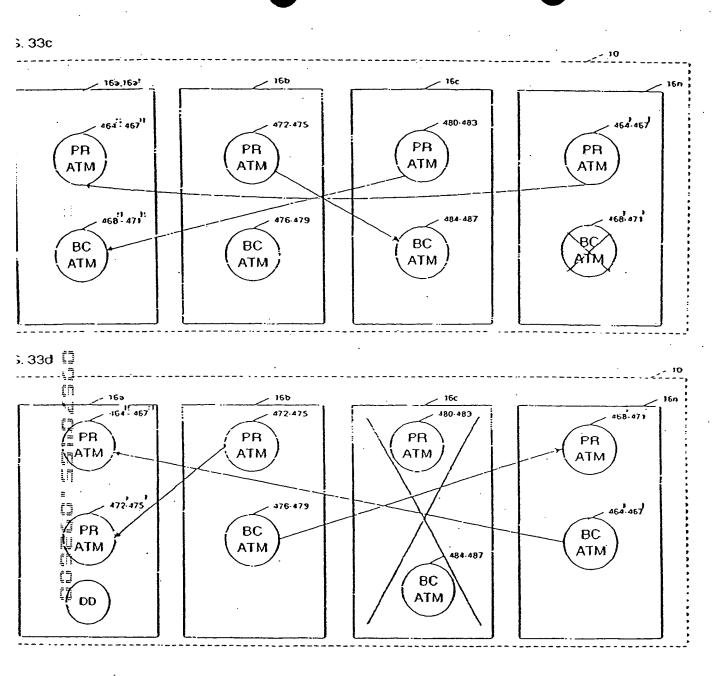
Fig. 32b



Įħ. 464-467 472-475 480-483 PR PR PR MTA ATM ل<sup>486-487</sup> ∠ <sup>484-485</sup> BC ATM BC BC ATM ATM 468-469 470-471 478-479 BC ATM DC вс ATM ATM







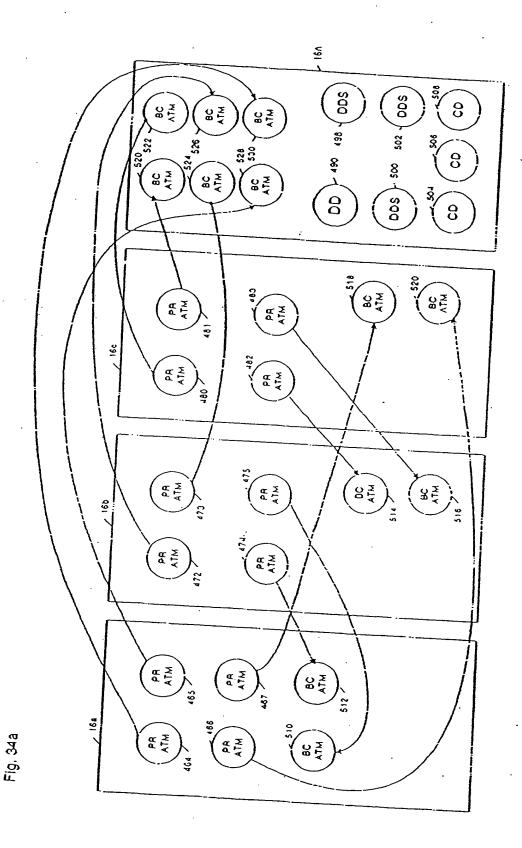
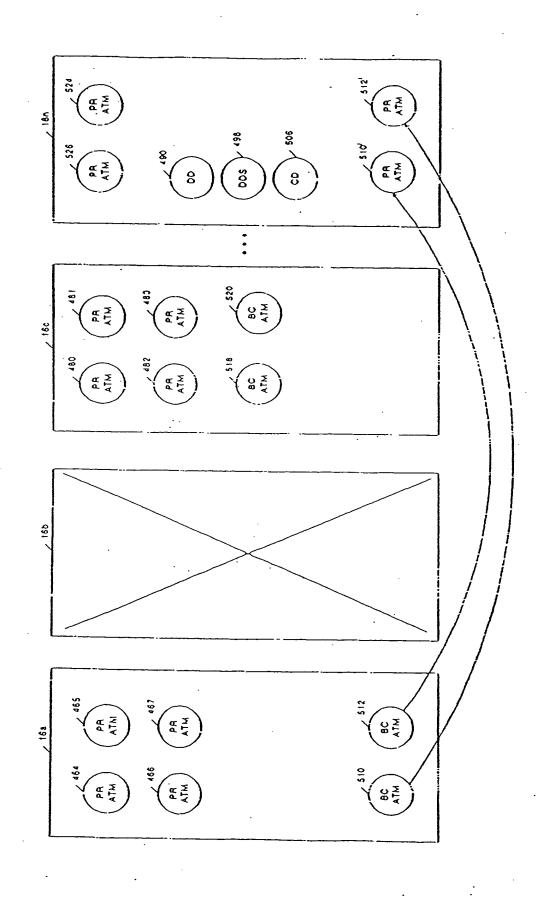
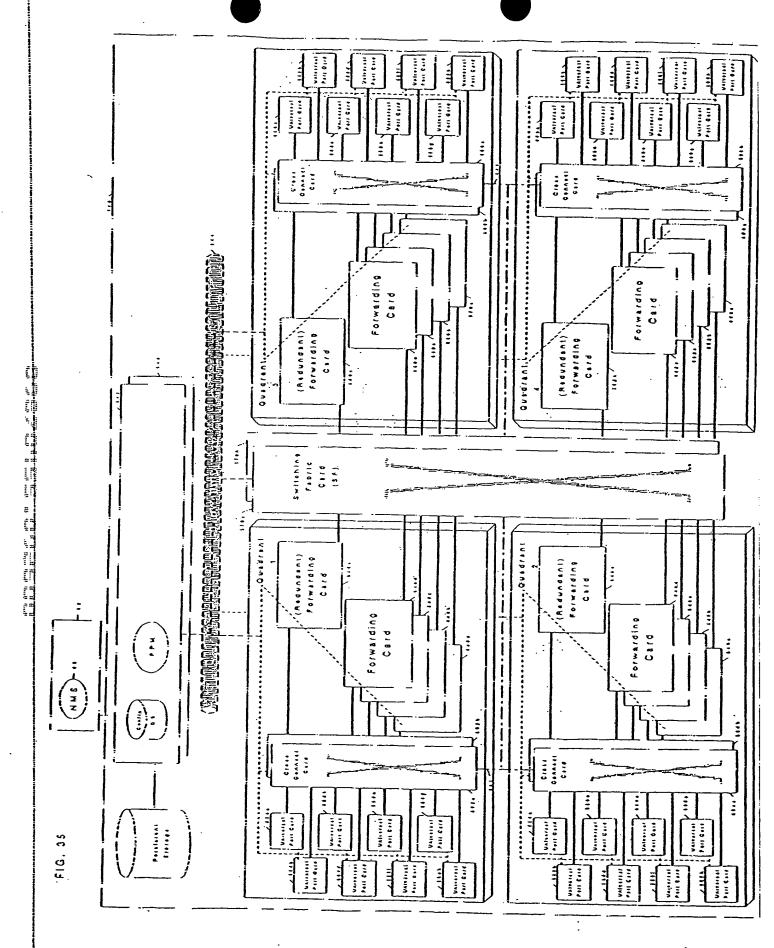
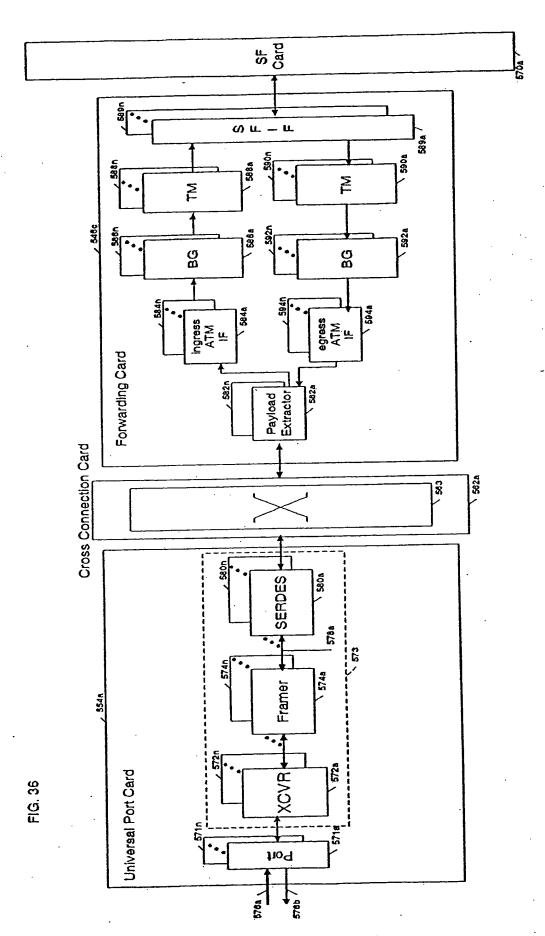


FIG. 34b





BA: Ednibe communications;



Path Table 600

# of Time Slots	3	• • •
Time Slot	4	
OP Pon LID	1231	• • •
Path LID	1666	•••
	602 —	

	·			•		• • •
		610	Path	<u>a</u>	1666	•••
Service End Point Table 76'	5 E	Slot		•••		
	ñ Ö	Slice		· • •		
	FC O	LID		• •		
	σ	#	-	•••		
			SE	#	878	• • •
				<del></del>	604	

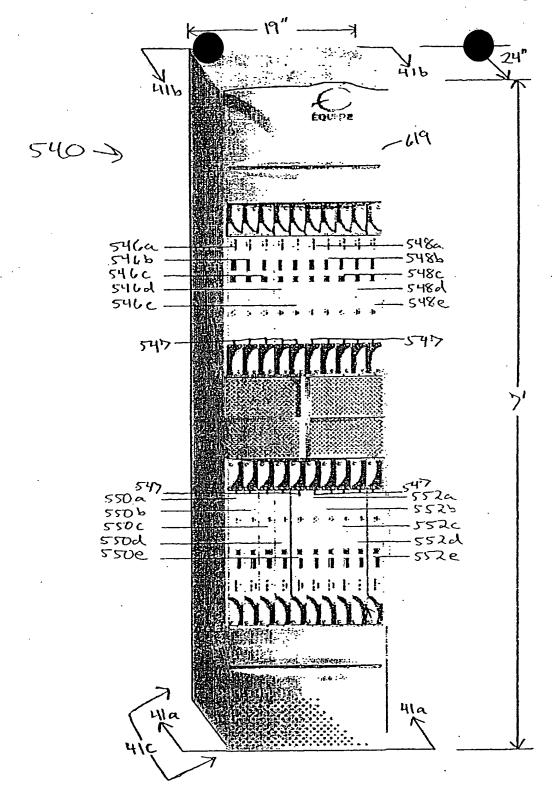
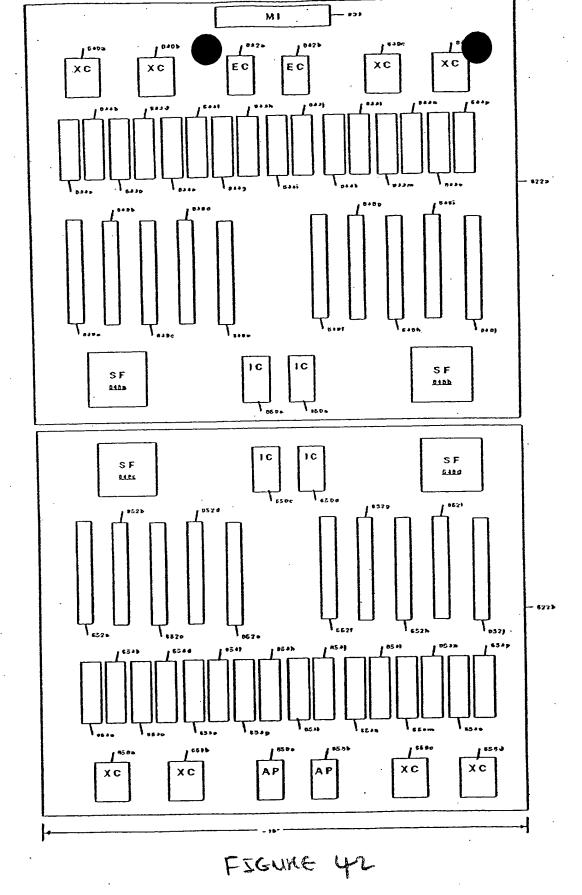
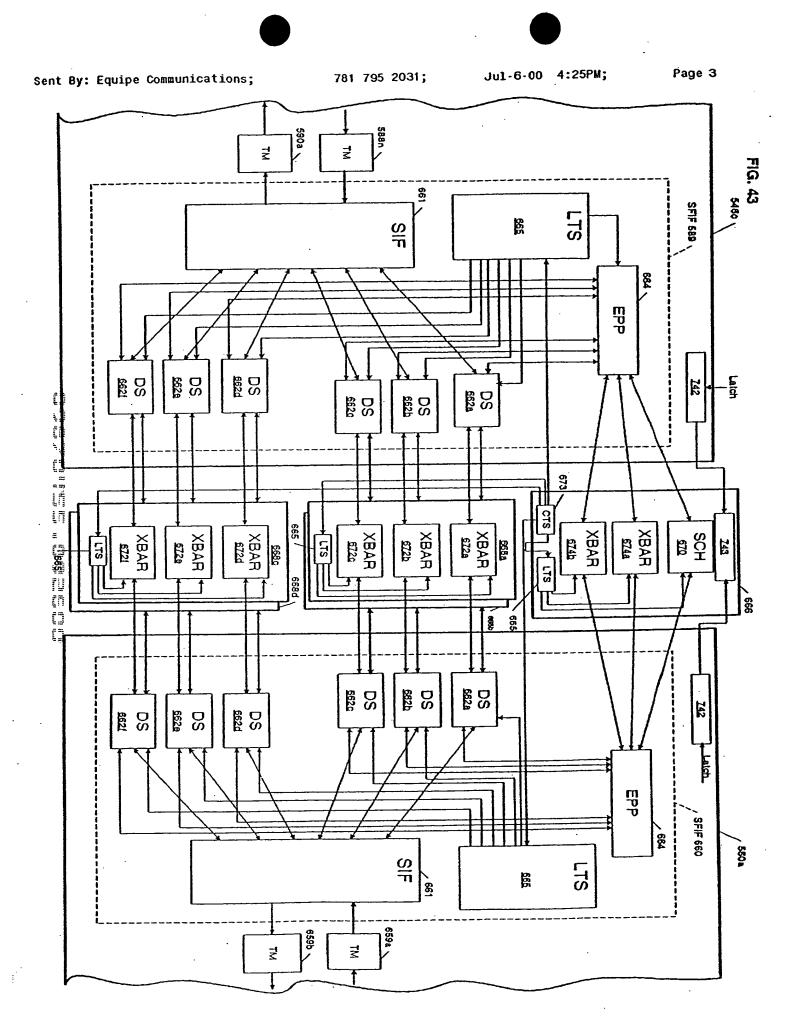
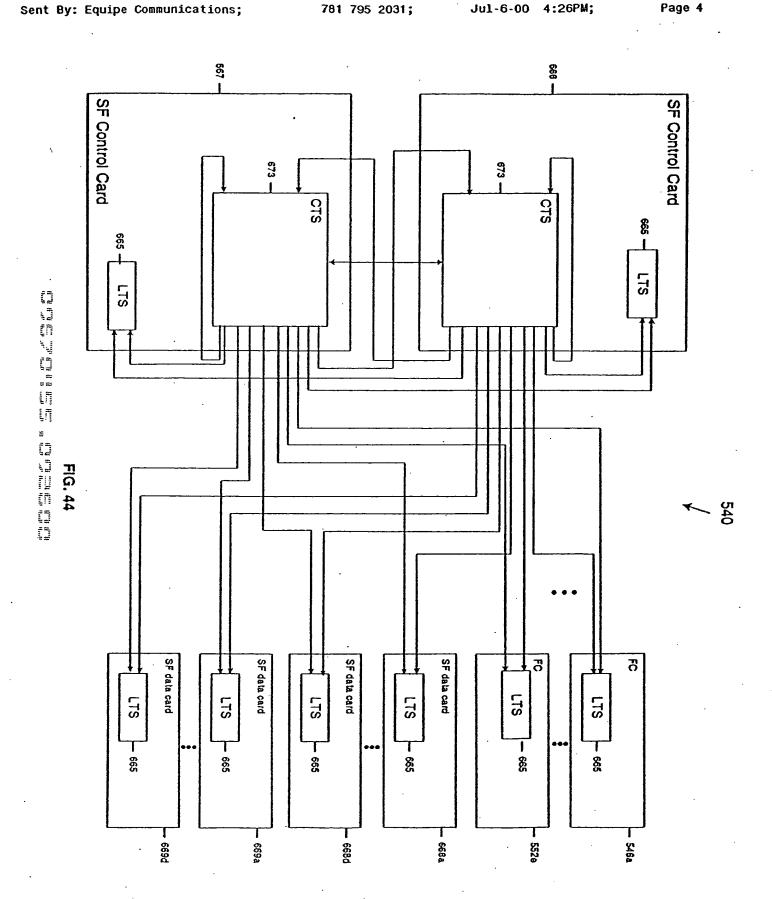


Fig. 40

FIGURE 41C







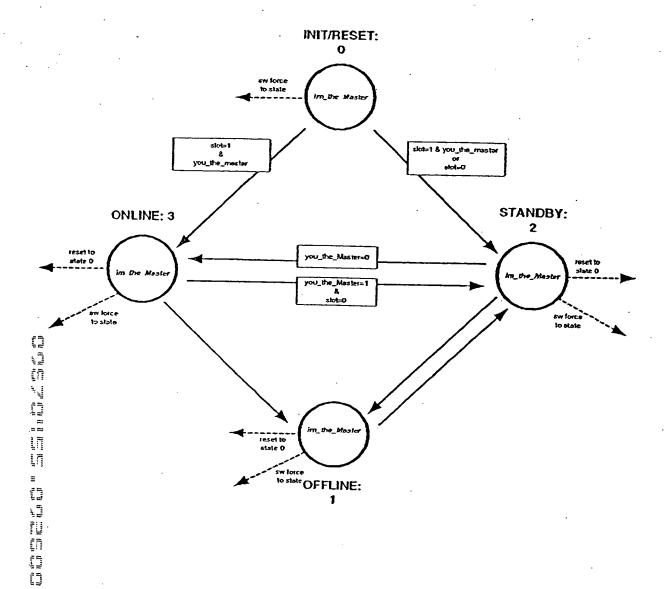
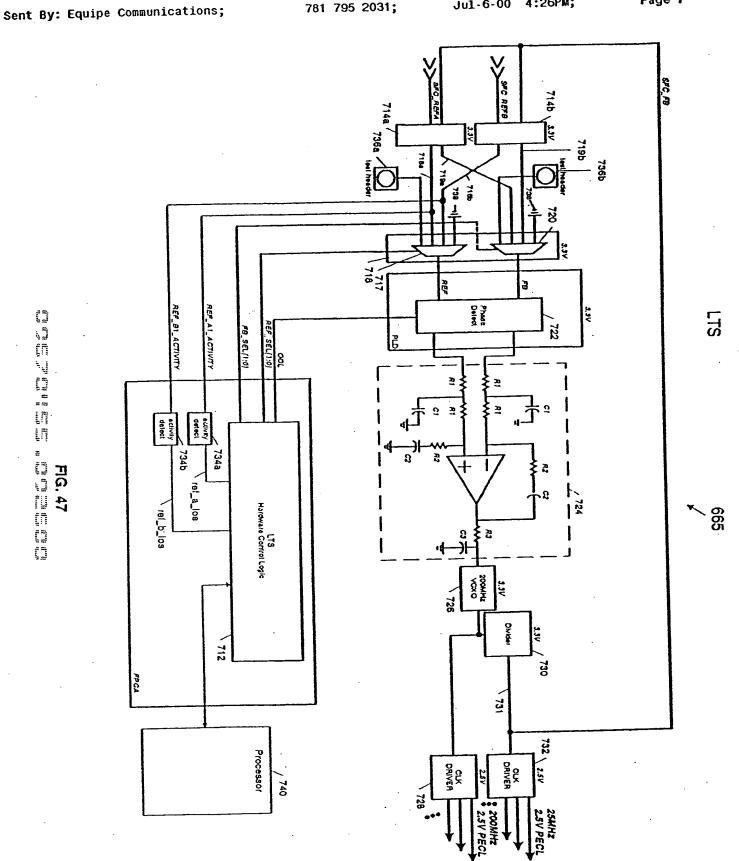
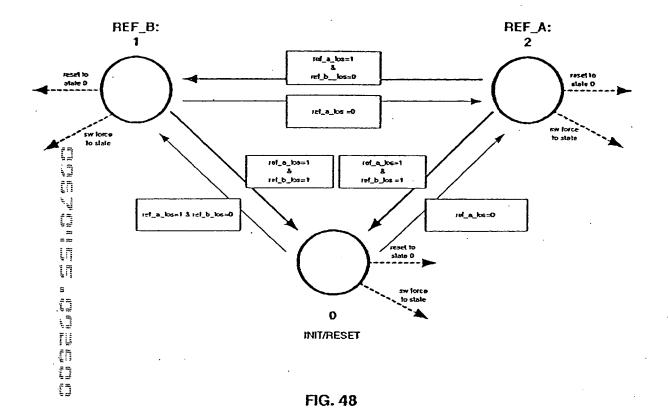
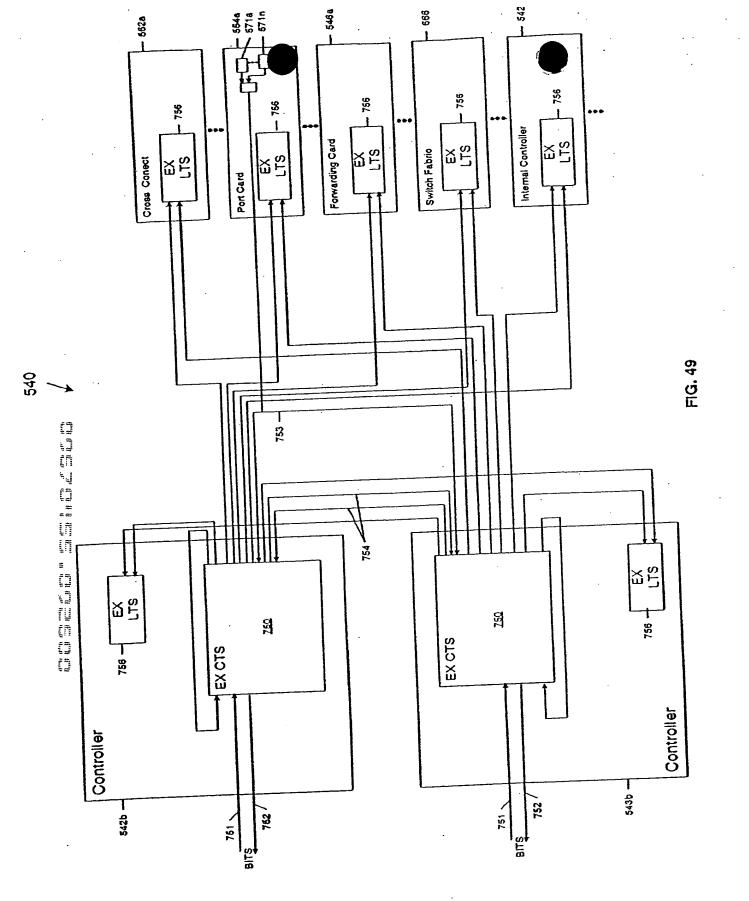


FIG. 46

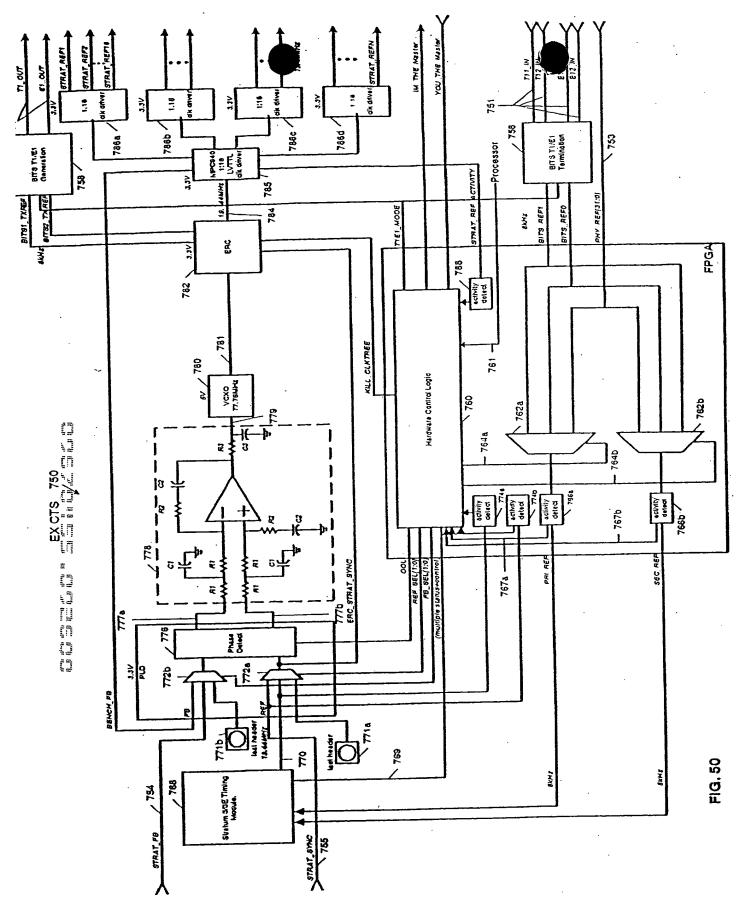






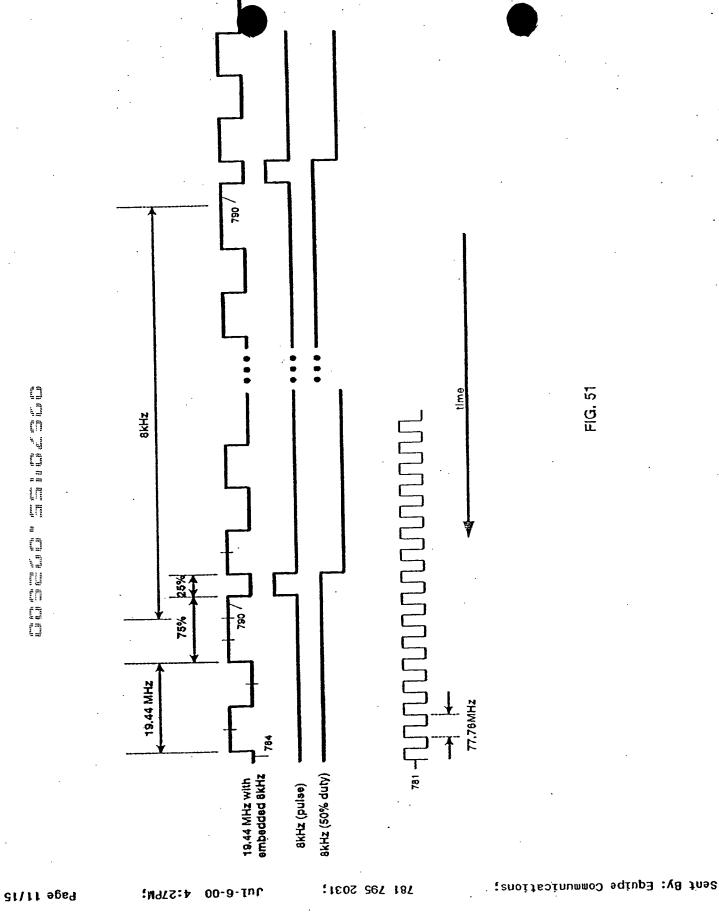
Sent By: Equipe Communications;

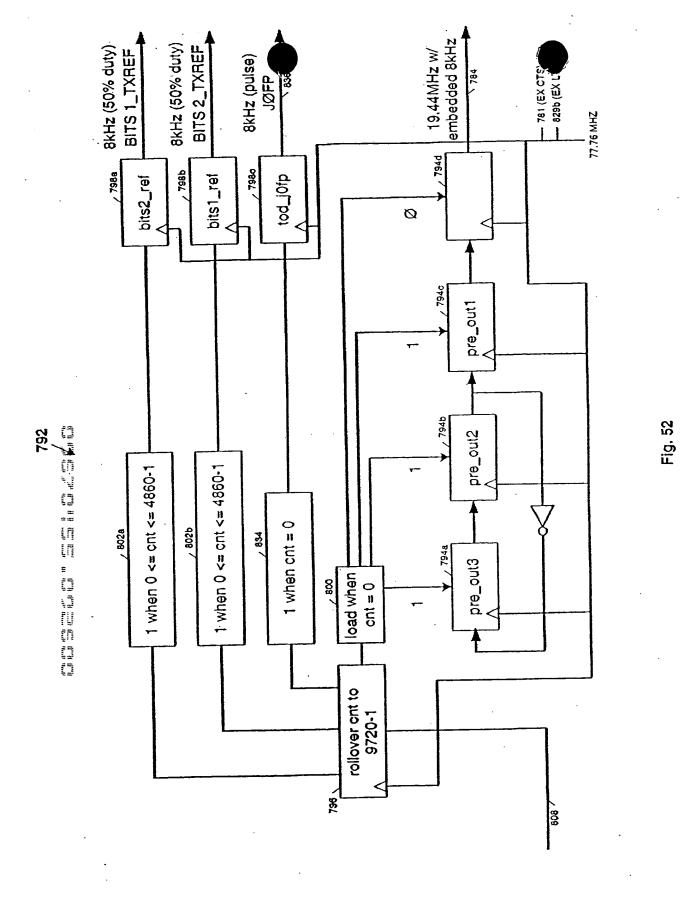
1802 367 187

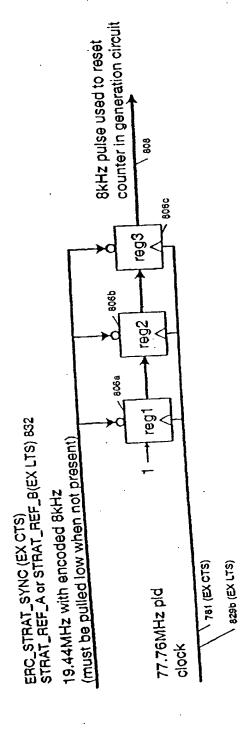


181 795 2031;

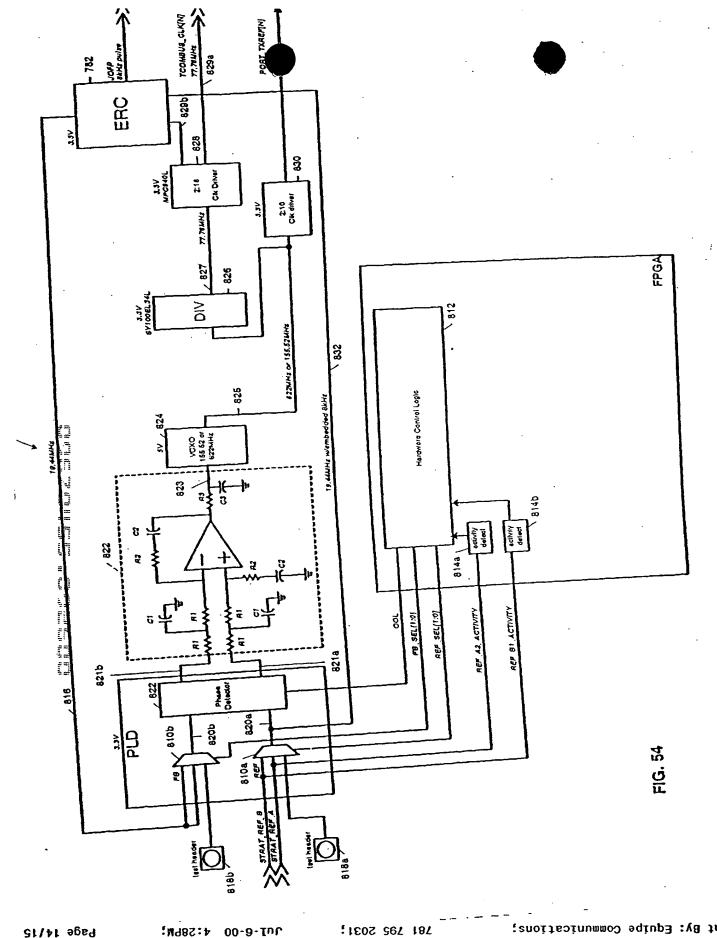
Sent By: Equipe Communications;



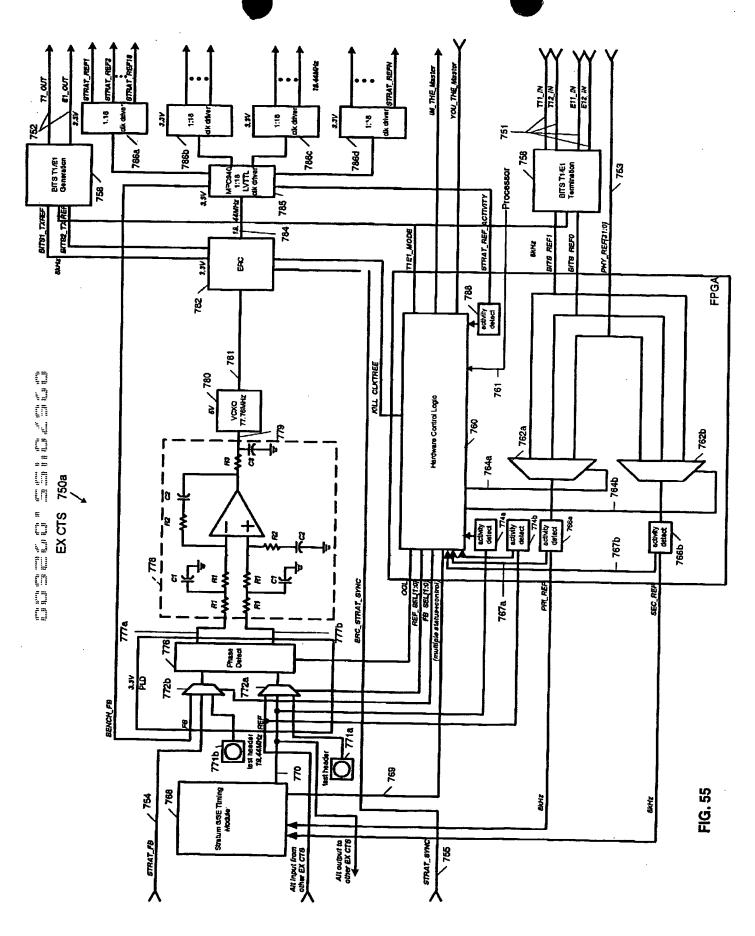


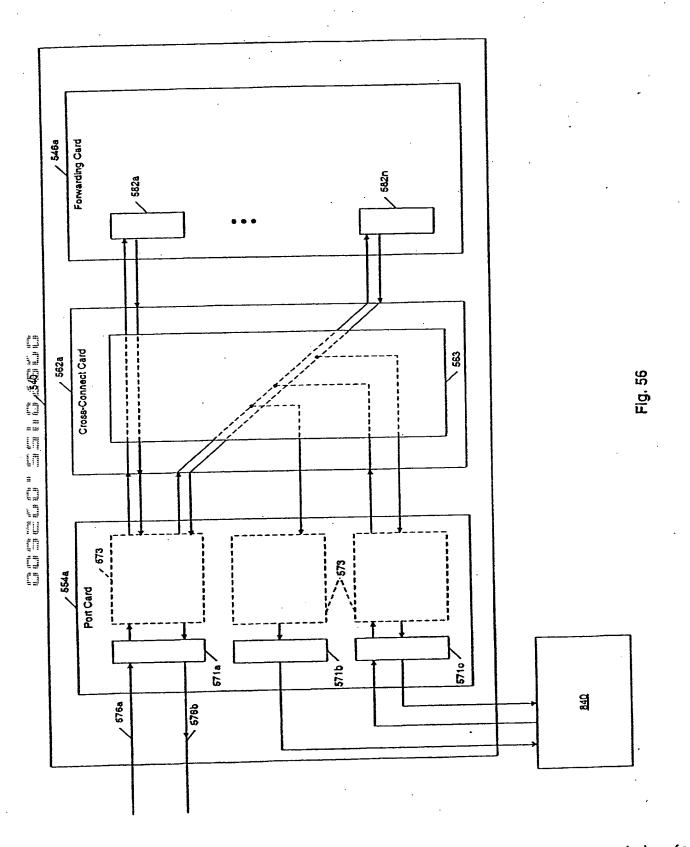


Extractor FIG. 53

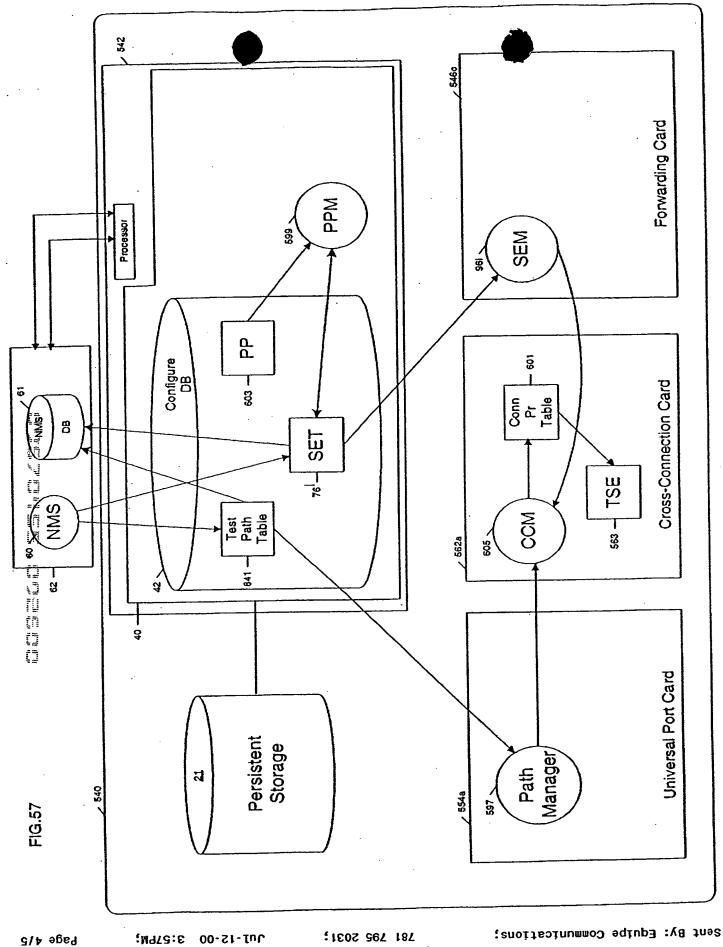


Sent By: Equipe Communications;





Sent By: Equipe Communications;



							•	•	
Test Path Table 841	845	Enable	TOT	No	No	Yes			
	844		Monitor	Ingress	Egress	Ingress			
		# of	ime	33	8	က	•	• •	
		i	Time Slot	4	4	4	•	• (	•
·		dD (	Hor.	1232	1233	1233	•	• (	•
	•	Path	9	1666	1666	1666	•	•	-
				242	843	844	-		

FIG. 58

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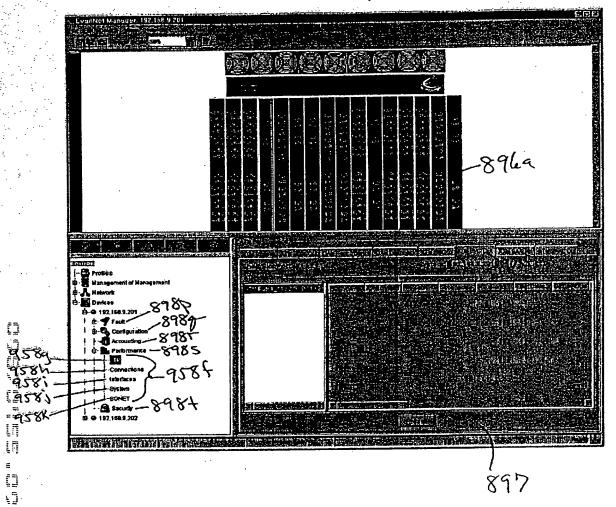


Fig. 7+